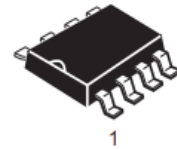


30V P-Channel Power MOSFET

FEATURES :

- Super high dense cell design for extremely low $R_{DS(ON)}$
- High power and current handing capability
- Pb-Free Lead Plating
- RoHS compliant

V_{DS}	-30V
I_D	-9.3A
$R_{DS(ON),MAX}$ @ $V_{GS}=-10V$	18m Ω



SO-8

MAXIMUM RATINGS (at $T_A = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A=25^\circ\text{C}$ $T_A=70^\circ\text{C}$	I_D	-9.3 -7.4	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	-37	A
Maximum Power dissipation	$T_c=25^\circ\text{C}$	P_D	2.5	W
Junction & Storage temperature Range		T_J, T_{STG}	-55~+150	$^\circ\text{C}$

Notes : 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient	$R_{\theta JA}$	50	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS (at $T_A = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = -250\mu A$	BV_{DSS}	-30			V
Zero Gate Voltage Drain Current $V_{DS} = -24V, V_{GS} = 0V$	I_{DSS}			-1	μA
Gate-Source Leakage Current $V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}			± 100	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = -250\mu A$	$V_{GS(th)}$	-1		-3	V
Drain-Source On-State Resistance $V_{GS} = -10V, I_D = -7A$ $V_{GS} = -4.5V, I_D = -5.8A$	$R_{DS(on)}$		14 23	18 30	m Ω
Input capacitance $f=1MHz, V_{DS}=-15V, V_{GS}=0V$	C_{iss}		1690		pF
Output capacitance $f=1MHz, V_{DS}=-15V, V_{GS}=0V$	C_{oss}		285		pF
Reverse transfer capacitance $f=1MHz, V_{DS}=-15V, V_{GS}=0V$	C_{rss}		210		pF
Total Gate Charge $V_{DS}= 15V, I_D= -7A, V_{GS}= -4.5V$	Q_G		19		nC
Gate to Source Charge $V_{DS}= 15V, I_D= -7A, V_{GS}= -4.5V$	Q_{GS}		5		nC
Gate to Drain Charge $V_{DS}= 15V, I_D= -7A, V_{GS}= -4.5V$	Q_{GD}		7		nC
Turn-on delay time $V_{DD}=-10V, V_{GS}= -10V, I_D= -1A, R_{GEN}=6\Omega$	$t_{d(ON)}$		15		ns
Turn-on Rise time $V_{DD}=-10V, V_{GS}= -10V, I_D= -1A, R_{GEN}=6\Omega$	t_r		9		ns
Turn-off delay time $V_{DD}=-10V, V_{GS}= -10V, I_D= -1A, R_{GEN}=6\Omega$	$t_{d(OFF)}$		60		ns
Turn-off Fall time $V_{DD}=-10V, V_{GS}= -10V, I_D= -1A, R_{GEN}=6\Omega$	t_f		20		ns

Drain-Source Diode

Drain-Source Diode Forward Voltage $V_{GS} = 0V, I_S = -2.1A$	V_{SD}			-1.2	V
Drain-Source Diode Forward Current	I_S			-2.1	A

Typical Performance Characteristics

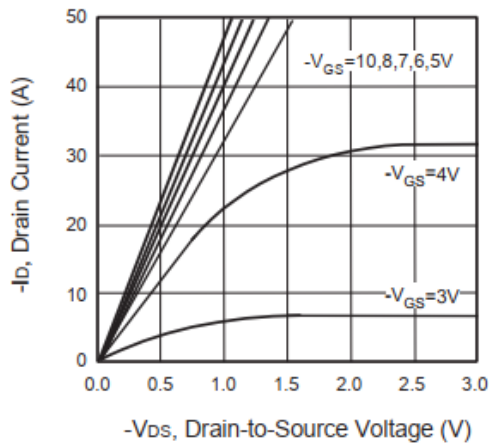


Figure 1. Output Characteristics

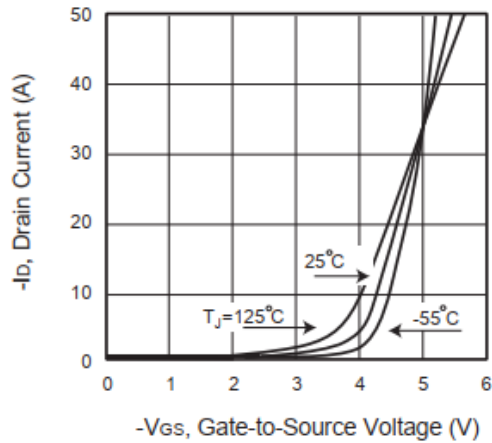


Figure 2. Typical Transfer Characteristics

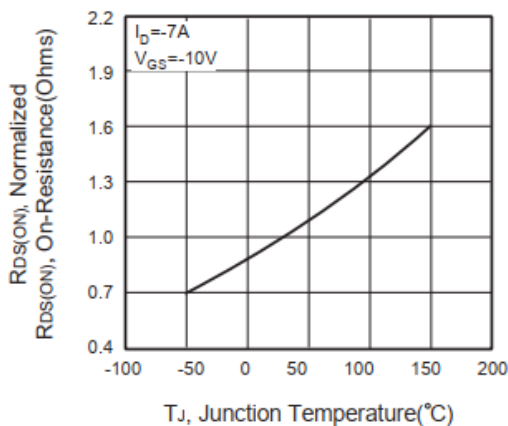


Figure 3. On-Resistance Variation with Temperature

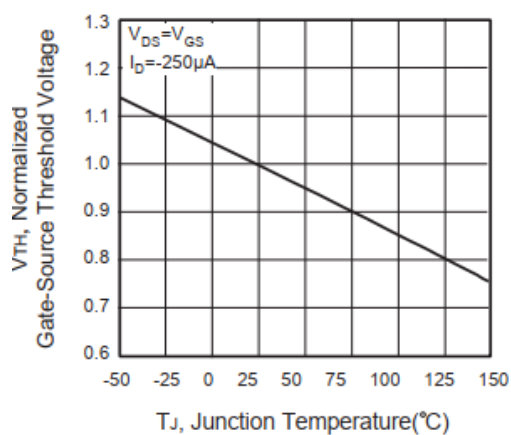


Figure 4. Gate Threshold Variation with Temperature

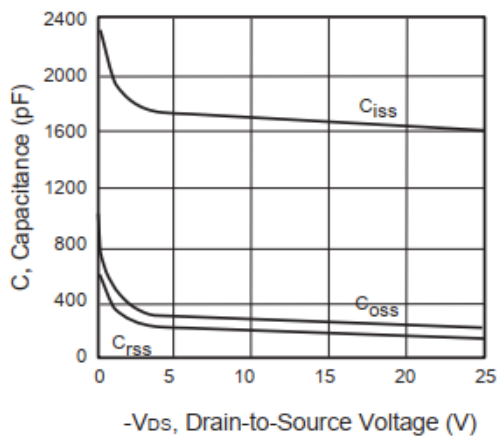


Figure 5. Capacitance characteristics

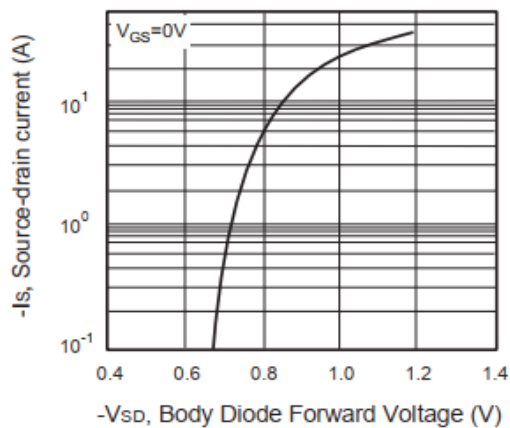


Figure 6. Body Diode Forward Voltage Variation with Source Current

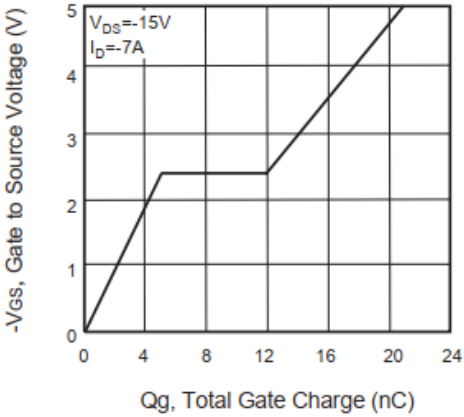


Figure 7. Gate Charge

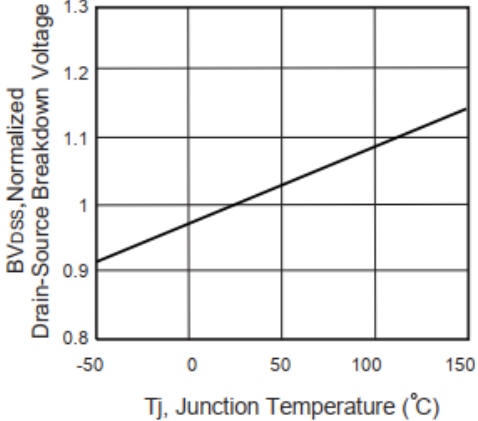


Figure 8. Breakdown Voltage Variation VS Temperature

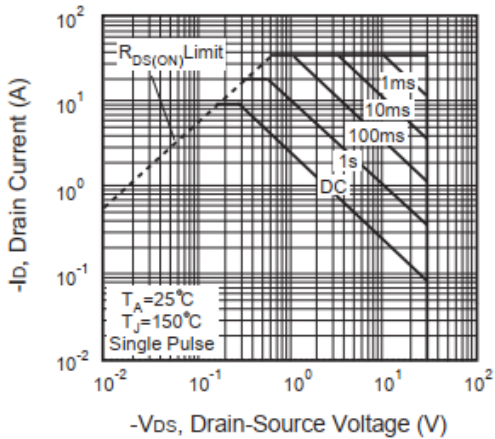


Figure 9. Maximum Safe Operating Area

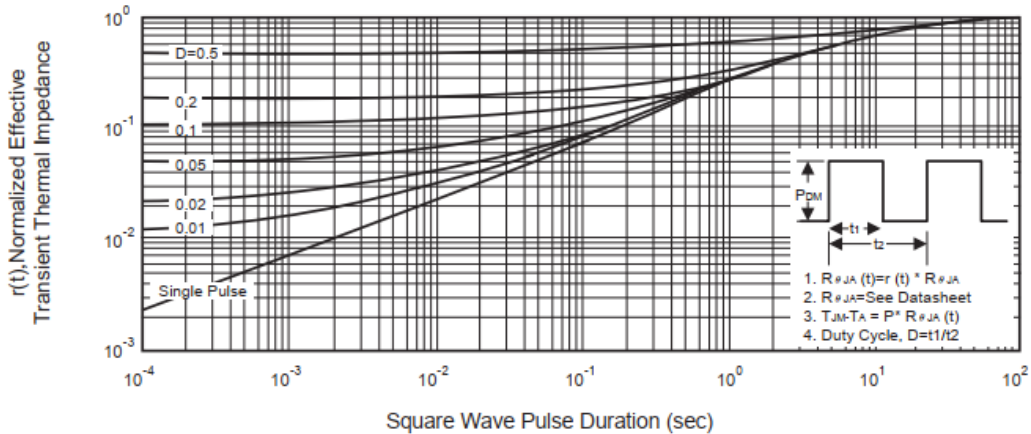
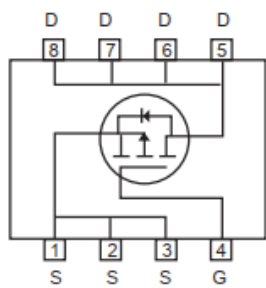


Figure 10. Normalized Thermal Transient Impedance Curve

- Circuit diagram



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