

## 60V N-Channel Power MOSFET

### DESCRIPTION :

- Excellent  $R_{DS(ON)}$
- Low Gate Charge
- 100% UIS Tested, 100%  $\Delta V_{ds}$  Tested
- Pb-Free Lead Plating
- RoHS compliant
- Halogen Free

$V_{DSS}$	200V
$I_D @ V_{GS} = 10V$	96A
$R_{DS(ON\_Typ.) @ V_{GS} = 10V}$	9.3m $\Omega$

### TYPICAL APPLICATIONS :

- Power Management
- Load Switch
- PWM Application



TO-220AB

### MAXIMUM RATINGS (at $T_C = 25^\circ C$ , unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	200	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C = 25^\circ C$ $T_C = 100^\circ C$	$I_D$	96 60	A
Pulsed Drain Current <sup>(1)</sup>		$I_{DM}$	Refer to Fig4	A
Single Pulsed Avalanche Energy <sup>(2)</sup>		$E_{AS}$	1006	mJ
Power dissipation	$T_C = 25^\circ C$ $T_C = 100^\circ C$	$P_D$	253 101	W
Junction & Storage temperature Range		$T_J, T_{STG}$	-55~+150	$^\circ C$

Notes : 1. Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature.

2.  $E_{AS}$  condition: Starting  $T_J = 25^\circ C$ ,  $V_{DD} = 100V$ ,  $V_G = 10V$ ,  $R_G = 25\Omega$ ,  $L = 3mH$ ,  $I_{AS} = 25.9A$ ,  $V_{DD} = 0V$  during time in avalanche.

### THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient Junction to Case	$R_{\theta JA}$ $R_{\theta JC}$	63 0.5	$^\circ C/W$

ELECTRICAL CHARACTERISTICS (at  $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = 250\mu A$	$V_{(BR)DSS}$	200			V
Zero Gate Voltage Drain Current $V_{DS} = 160V, V_{GS} = 0V$	$I_{DSS}$			1	$\mu A$
Gate-Source Leakage Current $V_{GS} = \pm 20V, V_{DS} = 0V$	$I_{GSS}$			$\pm 100$	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(th)}$	2.2	3.1	4.0	V
Drain-Source On-State Resistance $V_{GS} = 10V, I_D = 20A$	$R_{DS(on)}$		9.3	11.7	m $\Omega$
Input capacitance $f=1MHz, V_{DS}=100V, V_{GS}=0V$	$C_{iss}$		4089	5520	pF
Output capacitance $f=1MHz, V_{DS}=100V, V_{GS}=0V$	$C_{oss}$		429	580	pF
Reverse transfer capacitance $f=1MHz, V_{DS}=100V, V_{GS}=0V$	$C_{rss}$		11	14	pF
Gate Resistance $f=1MHz$	$R_g$		4.6		$\Omega$
Total Gate Charge $V_{DS}= 100V, I_D= 20A, V_{GS}= 0$ to 10V	$Q_G$		60	81	nC
Gate to Source Charge $V_{DS}= 100V, I_D= 20A, V_{GS}= 0$ to 10V	$Q_{GS}$		22	29	nC
Gate to Drain Charge $V_{DS}= 100V, I_D= 20A, V_{GS}= 0$ to 10V	$Q_{GD}$		15	20	nC
Turn-on delay time $V_{DD}=100V, V_{GS}= 10V, I_D= 20A, R_{GEN}=2.7\Omega$	$t_{d(ON)}$		14		ns
Rise time $V_{DD}=100V, V_{GS}= 10V, I_D= 20A, R_{GEN}=2.7\Omega$	$t_r$		37		ns
Turn-off delay time $V_{DD}=100V, V_{GS}= 10V, I_D= 20A, R_{GEN}=2.7\Omega$	$t_{d(OFF)}$		67		ns
Fall time $V_{DD}=100V, V_{GS}= 10V, I_D= 20A, R_{GEN}=2.7\Omega$	$t_f$		30		ns

## Body Diode

ELECTRICAL CHARACTERISTICS (at  $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Diode Forward Voltage $V_{GS} = 0V, I_S = 20A$	$V_{SD}$			1.2	V
Maximum Continuous Body Diode Forward Current	$I_S$			96	A
Maximum Pulsed Body Diode Forward Current	$I_{SM}$			383	A
Revers Recovery Time $I_F = 20A, di/dt = 100A/\mu s$	$T_{rr}$		196	265	ns
Revers Recovery Charge $I_F = 20A, di/dt = 100A/\mu s$	$Q_{rr}$		702		nC

Typical Performance Characteristics

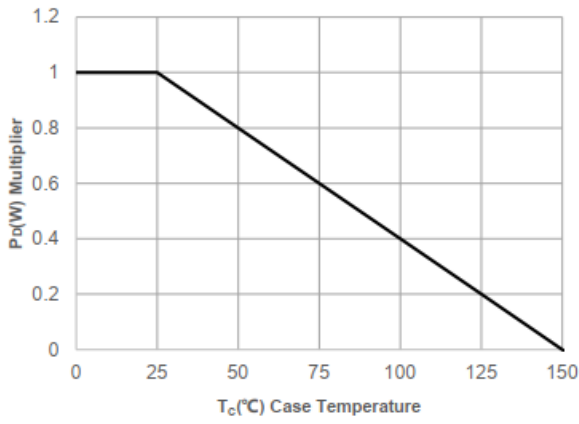


Figure 1. Power De-rating

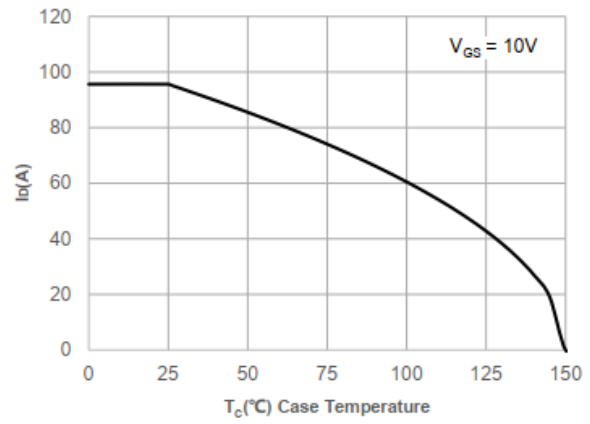


Figure 2. Current De-rating

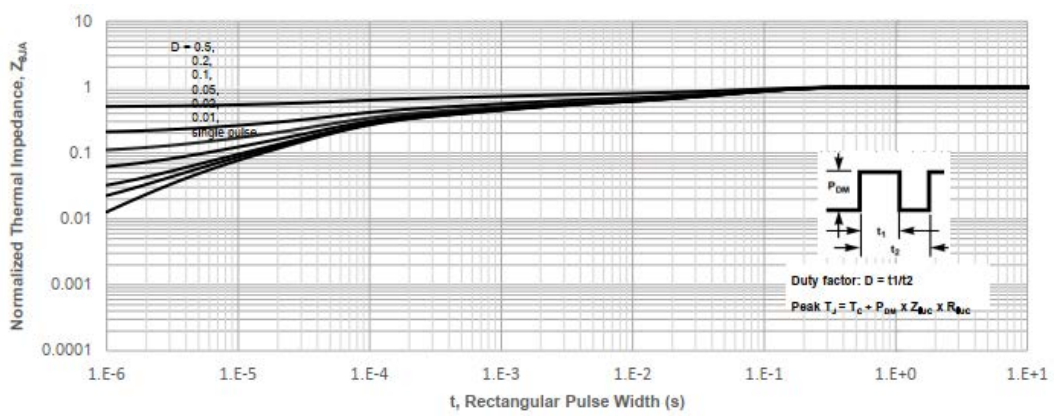


Figure 3. Normalized Maximum Transient Thermal Impedance

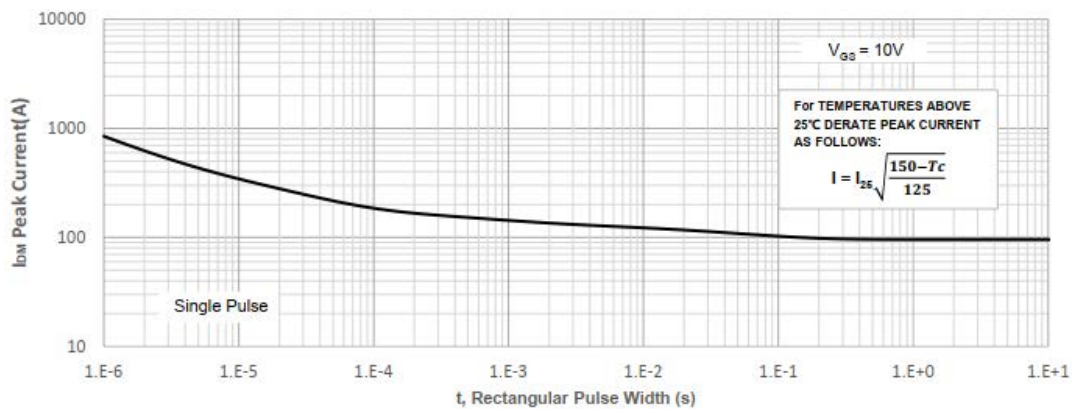


Figure 4. Peak Current Capacity

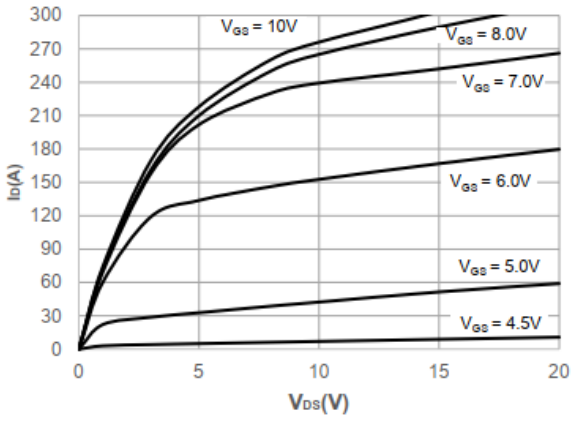


Figure 5. Output Characteristics

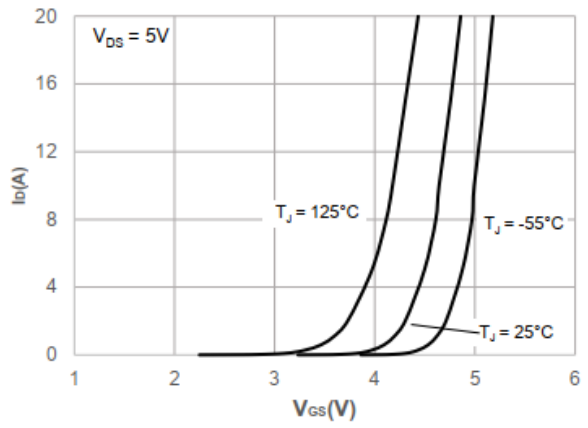


Figure 6. Typical Transfer Characteristics

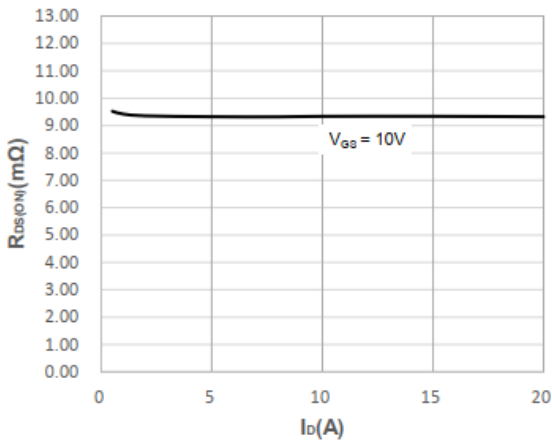


Figure 7. On-resistance vs. Drain Current

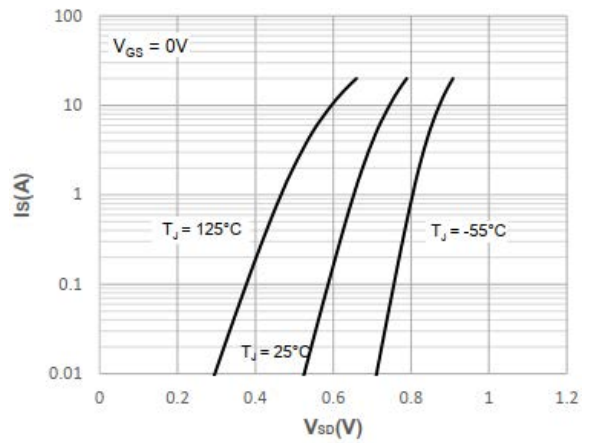


Figure 8. Body Diode Characteristics

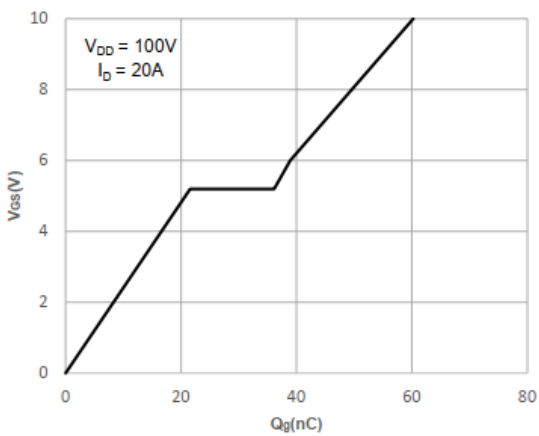


Figure 9. Gate Charge Characteristics

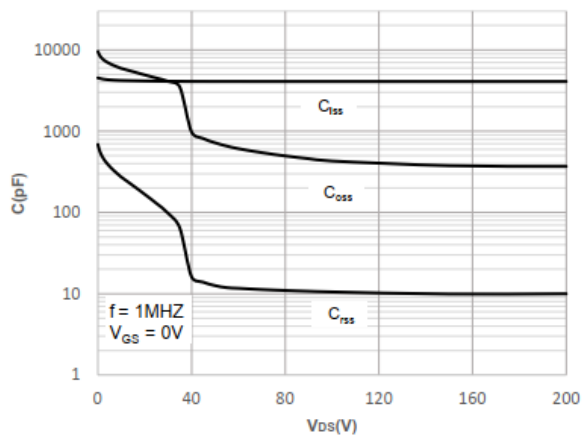


Figure 10. Capacitance Characteristics

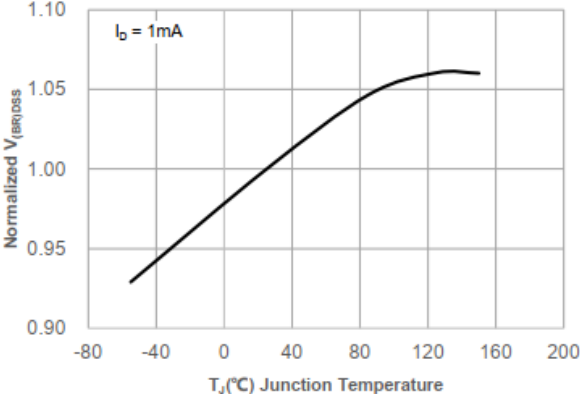


Figure 11. Normalized Breakdown voltage vs. Junction Temperature

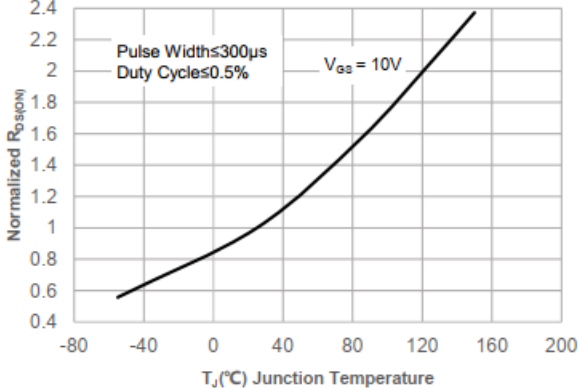


Figure 12. Normalized on Resistance vs. Junction Temperature

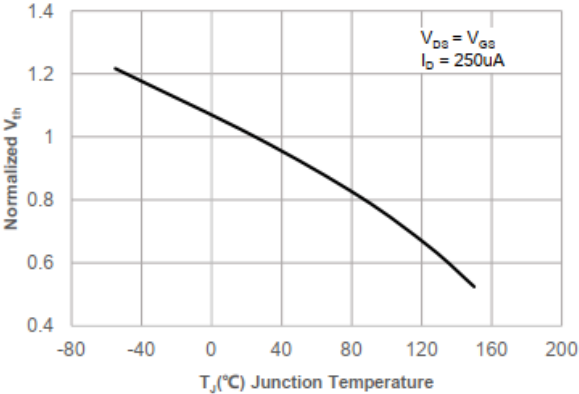


Figure 13. Normalized Threshold Voltage vs. Junction Temperature

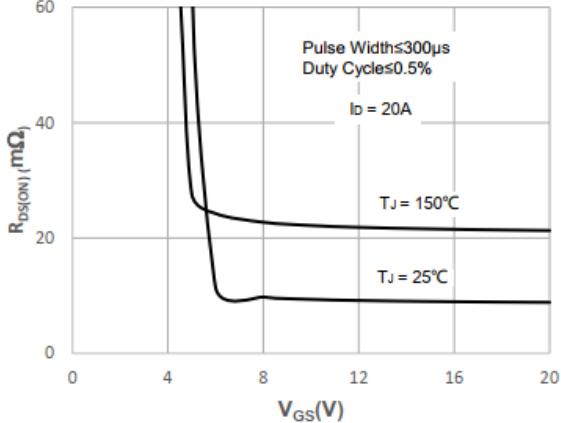


Figure 14. R<sub>DS(ON)</sub> vs. V<sub>GS</sub>

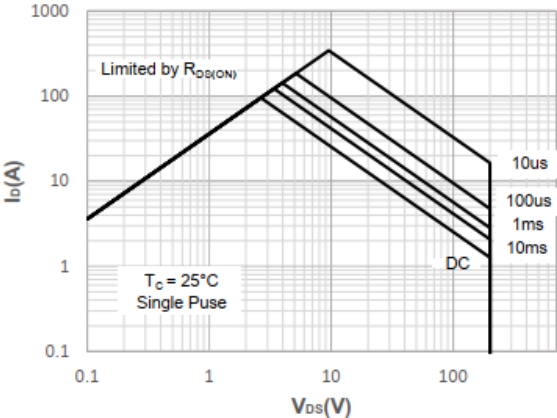
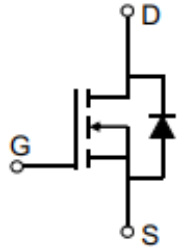
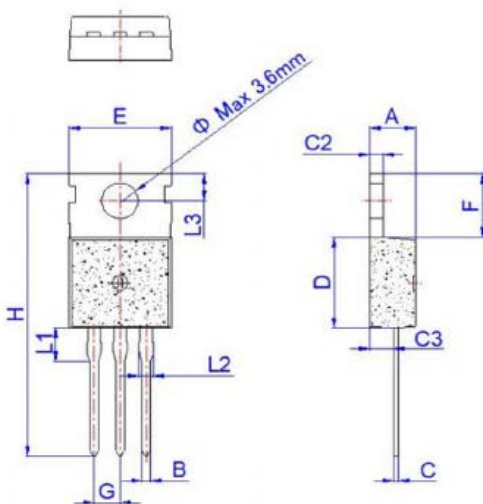


Figure 15. Maximum Safe Operating Area

- Circuit diagram



- Package outlines : Dimensions in (mm)



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.70		0.90	0.028		0.035
C	0.45		0.60	0.018		0.024
C2	1.23		1.32	0.048		0.052
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.3	0.390		0.406
F	6.30		6.90	0.248		0.272
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.39			0.133	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
Φ		3.6			0.142	

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