100V N-Channel Power MOSFET

DESCRIPTION：

·Low On-Resistance

·Excellent FoM (figure of merit)

·100% ΔVDS & UIS & Rg Tested

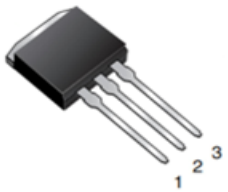
V_{DSS}	100V
I_D @V_{GS}=10V	91A
$R_{DS(ON)_Typ.}$ @V_{GS}=10V	5.2mΩ

TYPICAL APPLICATIONS：

·Green Molding Compound

·Moisture Sensitivity: Level 1 per J-STD-020

·UL Flammability Classification Rating 94V-0



TO-262

MAXIMUM RATINGS (at T_C = 25 °C, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain - Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current(1)	Tc=25℃ Tc=100℃	I_D	91 58	A
Pulsed Drain Current (2)		I_{DM}	366	A
Single Pulsed Avalanche Energy (3)		E_{AS}	384	mJ
Single Pulse Avalanche Current	L= 10.0mH	I_{AS}	49	A
Power dissipation	Tc=25℃ Tc=100℃	P_D	96 38	W
Junction & Storage temperature Range		T_J, T_{STG}	-55~+150	℃

Notes： 1. This current is chip limited, which is calculated based on Rthjc..
2. This current is calculated on single pulse with 10μs Pulse
3. Defined by design, not subject to production test, EAS condition: TJ=25°C, VDD=50V, VGS=10V, L=1.0mH.

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient Junction to Case	$R_{\theta JA}$ $R_{\theta JC}$	44 1.3	℃/W

ELECTRICAL CHARATERISTICS (at T_J = 25 °C, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage VGS = 0V, ID = 250uA	$V_{(BR)DSS}$	100			V
Zero Gate Voltage Drain Current VDS = 100 V, VGS = 0 V ,Tj=25℃ VDS = 100 V, VGS = 0 V ,Tj=125℃	I_{DSS}			1.0 100	uA
Gate-Source Leakage Current VGS = ±20V, VDS = 0V	I_{GSS}			±100	nA
Gate-Source threshold voltage VDS = VGS, ID = 250uA	$V_{GS(th)}$	2.0	3.0	4.0	V
Drain-Source On-State Resistance VGS = 10V, ID = 20A	$R_{DS(on)}$		5.2	6.3	mΩ
Forward Transconductance VDS = 5.0V, ID = 20A	gfs		24		S
Diode Forward Voltage VGS = 0V, I_{SD} =2.0A	V_{SD}		0.7	1.2	V
Gate Resistance f=1MHz	R_g		1.1		Ω
Input capacitance f=1MHz, VDS=50 V, VGS=0 V	C_{iss}		2472		pF
Output capacitance f=1MHz, VDS=50 V, VGS=0 V	C_{oss}		978		pF
Reverse transfer capacitance f=1MHz, VDS=50 V, VGS=0 V	C_{rss}		15		pF
Turn-on delay time VDS=50 V, ID=20A, VGS=10V, R_{GEN}=3.0Ω	td (ON)		6.5		ns
Rise time VDS=50 V, ID=20A, VGS=10V, R_{GEN}=3.0Ω	tr		9.5		ns
Turn-off delay time VDS=50 V, ID=20A, VGS=10V, R_{GEN}=3.0Ω	td (OFF)		34		ns
Fall time VDS=50 V, ID=20A, VGS=10V, R_{GEN}=3.0Ω	tf		23		ns
Total Gate Charge VDS= 50V, ID= 20A, VGS= 10V VDS= 50V, ID= 20A, VGS= 6.0V	Q_G		38 21		nC
Gate to Source Charge VDS= 50V, ID= 20A, VGS= 10V	Q_{GS}		6.4		nC
Gate to Drain Charge VDS= 50V, ID= 20A, VGS= 10V	Q_{GD}		8.5		nC
Gate Plateau Voltage VDS= 50V, ID= 20A, VGS= 10V	$V_{plateau}$		4.7		nC
Revers Recovery Time I_F =20A, di_F/dt = 100A/us ,Tj=25℃	Trr		58		ns
Revers Recovery Charge I_F =20A, di_F/dt = 100A/us ,Tj=25℃	Qrr		115		uC
Peak Reverse Recovery Current Tc=25℃	IS			91	A

Typical Performance Characteristics

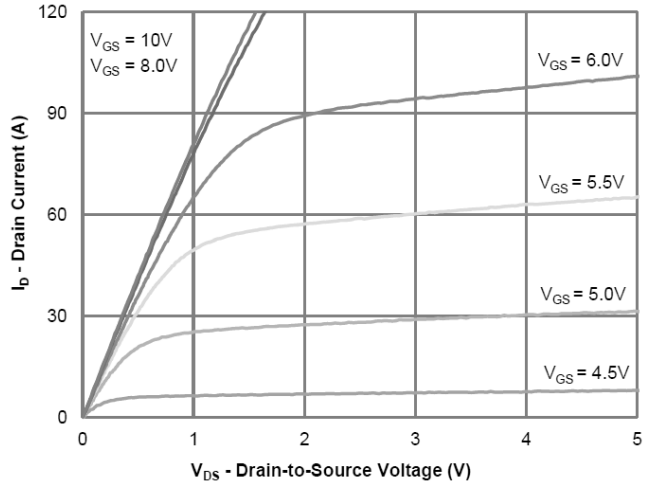


Figure 1. Output Characteristics

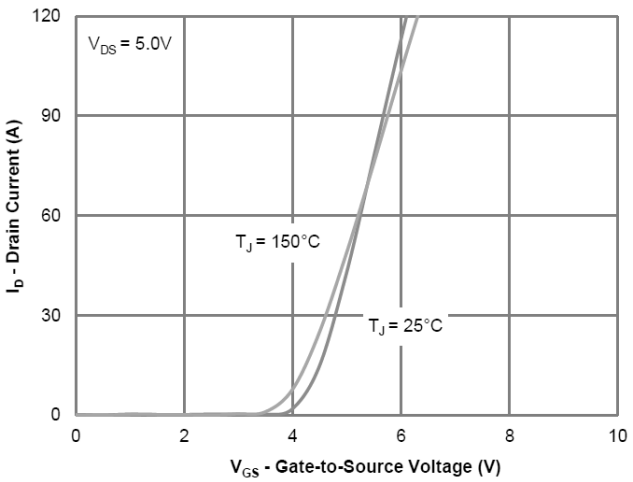


Figure 2. Transfer Characteristics

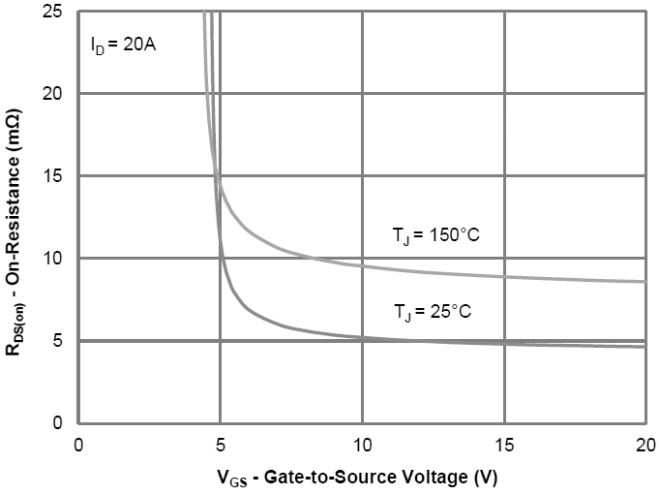


Figure 3. On-Resistance vs. Gate-Source Voltage

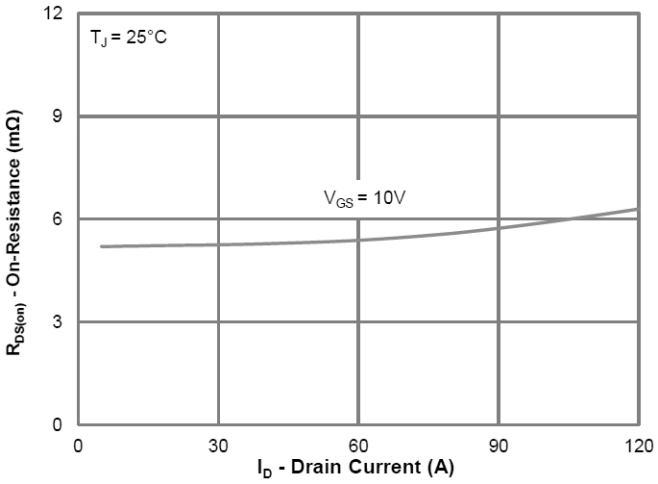


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

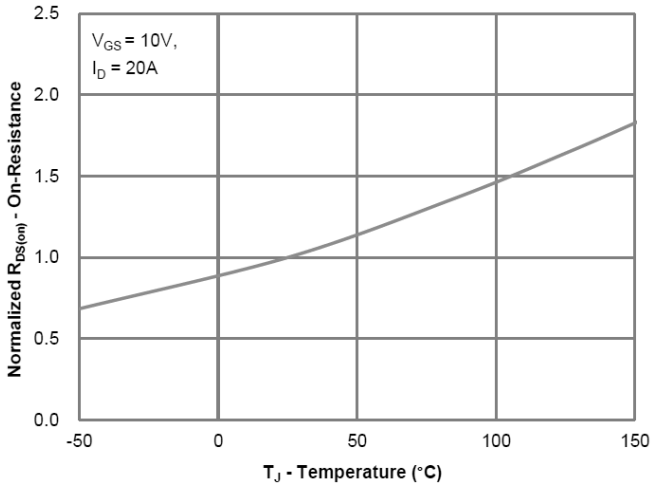


Figure 5. On-Resistance vs. Junction Temperature

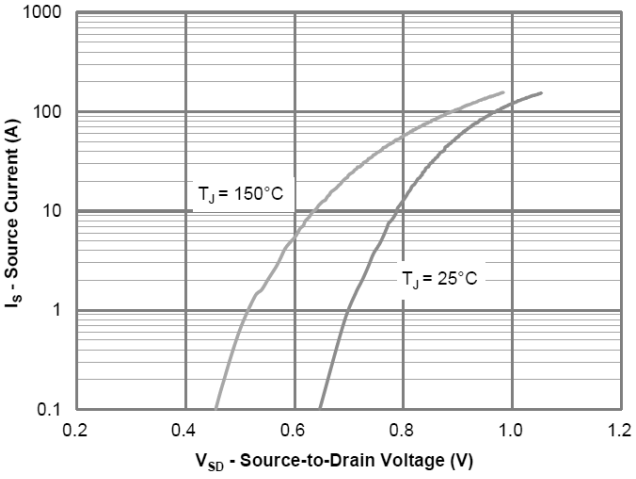


Figure 6. Source-Drain Diode Forward Voltage

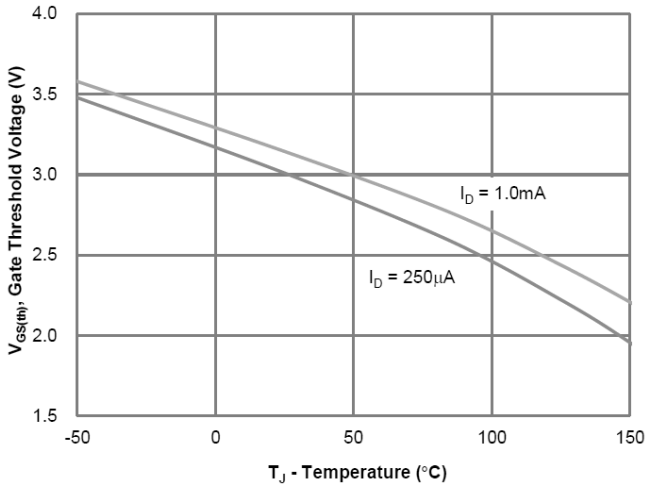


Figure 7. Gate Threshold Variation vs. Junction Temperature

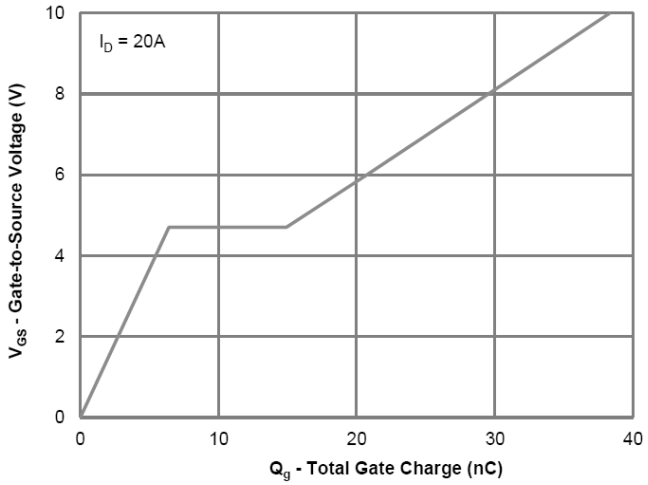


Figure8. Gate Charge Characteristics

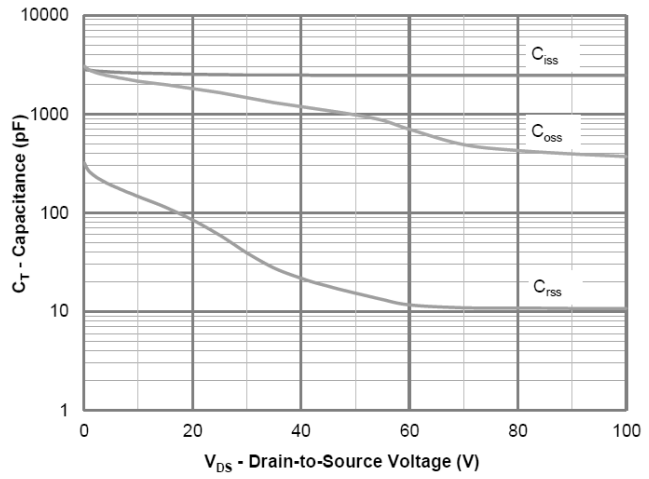


Figure 9. Capacitance Characteristics

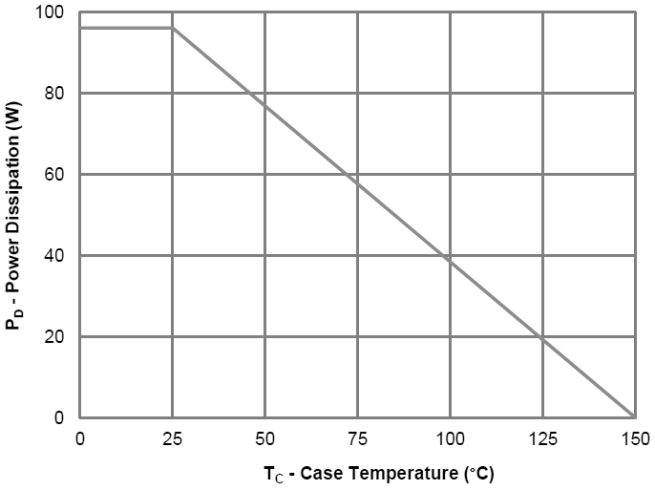


Figure 10. Power Derating

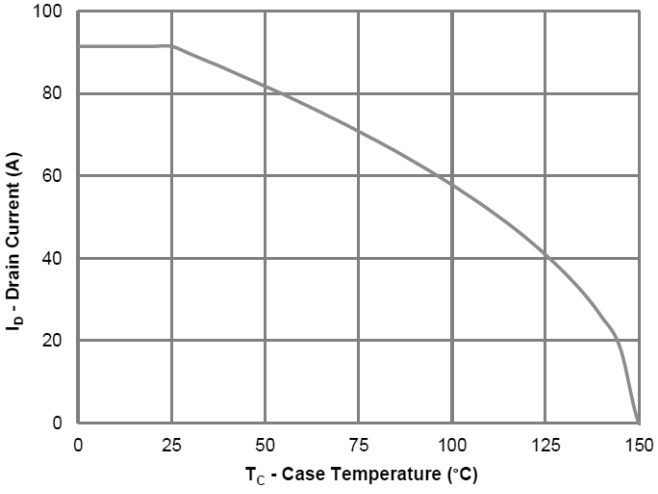


Figure 11. Current Derating

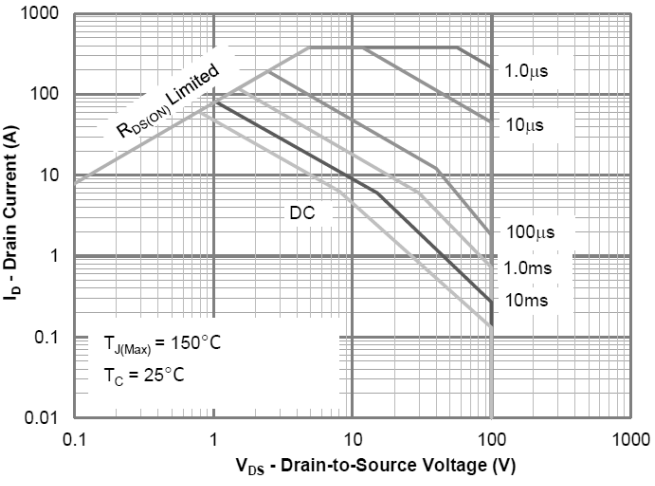


Figure 12. Safe Operating Area

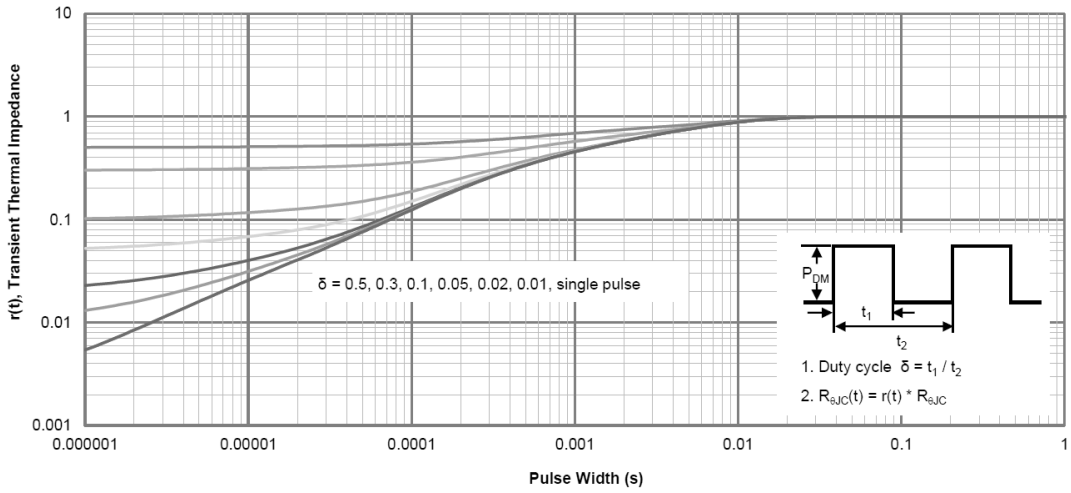
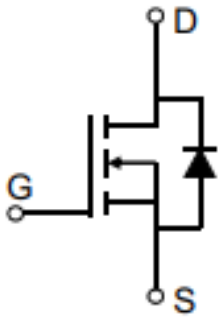
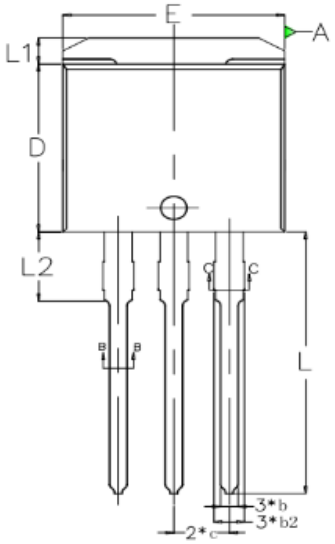


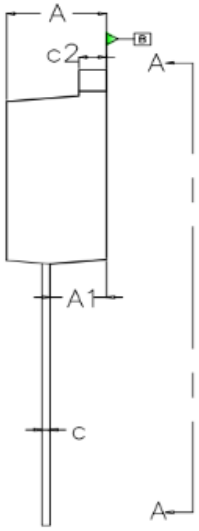
Figure 13. Normalized Maximum Transient Thermal Impedance

·Circuit diagram



·Package outlines : Dimensions in (mm)





DIM	MILLIMETERS	
	MIN	MAX
A	4.44	4.70
A1	2.54	2.79
b	0.70	0.90
b2	1.20	1.45
C2	1.14	1.40
D	8.51	8.89
E	10.00	10.40
e	2.54BSC	
L	13.46	14.00
L1	1.00	1.65
L2	3.56	3.71

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