

650V N-Channel Power MOSFET

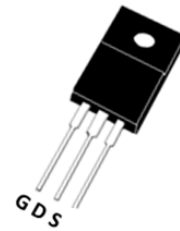
DESCRIPTION :

- Excellent $R_{DS(ON)}$
- Low Gate Charge
- 100% UIS Tested, 100% ΔV_{ds} Tested
- Pb-Free Lead Plating
- RoHS compliant
- Halogen Free

| | |
|--------------------------------|---------------|
| V_{DSS} | 650V |
| $I_D @ V_{GS}=10V$ | 7.4A |
| $R_{DS(ON)_Typ.} @ V_{GS}=10V$ | 311m Ω |

TYPICAL APPLICATIONS :

- SMPS with PFC
- Flyback and LLC topologies
- Silver ATX, adapter, TV, lighting, Telecom



ITO-220AB

MAXIMUM RATINGS (at $T_C = 25^\circ C$, unless otherwise specified)

| Characteristic | Condition | Symbol | Value | Unit |
|---|---------------------------------------|----------------|---------------|------------|
| Drain-Source Voltage | | V_{DS} | 650 | V |
| Gate-Source Voltage | | V_{GS} | ± 30 | V |
| Continuous Drain Current | $T_C=25^\circ C$ $T_C=100^\circ C$ | I_D | 7.4 4.7 | A |
| Pulsed Drain Current ⁽¹⁾ | | I_{DM} | Refer to Fig4 | A |
| Single Pulsed Avalanche Energy ⁽²⁾ | | E_{AS} | 45 | mJ |
| Power dissipation | $T_C=25^\circ C$ $T_C=100^\circ C$ | P_D | 51 20 | W |
| Junction & Storage temperature Range | | T_J, T_{STG} | -55~+150 | $^\circ C$ |

- Notes : 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. EAS condition: Starting $T_J=25^\circ C$, $V_{DD}=75V$, $V_G=10V$, $R_G=25\Omega$, $L=10mH$, $I_{AS}=3A$, $V_{DD}=0V$ during time in avalanche.

THERMAL CHARACTERISTICS

| Characteristic | Condition | Symbol | Value | Unit |
|---------------------|---|------------------------------------|-----------|--------------|
| Thermal resistance, | Junction to Ambient Junction to Case | $R_{\theta JA}$ $R_{\theta JC}$ | 58 2.5 | $^\circ C/W$ |

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

| Characteristic | Symbol | Min. | Typ. | Max. | Unit |
|---|---------------|------|------|-----------|------------|
| Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = 250\mu A$ | $V_{(BR)DSS}$ | 650 | | | V |
| Zero Gate Voltage Drain Current $V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$ | I_{DSS} | | | 1 | μA |
| Gate-Source Leakage Current $V_{GS} = \pm 30V, V_{DS} = 0V$ | I_{GSS} | | | ± 100 | nA |
| Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = 250\mu A$ | $V_{GS(th)}$ | 2.4 | 3.4 | 4.4 | V |
| Drain-Source On-State Resistance $V_{GS} = 10V, I_D = 5.5A$ | $R_{DS(on)}$ | | 311 | 360 | m Ω |
| Gate Resistance $f=1\text{MHz}$ | R_g | | 17 | | Ω |
| Input capacitance $f=1\text{MHz}, V_{DS}=325\text{ V}, V_{GS}=0\text{ V}$ | C_{iss} | | 782 | 1056 | pF |
| Output capacitance $f=1\text{MHz}, V_{DS}=325\text{ V}, V_{GS}=0\text{ V}$ | C_{oss} | | 26 | 35 | pF |
| Reverse transfer capacitance $f=1\text{MHz}, V_{DS}=325\text{ V}, V_{GS}=0\text{ V}$ | C_{rss} | | 5.6 | | pF |
| Total Gate Charge $V_{DS}= 325V, I_D= 5.5A, V_{GS}= 0\text{ to }10V$ | Q_G | | 19 | 26 | nC |
| Gate to Source Charge $V_{DS}= 325V, I_D= 5.5A, V_{GS}= 0\text{ to }10V$ | Q_{GS} | | 4.8 | | nC |
| Gate to Drain Charge $V_{DS}= 325V, I_D= 5.5A, V_{GS}= 0\text{ to }10V$ | Q_{GD} | | 7 | | nC |
| Turn-on delay time $V_{DD}=330\text{ V}, V_{GS}= 10V, I_D= 5.5A, R_{GEN}=24\Omega$ | $t_{d(ON)}$ | | 24 | | ns |
| Rise time $V_{DD}=330\text{ V}, V_{GS}= 10V, I_D= 5.5A, R_{GEN}=24\Omega$ | t_r | | 29 | | ns |
| Turn-off delay time $V_{DD}=330\text{ V}, V_{GS}= 10V, I_D= 5.5A, R_{GEN}=24\Omega$ | $t_{d(OFF)}$ | | 99 | | ns |
| Fall time $V_{DD}=330\text{ V}, V_{GS}= 10V, I_D= 5.5A, R_{GEN}=24\Omega$ | t_f | | 31 | | ns |

Body Diode

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

| Characteristic | Symbol | Min. | Typ. | Max. | Unit |
|--|----------|------|------|------|------|
| Diode Forward Voltage $V_{GS} = 0V, I_S = 5.5A$ | V_{SD} | | | 1.2 | V |
| Maximum Continuous Body Diode Forward Current | I_S | | | 7 | A |
| Maximum Pulsed Body Diode Forward Current | I_{SM} | | | 30 | A |
| Revers Recovery Time $I_F = 5.5A, di/dt = 100A/\mu s$ | T_{rr} | | 256 | 346 | ns |
| Revers Recovery Charge $I_F = 5.5A, di/dt = 100A/\mu s$ | Q_{rr} | | 2.5 | | nC |

Typical Performance Characteristics

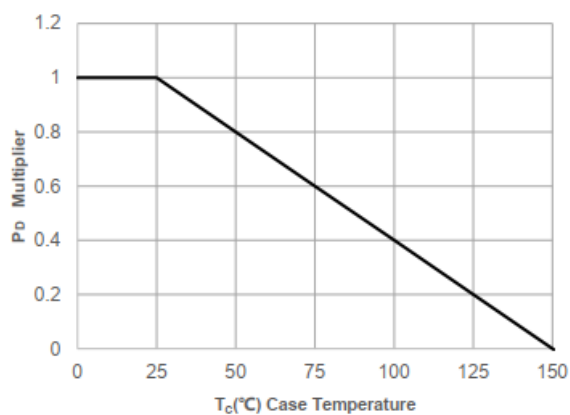


Figure 1. Power De-rating

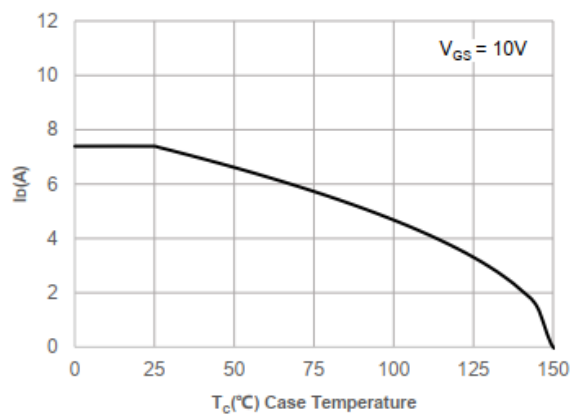


Figure 2. Current De-rating

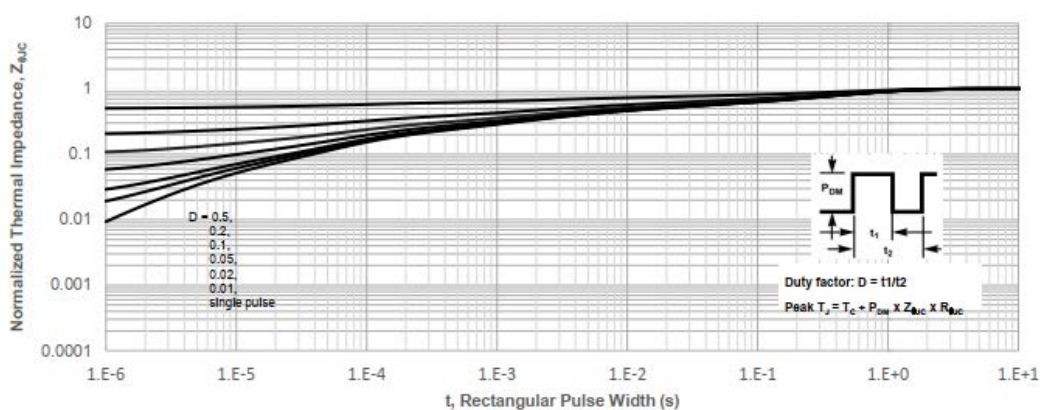


Figure 3. Normalized Maximum Transient Thermal Impedance

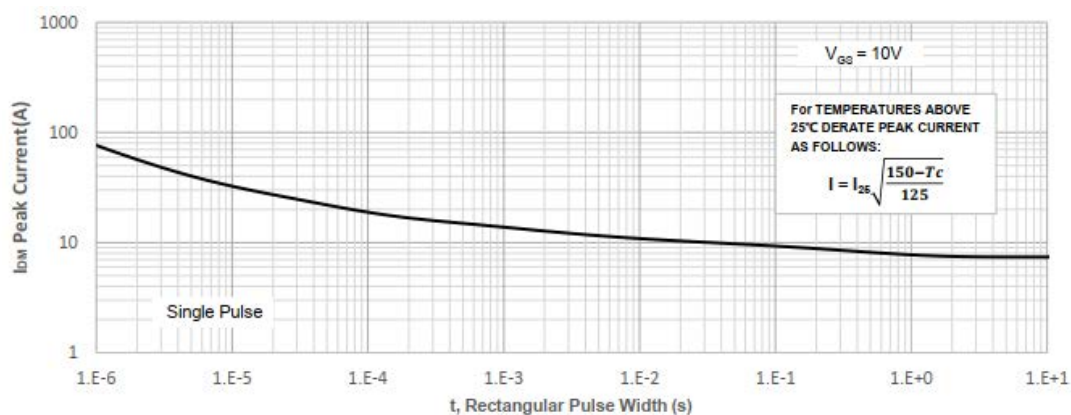


Figure 4. Peak Current Capacity

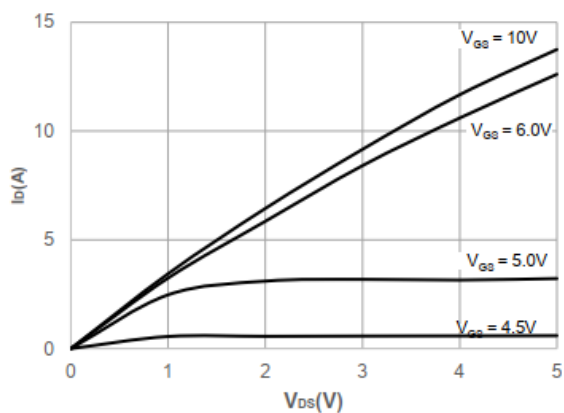


Figure 5. Output Characteristics

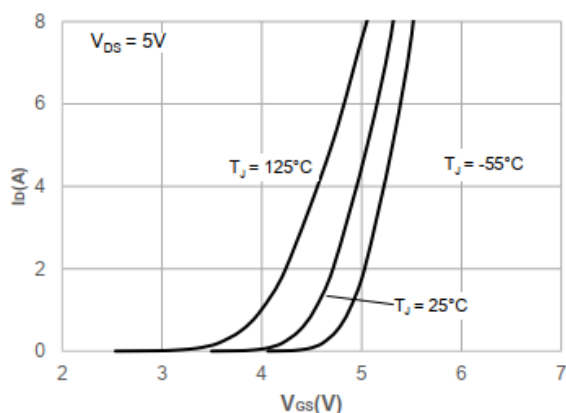


Figure 6. Typical Transfer Characteristics

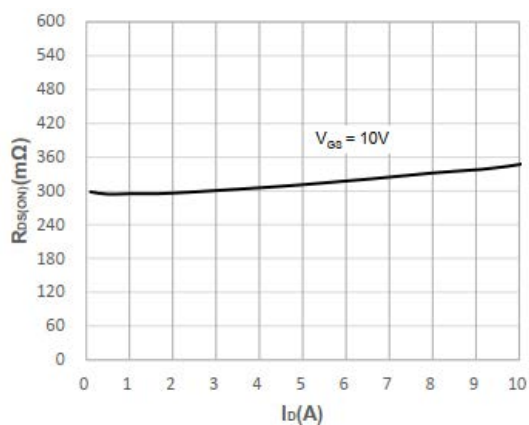


Figure 7. On-resistance vs. Drain Current

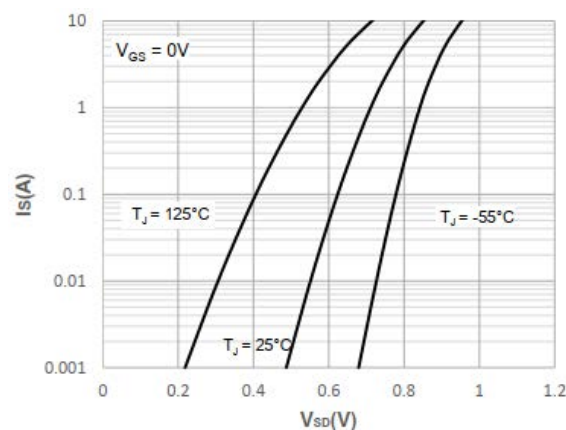


Figure 8. Body Diode Characteristics

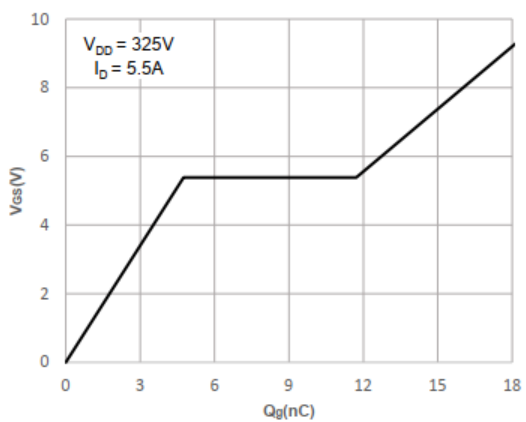


Figure 9. Gate Charge Characteristics

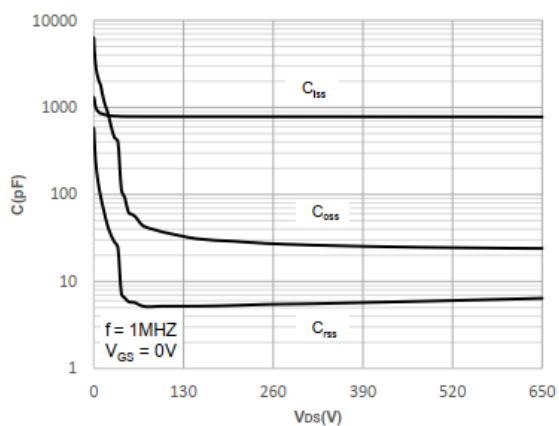


Figure 10. Capacitance Characteristics

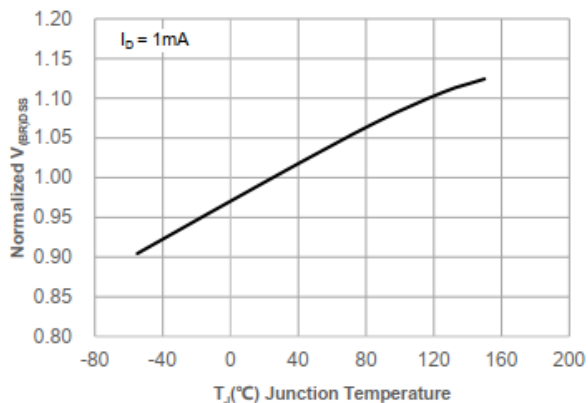


Figure 11. Normalized Breakdown voltage vs. Junction Temperature

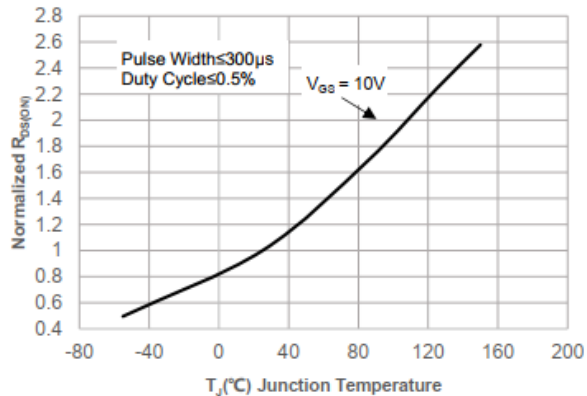


Figure 12. Normalized on Resistance vs. Junction Temperature

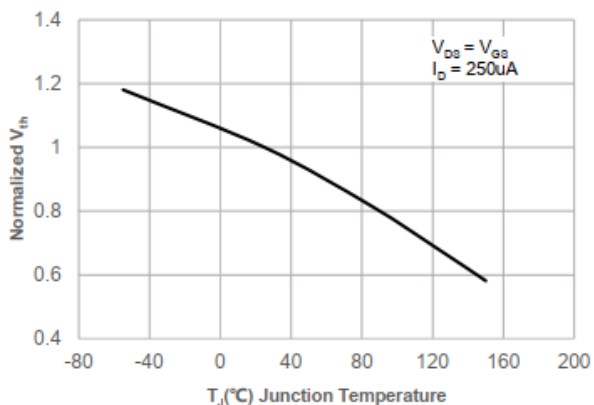


Figure 13. Normalized Threshold Voltage vs. Junction Temperature

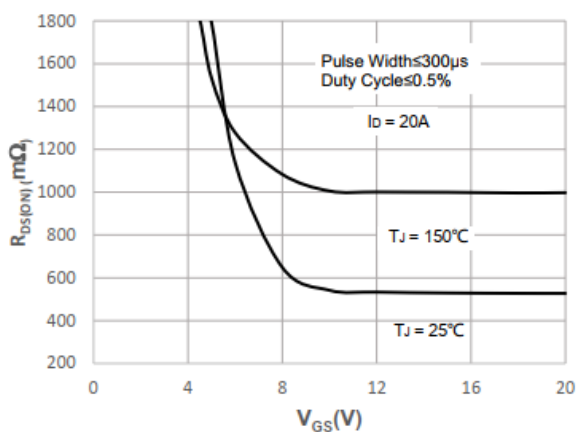


Figure 14. R_DS(ON) vs. V_GS

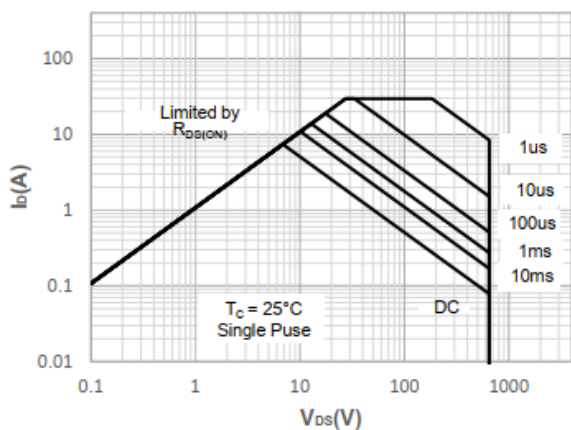
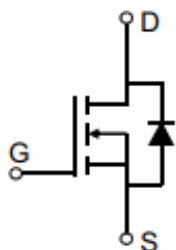
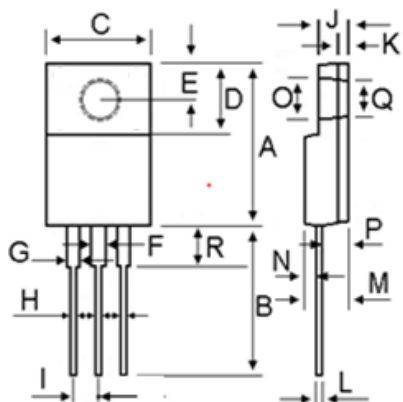


Figure 15. Maximum Safe Operating Area

- Circuit diagram



- Package outlines :



| DIM | MILLIMETERS | |
|-----|-------------|-------|
| | MIN | MAX |
| A | 14.80 | 16.10 |
| B | 12.65 | 14.40 |
| C | 9.70 | 10.36 |
| D | 4.60 | 6.80 |
| E | 2.50 | 3.50 |
| F | 0.90 | 1.55 |
| G | 0.90 | 1.55 |
| H | 0.50 | 0.90 |
| I | 2.40 | 2.70 |
| J | 2.34 | 3.30 |
| K | 0.55 | 1.30 |
| L | 0.36 | 0.80 |
| M | 4.20 | 4.90 |
| N | 1.10 | 1.80 |
| O | 2.90 | 3.50 |
| P | 2.30 | 3.15 |
| Q | 2.90 | 3.50 |
| R | 2.80 | 4.85 |

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