30V Dual P-Channel Power MOSFET

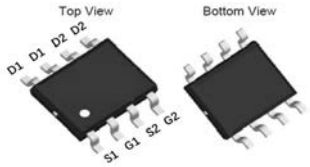
DESCRIPTION：

- Excellent $R_{DS(ON)}$
- Low Gate Charge
- 100% UIS Tested, 100% △Vds Tested
- Pb-Free Lead Plating
- RoHS compliant

V_{DSS}	-30V
I_D (T_A=25℃)	-5.1A
$R_{DS(ON)\_Typ.}$ @V_{GS}=-10V	33mΩ

TYPICAL APPLICATIONS：

- Load Switch
- PWM Application
- Power Management



SOP-8

MAXIMUM RATINGS (at T_A = 25 °C, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_A=25℃ T_A=100℃	I_D	-5.1 -3.2	A
Pulsed Drain Current (1)		I_{DM}	-20	A
Single Pulsed Avalanche Energy (2)		E_{AS}	12	mJ
Power dissipation		P_D	1.5	W
Junction & Storage temperature Range		T_J, T_{STG}	-55~+150	℃

Notes： 1. Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25℃, V_{DD}= -15V, V_{GS}= -10V, R_G=25ohm, L=0.5mH, I_{AS}= -7A, VDD=0V during time in avalanche.

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient	$R_{\theta JA}$	83	℃/W

ELECTRICAL CHARATERISTICS (at T_J = 25 °C, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage VGS = 0V, ID = -250uA	$V_{(BR)DSS}$	-30			V
Zero Gate Voltage Drain Current VDS = -30 V, VGS = 0 V	I_{DSS}			-1	uA
Gate-Source Leakage Current VGS = ±20V, VDS = 0V	I_{GSS}			±100	nA
Gate-Source threshold voltage VDS = VGS, ID = -250uA	$V_{GS(th)}$	-1.0	-1.8	-2.5	V
Drain-Source On-State Resistance VGS = -10V, ID = -5A VGS = -4.5V, ID = -4A	$R_{DS(on)}$		 33 49	 43 64	mΩ
Input capacitance f=1MHz, VDS= -15 V, VGS=0 V	C_{iss}		540		pF
Output capacitance f=1MHz, VDS= -15 V, VGS=0 V	C_{oss}		75		pF
Reverse transfer capacitance f=1MHz, VDS= -15 V, VGS=0 V	C_{rss}		57		pF
Total Gate Charge VDS= -15V, ID= -2A,VGS= -10V	Q_G		11		nC
Gate to Source Charge VDS= -15V, ID= -2A,VGS= -10V	Q_{GS}		2		nC
Gate to Drain Charge VDS= -15V, ID= -2A,VGS= -10V	Q_{GD}		2		nC
Turn-on delay time VDD= -15 V, VGS= -10V, ID= -2A, R_{GEN}=3Ω	td (ON)		3		ns
Rise time VDD= -15 V, VGS= -10V, ID= -2A, R_{GEN}=3Ω	tr		2		ns
Turn-off delay time VDD= -15 V, VGS= -10V, ID= -2A, R_{GEN}=3Ω	td (OFF)		26		ns
Fall time VDD= -15 V, VGS= -10V, ID= -2A, R_{GEN}=3Ω	tf		15		ns

Body Diode

ELECTRICAL CHARATERISTICS (at T_J = 25 °C, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Diode Forward Voltage V_{GS} = 0V, I_S = -5.1A	V_{SD}			-1.2	V
Maximum Continuous Body Diode Forward Current	I_S			-5.1	A
Maximum Pulsed Body Diode Forward Current	I_{SM}			-20	A
Revers Recovery Time IF= -2A, di/dt = 100A/us	Trr		9		ns
Revers Recovery Charge IF= -2A, di/dt = 100A/us	Qrr		3		nC

Typical Performance Characteristics

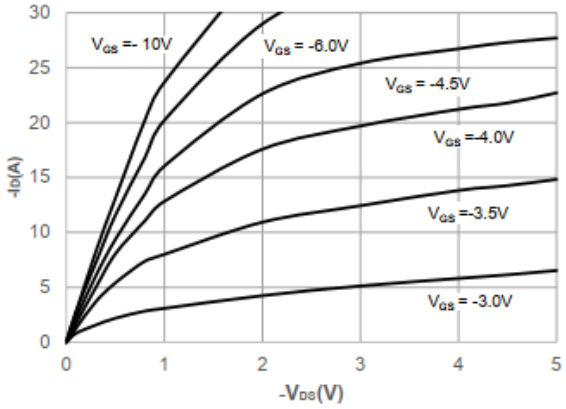


Figure 1. Output Characteristics

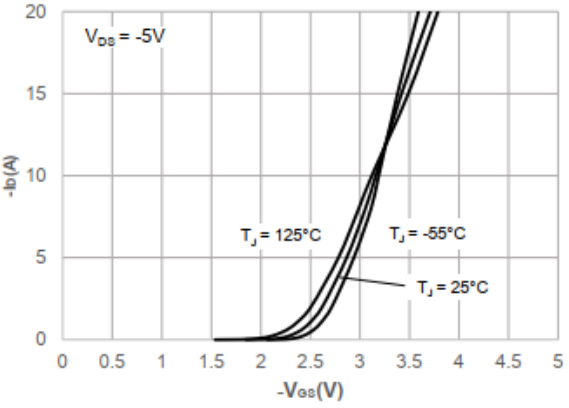


Figure 2. Typical Transfer Characteristics

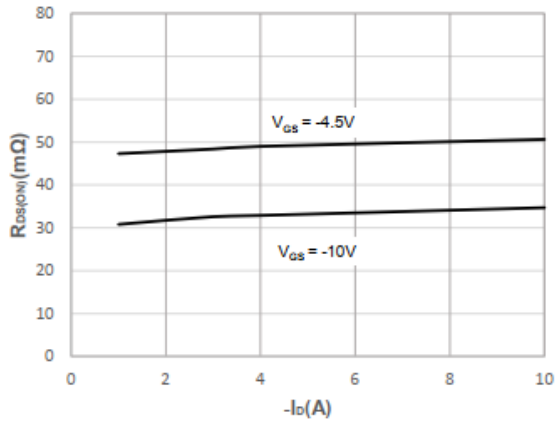


Figure 3. On-resistance vs. Drain Current

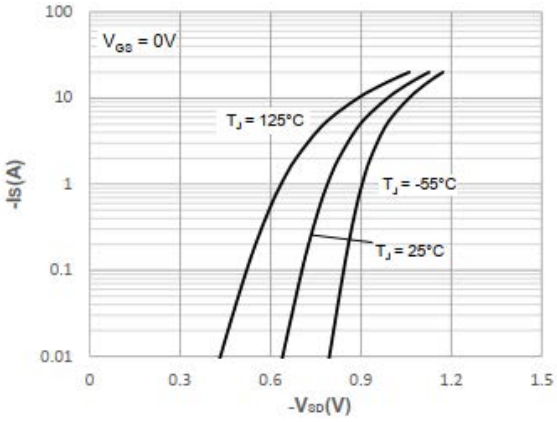


Figure 4. Body Diode Characteristics

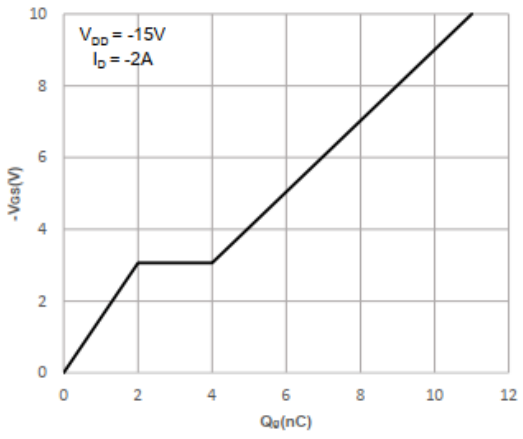


Figure 5. Gate Charge Characteristics

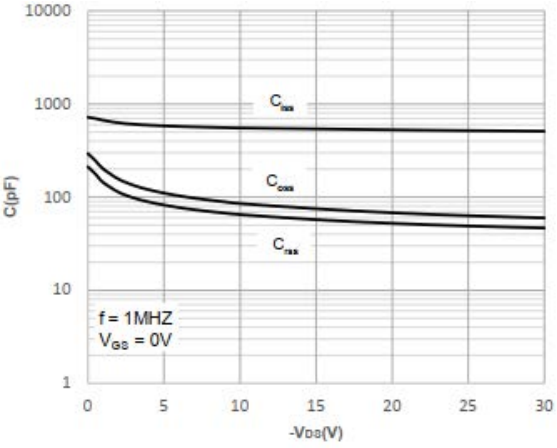


Figure 6. Capacitance Characteristics

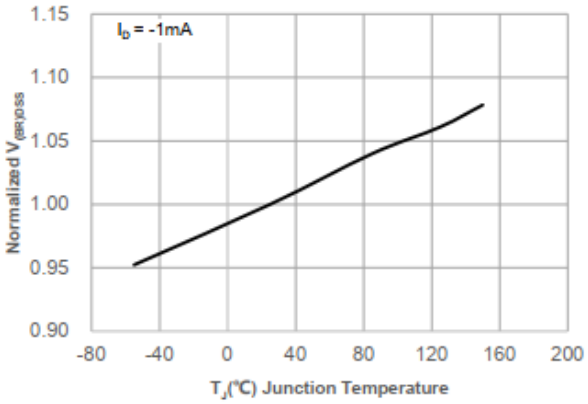


Figure 7. Normalized Breakdown voltage vs. Junction Temperature

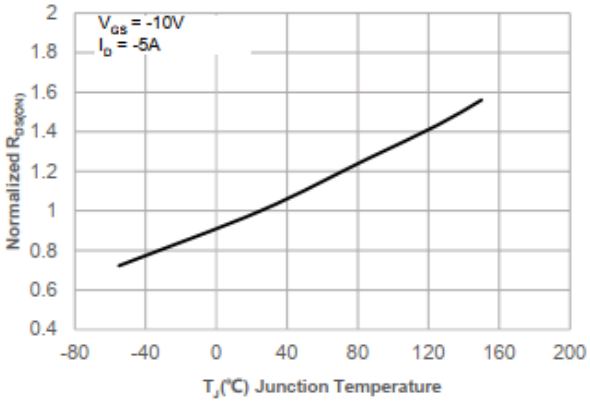


Figure 8. Normalized on Resistance vs. Junction Temperature

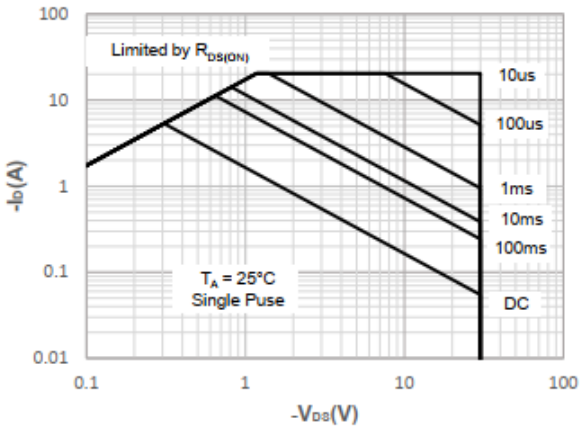


Figure 9. Maximum Safe Operating Area

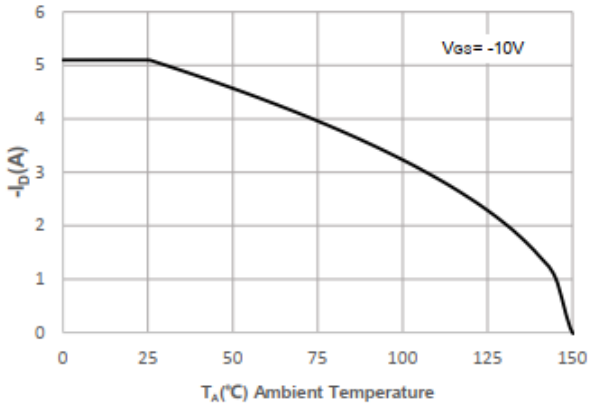


Figure 10. Maximum Continuous Drain Current vs. Ambient Temperature

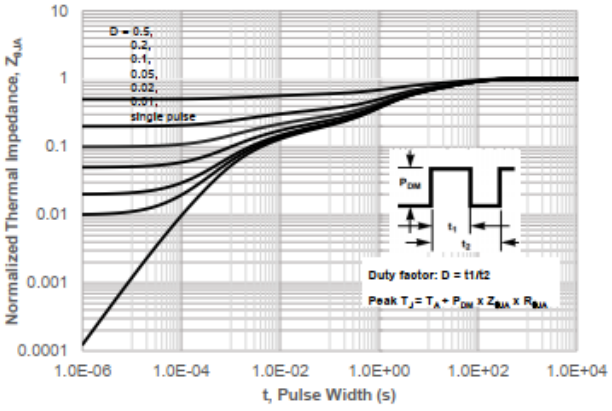


Figure 11. Normalized Maximum Transient Thermal Impedance

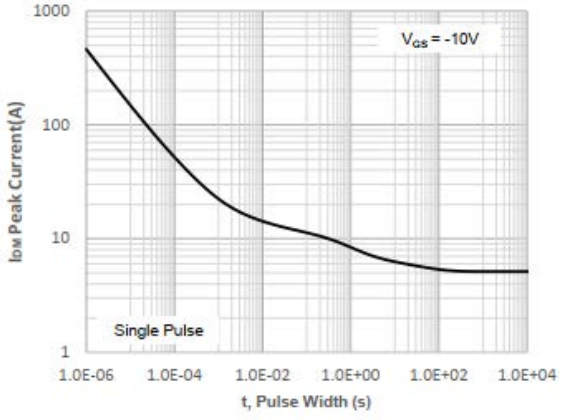
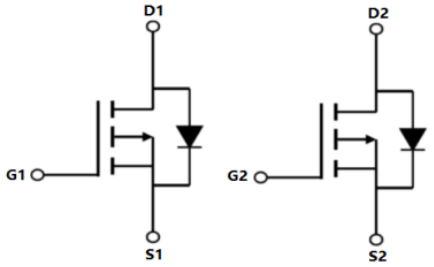
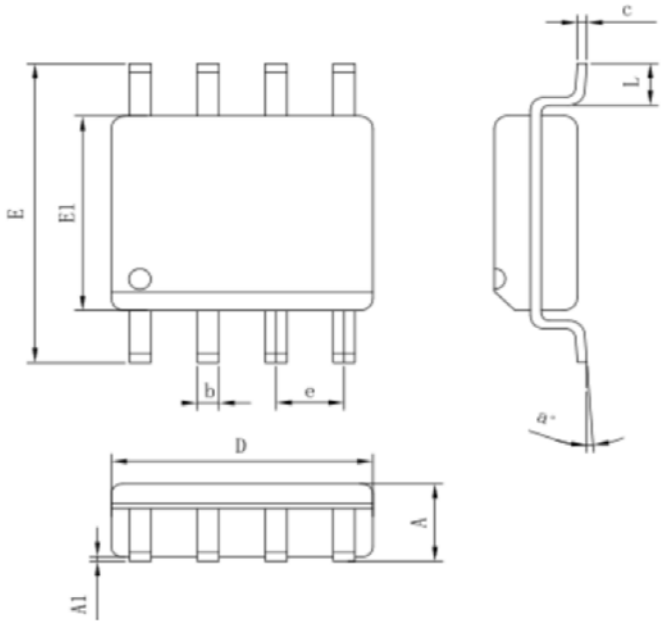


Figure 12. Peak Current Capacity

- Circuit diagram：



- Package outlines : Dimensions in (mm)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	--	--	1.75
A1	0.10	--	0.23
b	0.35	--	0.48
c	0.19	--	0.25
D	4.70	4.90	5.00
E	5.80	6.00	6.20
E1	3.70	3.90	4.10
e	1.27BSC		
L	0.50	--	0.80
a°	0°	--	8°

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