

150V N-Channel Power MOSFET

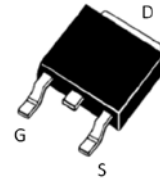
DESCRIPTION :

- Super high dense cell design for extremely low $R_{DS(ON)}$
- High power and current handing capability
- Pb-Free Lead Plating
- RoHS compliant

V_{DSS}	150V
I_D	55A
$R_{DS(ON_Typ.)@V_{GS}=10V}$	17m Ω

TYPICAL APPLICATIONS :

- Load Switch
- PWM Application
- Power Management



TO-252

MAXIMUM RATINGS (at $T_C = 25^\circ\text{C}$, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$	I_D	55 35	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	220	A
Single Pulsed Avalanche Energy ⁽²⁾		E_{AS}	31.25	mJ
Maximum Power dissipation	$T_C=25^\circ\text{C}$ - Derate above 25 C	P_D	86 0.68	W
Single Pulsed Avalanche Current ⁽²⁾		I_{AS}	25	A
Junction & Storage temperature Range		T_J, T_{STG}	-55~+150	$^\circ\text{C}$

Notes : 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. L =0.1mH, IAS =25A, VDD = 50V, RG = 25W, Starting TJ = 25 C..

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction – Ambient Junction - Case	$R_{\theta(j-A)}$ $R_{\theta(j-C)}$	50 1.45	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage VGS = 0V, ID = 250uA	BV _{DSS}	150			V
Zero Gate Voltage Drain Current VDS = 150 V, VGS = 0 V	I _{DSS}			1	uA
Gate-Source Leakage Current VGS = ±20V, VDS = 0V	I _{GSS}			±100	nA
Gate-Source threshold voltage VDS = VGS, ID = 250uA	V _{GS(th)}	2		4	V
Drain-Source On-State Resistance VGS = 10V, ID = 20A	R _{DS(on)}		13.2	17	mΩ
Input capacitance f=1MHz, VDS=75 V, VGS=0 V	C _{iss}		1920		pF
Output capacitance f=1MHz, VDS=75 V, VGS=0 V	C _{oss}		225		pF
Reverse transfer capacitance f=1MHz, VDS=75 V, VGS=0 V	C _{rss}		15		pF
Total Gate Charge VDS= 75V, ID= 20A, VGS= 10V	Q _G		30		nC
Gate to Source Charge VDS= 75V, ID= 20A, VGS= 10V	Q _{GS}		9		nC
Gate to Drain Charge VDS= 75V, ID= 20A, VGS= 10V	Q _{GD}		6		nC
Turn-on delay time VDD=75 V, VGS= 10V, ID= 20A, R _{GEN} =10Ω	t _{d(ON)}		25		ns
Turn-on Rise time VDD=75 V, VGS= 10V, ID= 20A, R _{GEN} =10Ω	t _r		6		ns
Turn-off delay time VDD=75 V, VGS= 10V, ID= 20A, R _{GEN} =10Ω	t _{d(OFF)}		38		ns
Turn-off Fall time VDD=75 V, VGS= 10V, ID= 20A, R _{GEN} =10Ω	t _f		7		ns

Typical Performance Characteristics

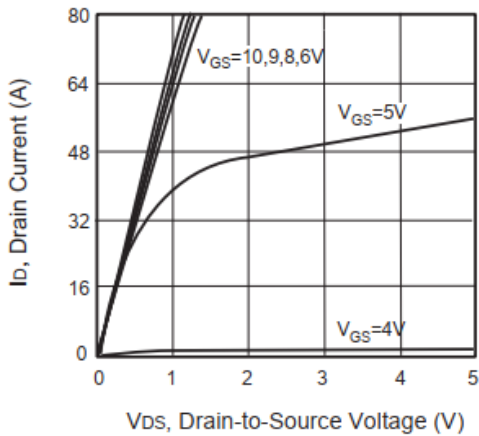


Figure 1. Output Characteristics

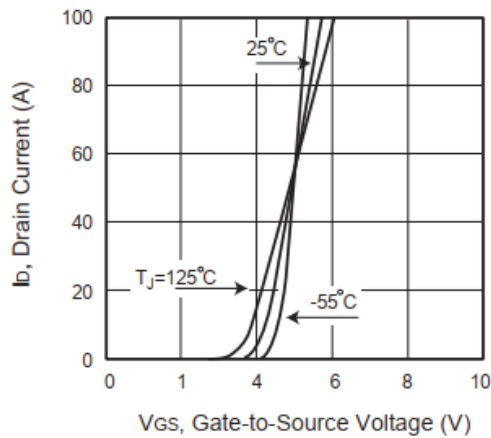


Figure 2. Typical Transfer Characteristics

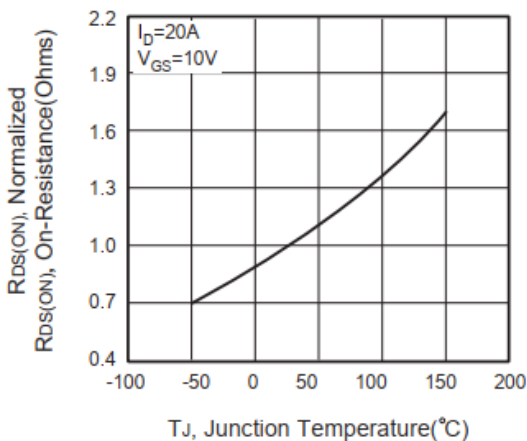


Figure 3. On-Resistance Variation with Temperature

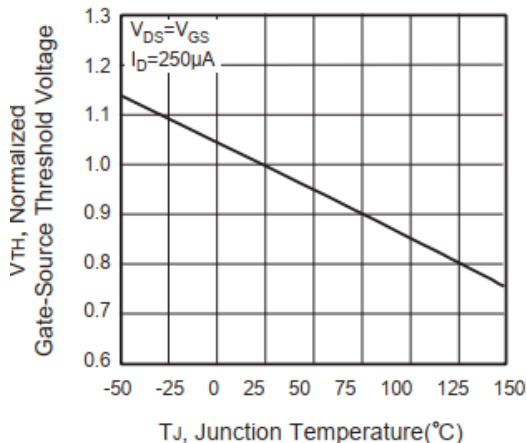


Figure 4. Gate Threshold Variation with Temperature

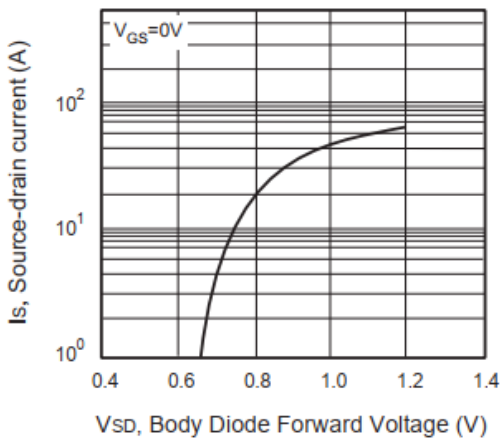


Figure 5. Gate Charge Characteristics

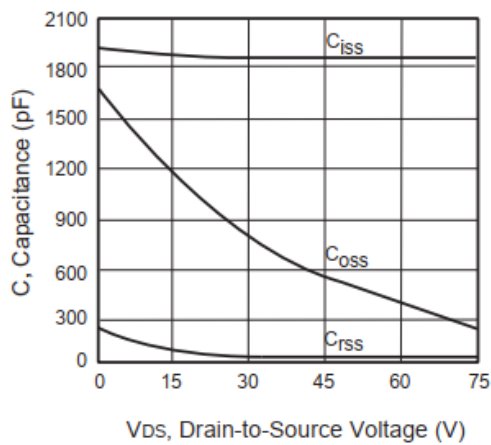


Figure 6. Body Diode Forward Voltage Variation with Source Current

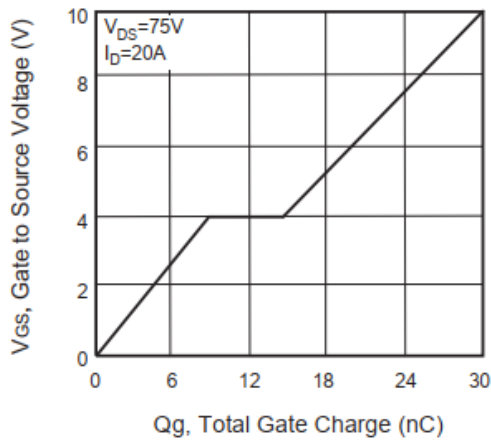


Figure 7. Gate Charge

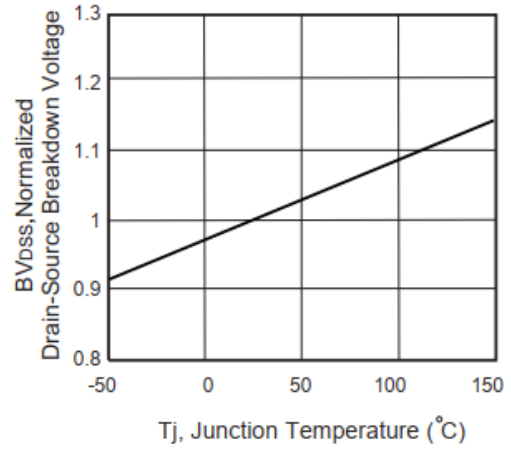


Figure 8. Breakdown Voltage Variation VS Temperature

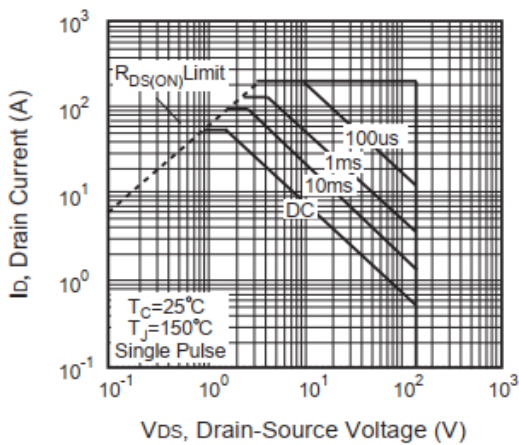


Figure 9. Maximum Safe Operating Area

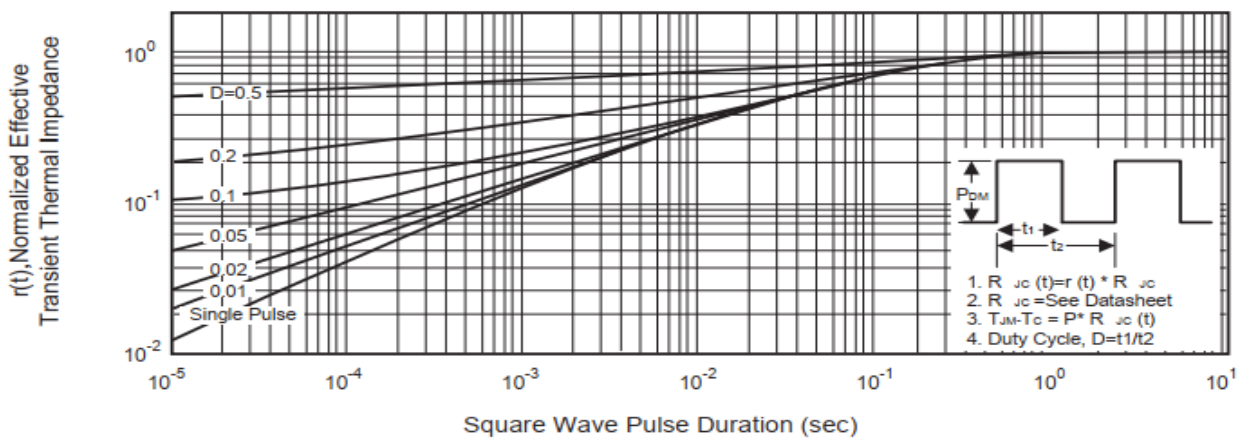
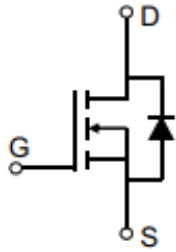


Figure 10. Normalized Thermal Transient Impedance Curve

- Circuit diagram



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