

30V N+P Channel MOSFET

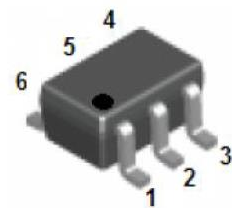
DESCRIPTION :

- N+P Channel
- Very Low on-resistance
- SOT23-6L Package
- Pb-Free, RoHS Compliant

V_{DS}	30V	-30V
I_D	6.0A	-4.5A
$R_{DS(ON_Typ. @V_{GS}=\pm 10V}$	33m Ω	46m Ω

TYPICAL APPLICATIONS :

- DC-DC Conversion Circuits
- Load/Power Switching with Level Shift
- Switching circuits
- Power supply converter



SOT-23-6L

ABSOLUTE MAXIMUM RATINGS (at $T_C = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Condition	Symbol	N MOS	P MOS	Unit
Gate-Source Voltage		V_{GS}	± 20	± 20	V
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	30	-30	V
Diode Continuous Forward Current		I_S	6.0	-4.5	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	30	-25	A
Continuous Drain Current	$T_C=25\text{ }^\circ\text{C}$ $T_C=70\text{ }^\circ\text{C}$	I_D	6.0 4.8	-4.5 -3.6	A
Power dissipation	$T_C=25\text{ }^\circ\text{C}$ $T_A=70\text{ }^\circ\text{C}$	P_D	1.56 1.0		W
Thermal Resistance from Junction to Ambient		θ_{thJA}	80		$^\circ\text{C/W}$
Junction & Storage temperature Range		T_J, T_{STG}	-55~150		$^\circ\text{C}$

Notes : 1. Pulse width limited by maximum allowable junction temperature.

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified) -N-Channel

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = 250\mu A$	$V_{(BR)DSS}$	30			V
Zero Gate Voltage Drain Current $V_{DS} = 30V, V_{GS} = 0V, T_J = 25\text{ }^\circ\text{C}$ $V_{DS} = 24V, V_{GS} = 0V, T_J = 125\text{ }^\circ\text{C}$	I_{DSS}			1.0 100	μA
Gate-Source Leakage Current $V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}			± 100	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(th)}$	1.0	1.6	2.5	V
Drain-Source On-State Resistance $V_{GS} = 10V, I_D = 3A,$ $V_{GS} = 4.5V, I_D = 2A,$	$R_{DS(on)}$		33 45	45 60	m Ω
Input capacitance $f = 1MHz, V_{DS} = 15V, V_{GS} = 0V$	C_{iss}		467		pF
Output capacitance $f = 1MHz, V_{DS} = 15V, V_{GS} = 0V$	C_{oss}		45		pF
Reverse transfer capacitance $f = 1MHz, V_{DS} = 15V, V_{GS} = 0V$	C_{rss}		35		pF
Total Gate Charge $V_{DS} = 15V, I_D = 4A, V_{GS} = 10V$	Q_G		10.4		nC
Gate to Source Charge $V_{DS} = 15V, I_D = 4A, V_{GS} = 10V$	Q_{GS}		1.6		nC
Gate to Drain Charge $V_{DS} = 15V, I_D = 4A, V_{GS} = 10V$	Q_{GD}		2.1		nC
Turn-on delay time $V_{DD} = 15V, V_{GS} = 10V, I_D = 1A, R_G = 3.3\Omega$	$t_{d(ON)}$		4.4		ns
Rise time $V_{DD} = 15V, V_{GS} = 10V, I_D = 1A, R_G = 3.3\Omega$	t_r		2.6		ns
Turn-off delay time $V_{DD} = 15V, V_{GS} = 10V, I_D = 1A, R_G = 3.3\Omega$	$t_{d(OFF)}$		25.5		ns
Fall time $V_{DD} = 15V, V_{GS} = 10V, I_D = 1A, R_G = 3.3\Omega$	t_f		3.3		ns
Diode Forward Voltage $V_{GS} = 0V, I_S = 2A$	V_{SD}			1.2	V

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified) -P-Channel

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = -250\mu A$	$V_{(BR)DSS}$	-30			V
Zero Gate Voltage Drain Current $V_{DS} = -30V, V_{GS} = 0V, T_J = 25\text{ }^\circ\text{C}$ $V_{DS} = -24V, V_{GS} = 0V, T_J = 125\text{ }^\circ\text{C}$	I_{DSS}			-1.0 -100	μA
Gate-Source Leakage Current $V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}			± 100	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = -250\mu A$	$V_{GS(th)}$	-1.0	-1.4	-2.5	V
Drain-Source On-State Resistance $V_{GS} = -10V, I_D = -3A,$ $V_{GS} = -4.5V, I_D = -2A,$	$R_{DS(on)}$		46 63	60 80	m Ω
Input capacitance $f=1MHz, V_{DS} = -15V, V_{GS} = 0V$	C_{iss}		563		pF
Output capacitance $f=1MHz, V_{DS} = -15V, V_{GS} = 0V$	C_{oss}		67		pF
Reverse transfer capacitance $f=1MHz, V_{DS} = -15V, V_{GS} = 0V$	C_{rss}		57		pF
Total Gate Charge $V_{DS} = -15V, I_D = -4A, V_{GS} = -10V$	Q_G		8.2		nC
Gate to Source Charge $V_{DS} = -15V, I_D = -4A, V_{GS} = -10V$	Q_{GS}		1.4		nC
Gate to Drain Charge $V_{DS} = -15V, I_D = -4A, V_{GS} = -10V$	Q_{GD}		2.7		nC
Turn-on delay time $V_{DD} = -15V, V_{GS} = -10V, I_D = -1.5A, R_G = 3.3\Omega$	$t_{d(ON)}$		10		ns
Rise time $V_{DD} = -15V, V_{GS} = -10V, I_D = -1.5A, R_G = 3.3\Omega$	t_r		29		ns
Turn-off delay time $V_{DD} = -15V, V_{GS} = -10V, I_D = -1.5A, R_G = 3.3\Omega$	$t_{d(OFF)}$		23		ns
Fall time $V_{DD} = -15V, V_{GS} = -10V, I_D = -1.5A, R_G = 3.3\Omega$	t_f		38		ns
Diode Forward Voltage $V_{GS} = 0V, I_{SD} = -2A$	V_{SD}			-1.2	V

Typical Performance Characteristics- N-Channel

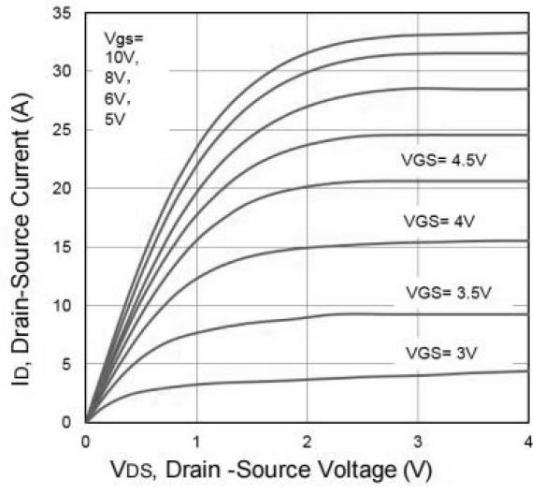


Figure 1 Output Characteristics

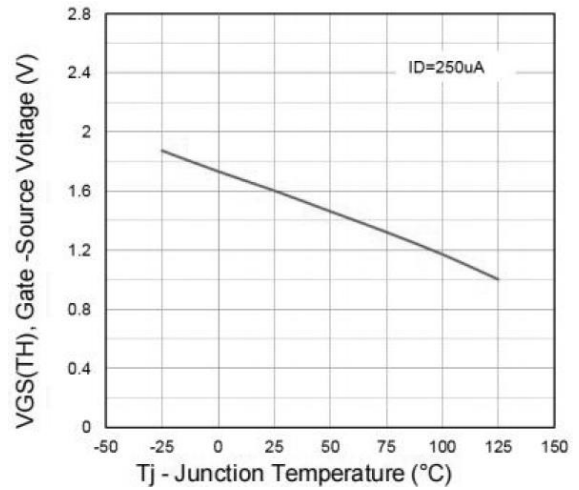


Figure 2. Normalized Threshold Voltage Vs. Temp.

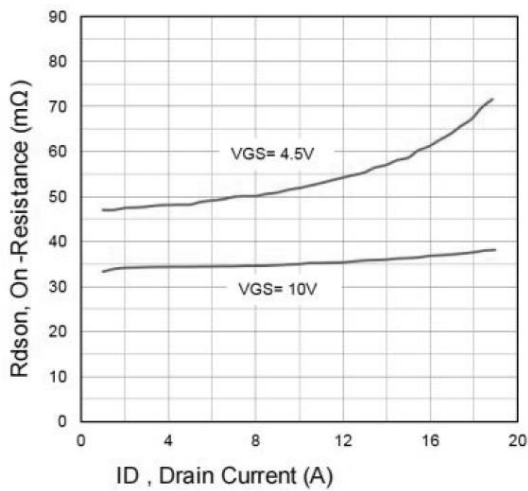


Figure 3. On-Resistance vs. Drain Current and Gate

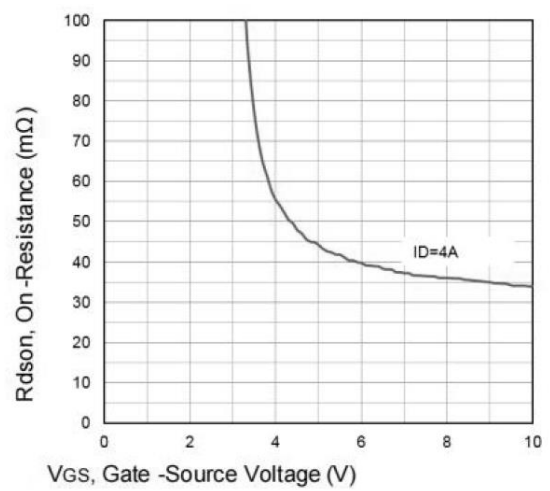


Figure 4. On-Resistance vs. Gate Source Voltage

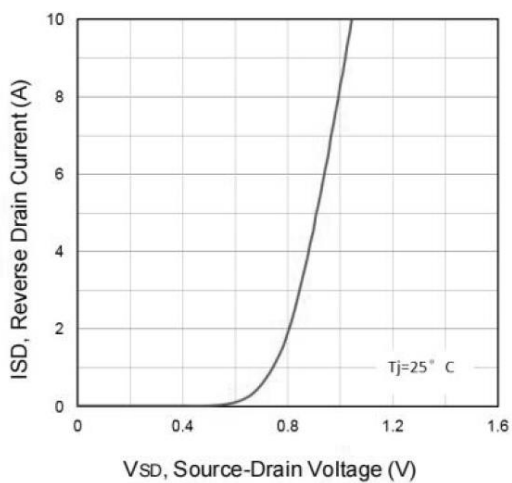


Figure 5. Source-Drain Diode Forward Voltage

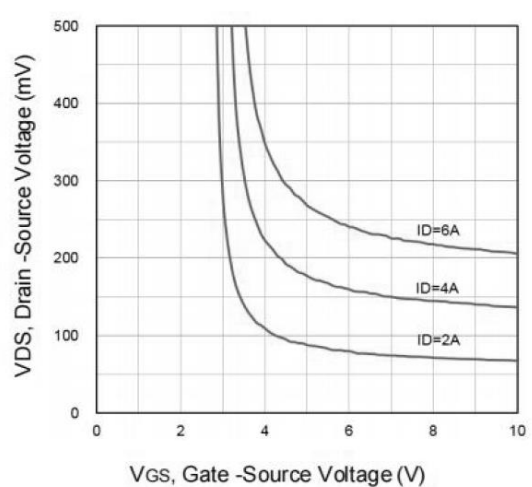


Figure 6. Drain-Source Voltage vs Gate-Source Voltage

Typical Performance Characteristics

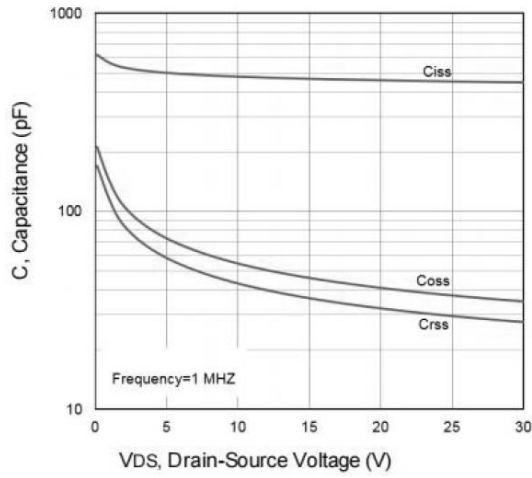


Figure 7. Capacitance Vs. Drain-Source Voltage

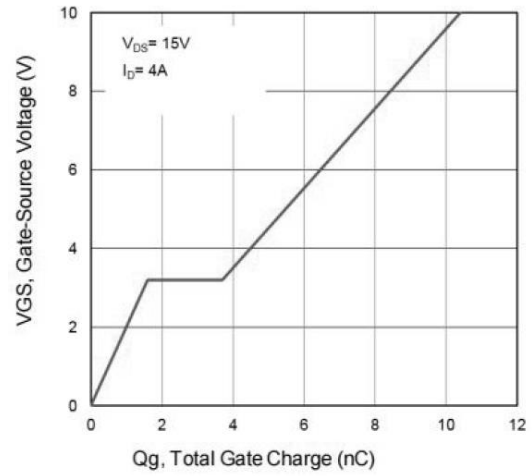


Figure 8. Gate Charge Vs. Gate-Source Voltage

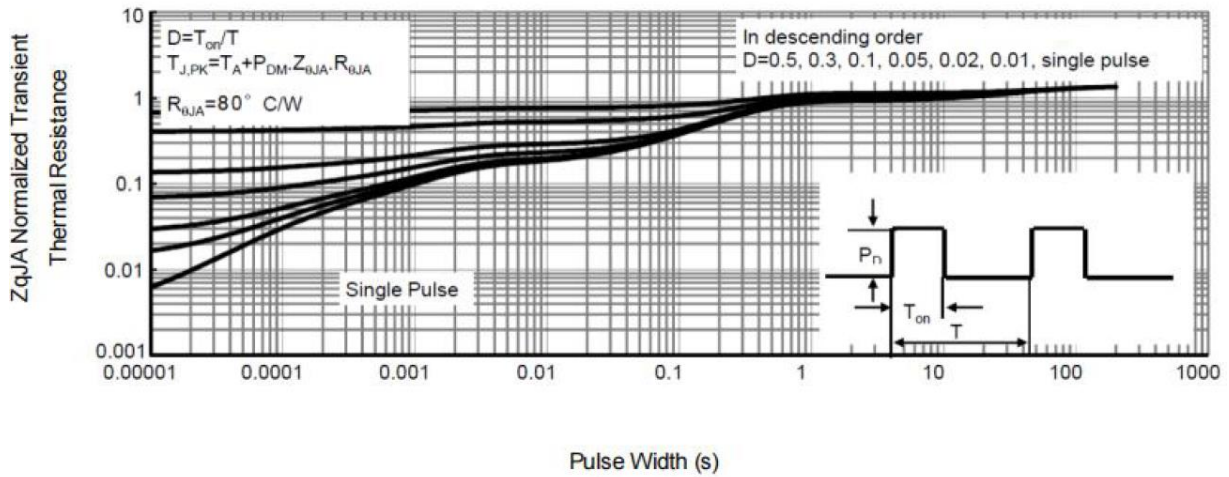


Figure 9. Normalized Maximum Transient Thermal Impedance

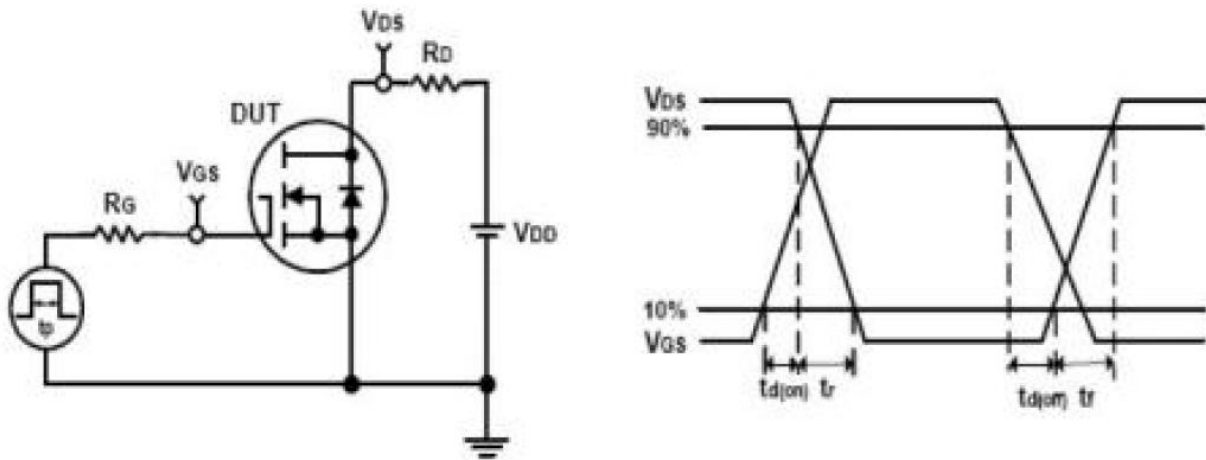


Figure 10. Switching Time Test Circuit and Waveforms

Typical Performance Characteristics- P-Channel

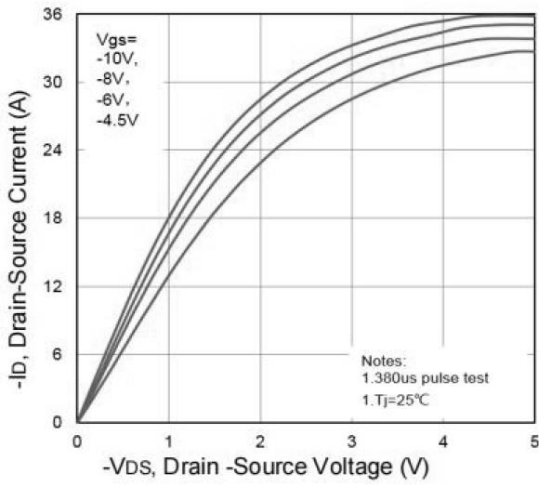


Figure 1 Output Characteristics

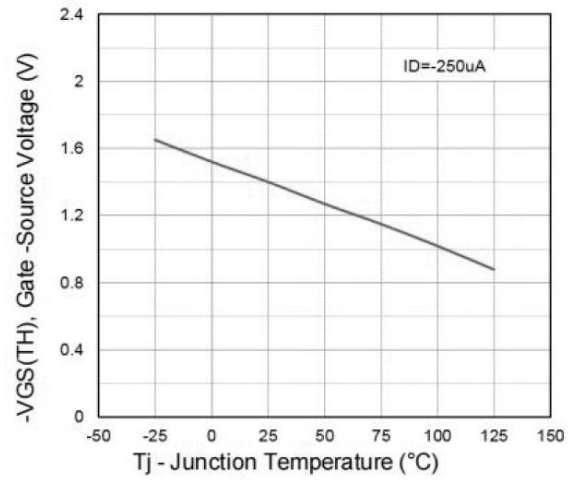


Figure 2. Normalized Threshold Voltage Vs. Temp.

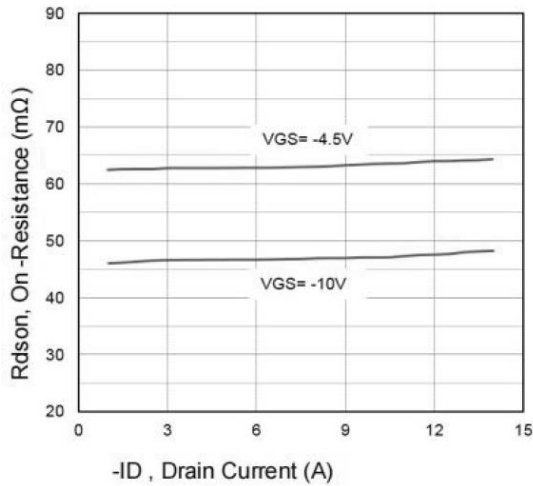


Figure 3. On-Resistance vs. Drain Current and Gate

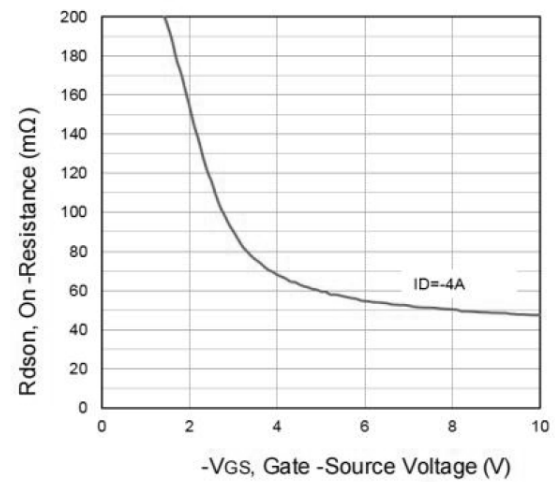


Figure 4. On-Resistance vs. Gate Source Voltage

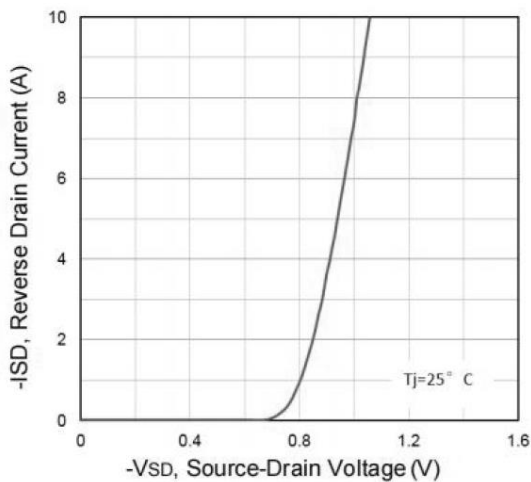


Figure 5. Source-Drain Diode Forward Voltage

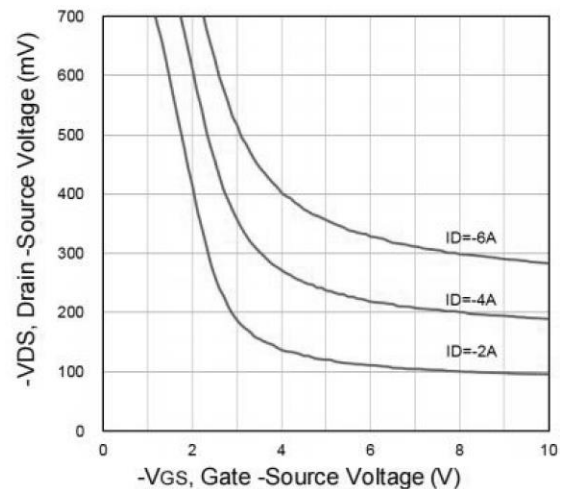


Figure 6. Drain-Source Voltage vs Gate-Source Voltage

Typical Performance Characteristics- P-Channel

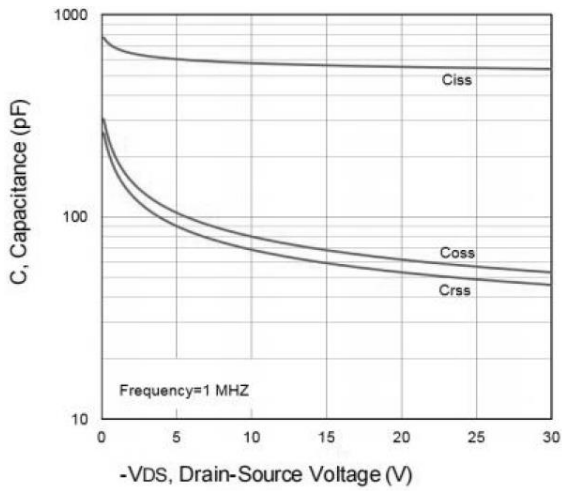


Figure 7. Capacitance Vs. Drain-Source Voltage

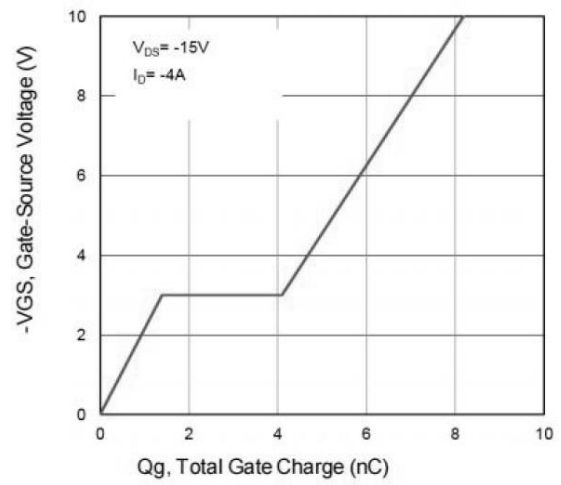


Figure 8. Gate Charge Vs. Gate-Source Voltage

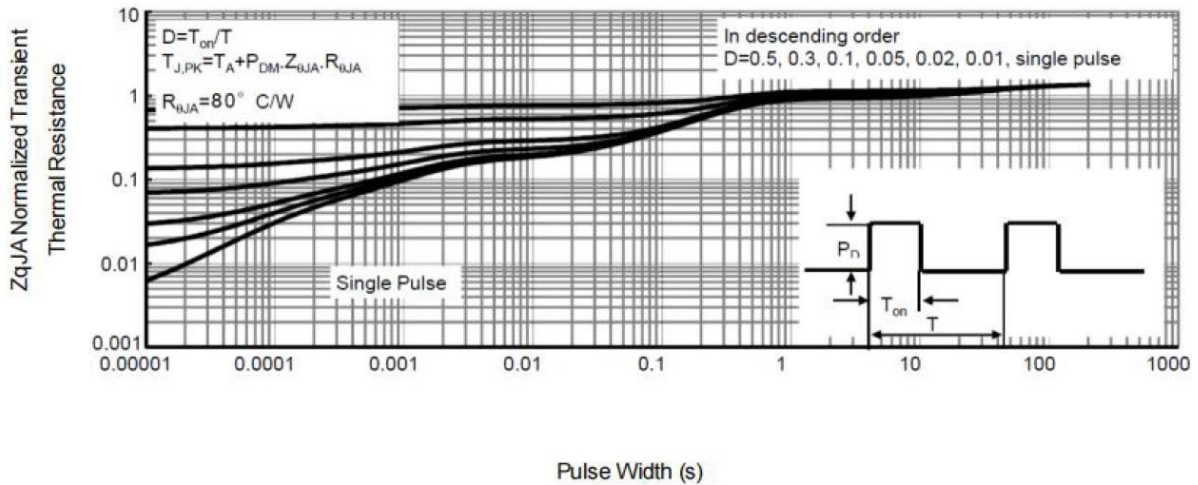


Figure 9. Normalized Maximum Transient Thermal Impedance

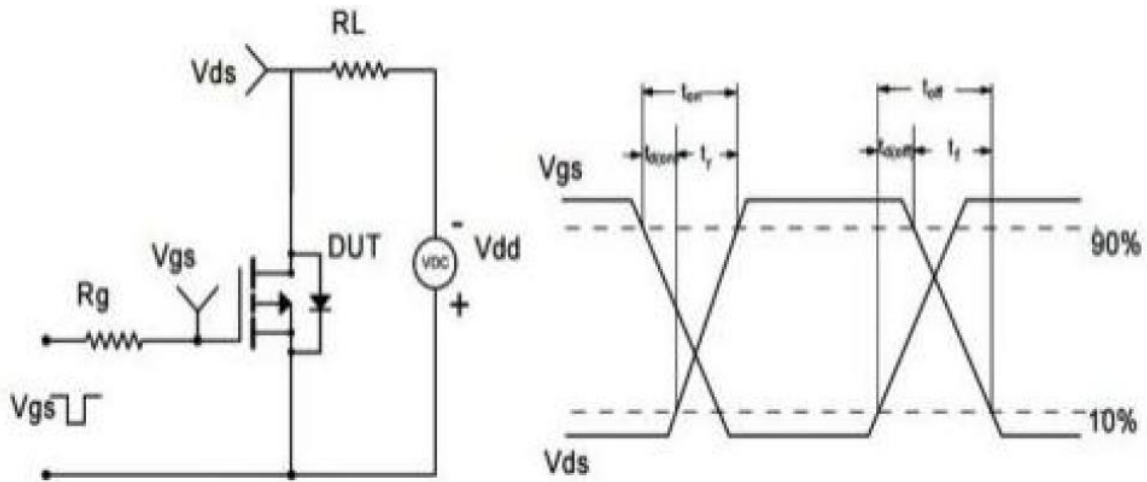
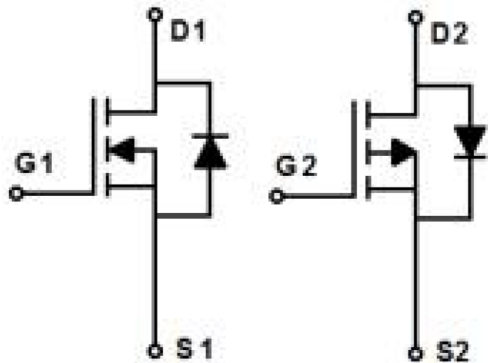
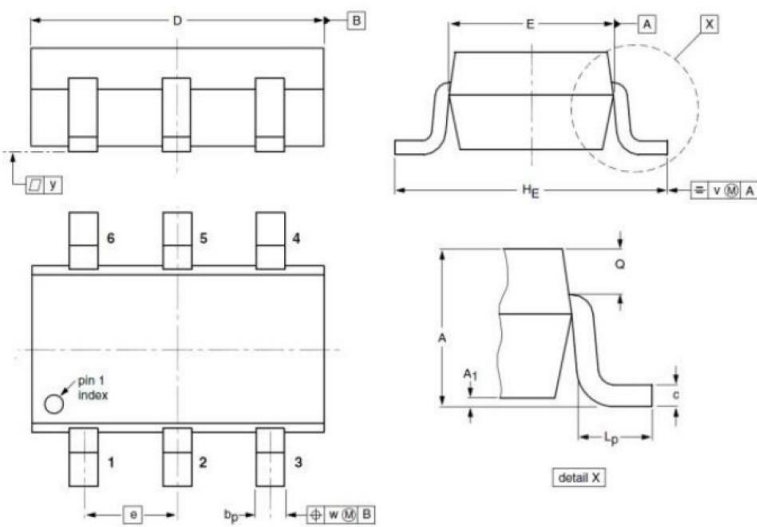


Figure 10. Switching Time Test Circuit and Waveforms

·Circuit diagram



·Package outlines : Dimensions in (mm)



Symbol	Min.	Max.
A	0.90	1.25
A1	0.01	0.10
bp	0.25	0.40
c	0.10	0.26
D	2.70	3.10
E	1.30	1.70
e	0.95 TYP	
HE	2.50	3.00
Lp	0.20	0.60
Q	0.23	0.33
v	0.20 TYP	
w	0.20 TYP	
y	0.10 TYP	

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