

20V N-Channel Enhancement Mode Field Effect Transistor

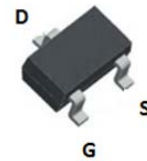
DESCRIPTION :

- High dense cell design for extremely low $R_{DS(ON)}$
- Rugged and reliable
- $R_{DS(ON),typ.} = 40m\Omega @ V_{GS}=4.5V$
- RoHS compliant

V_{DS}	20V
I_{D_MAX}	4.4A
$R_{DS(ON)_MAX} @ V_{GS}=4.5V$	40m Ω

TYPICAL APPLICATIONS :

- Synchronous Rectification
- Power Management
- Load Switch



SOT-23

MAXIMUM RATINGS (at $T_A = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	± 12	V
Continuous Drain Current		I_D	4.4	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	17.6	A
Power dissipation		P_D	1.25	W
Operating & Storage temperature Range		T_J, T_{STG}	-55~+150	$^\circ\text{C}$

Notes : 1. Repetitive rating; pulse width limited by maximum junction temperature.

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction-to-Ambient	$R_{\theta JA}$	100	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS (at $T_A = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = 250\mu A$	BV_{DSS}	20			V
Zero Gate Voltage Drain Current $V_{DS} = 20V, V_{GS} = 0V$	I_{DSS}			1	μA
Gate-Source Leakage Current $V_{GS} = \pm 12V, V_{DS} = 0V$	I_{GSS}			± 100	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(th)}$	0.4		1.0	V
Drain-Source On-State Resistance $V_{GS} = 4.5V, I_D = 1A$ $V_{GS} = 2.5V, I_D = 1A$ $V_{GS} = 1.8V, I_D = 1A$	$R_{DS(on)}$		30 40 75	40 70 110	m Ω
Input capacitance $f=1MHz, V_{DS}=10V, V_{GS}=0V$	C_{iss}		470		pF
Output capacitance $f=1MHz, V_{DS}=10V, V_{GS}=0V$	C_{oss}		85		pF
Reverse transfer capacitance $f=1MHz, V_{DS}=10V, V_{GS}=0V$	C_{rss}		50		pF
Total Gate Charge $V_{DD}= 10V, I_D= 4A, V_{GS}= 4.5V$	Q_G		4.7		nC
Gate to Source Charge $V_{DD}= 10V, I_D= 4A, V_{GS}= 4.5V$	Q_{GS}		0.4		nC
Gate to Drain Charge $V_{DD}= 10V, I_D= 4A, V_{GS}= 4.5V$	Q_{GD}		1.2		nC
Turn-on delay time $V_{DD}=10V, V_{GS}= 4.5V, I_D= 4A, R_{GEN}= 6\Omega$	$t_{d(ON)}$		10		ns
Turn-on Rise time $V_{DD}=10V, V_{GS}= 4.5V, I_D= 4A, R_{GEN}= 6\Omega$	tr		5		ns
Turn-off delay time $V_{DD}=10V, V_{GS}= 4.5V, I_D= 4A, R_{GEN}= 6\Omega$	$t_{d(OFF)}$		36		ns
Turn-off Fall time $V_{DD}=10V, V_{GS}= 4.5V, I_D= 4A, R_{GEN}= 6\Omega$	tf		9		ns

Body Diode

Drain to Source Diode Forward Current	I_S			1.0	A
Drain to Source Diode Forward Voltage $V_{GS} = 0V, I_S = 1A$	V_{SD}			1.2	V

Typical Characteristics

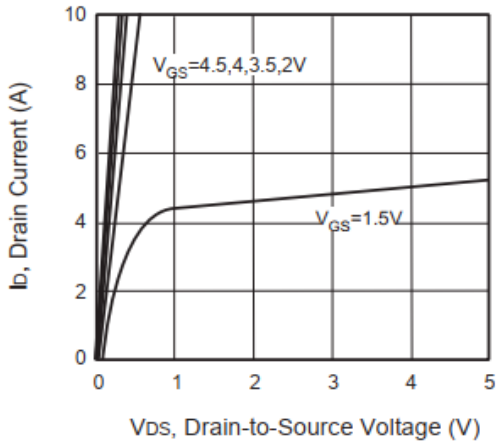


Figure 1. Output Characteristics

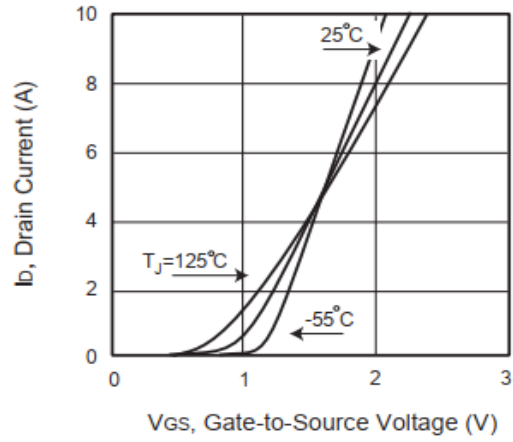


Figure 2. Transfer Characteristics

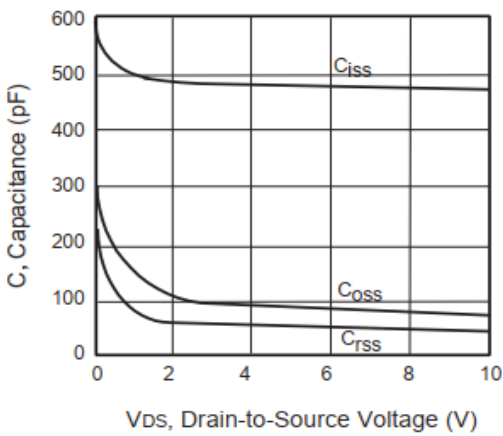


Figure 3. Capacitance

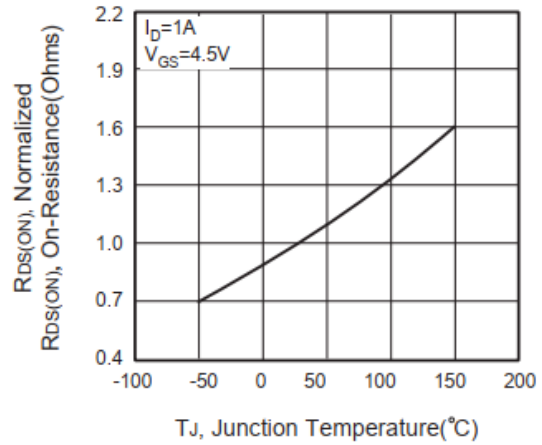


Figure 4. On-Resistance Variation with Temperature

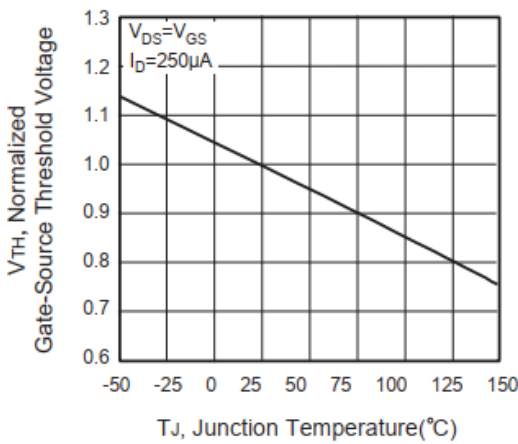


Figure 5. Gate Threshold Variation with Temperature

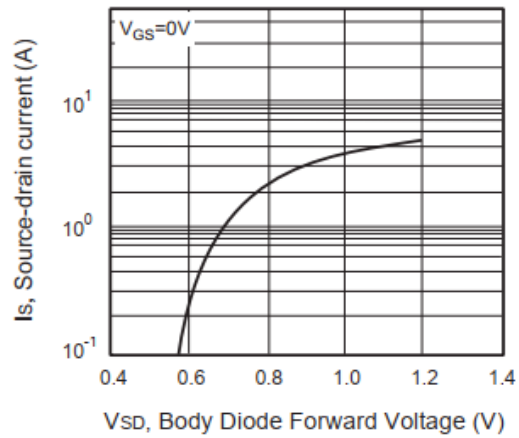


Figure 6. . Body Diode Forward Voltage Variation with Source Current

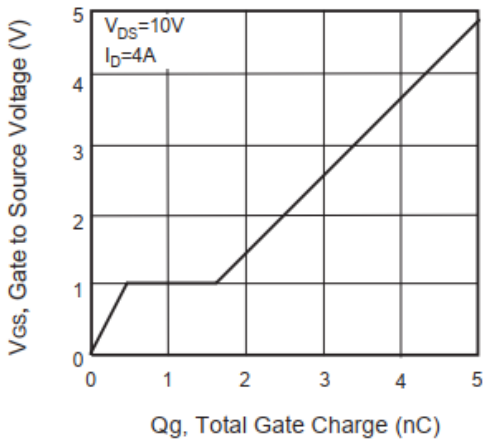


Figure 7. Gate Charge Characteristics

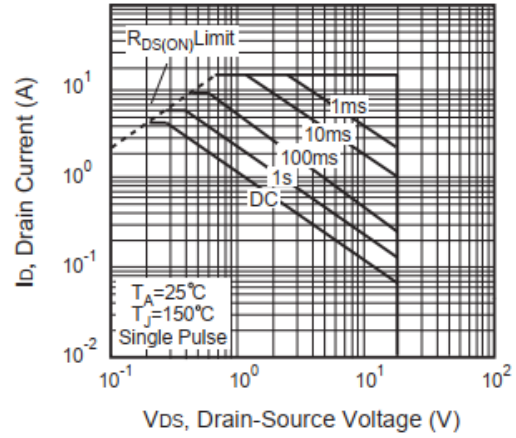


Figure 8. Maximum Safe Operating Area

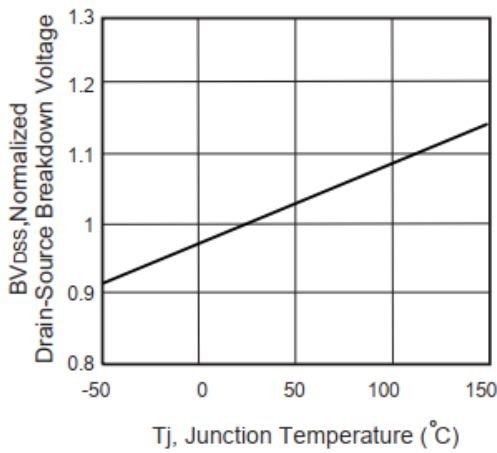


Figure 9. Breakdown Voltage Variation vs. Temperature

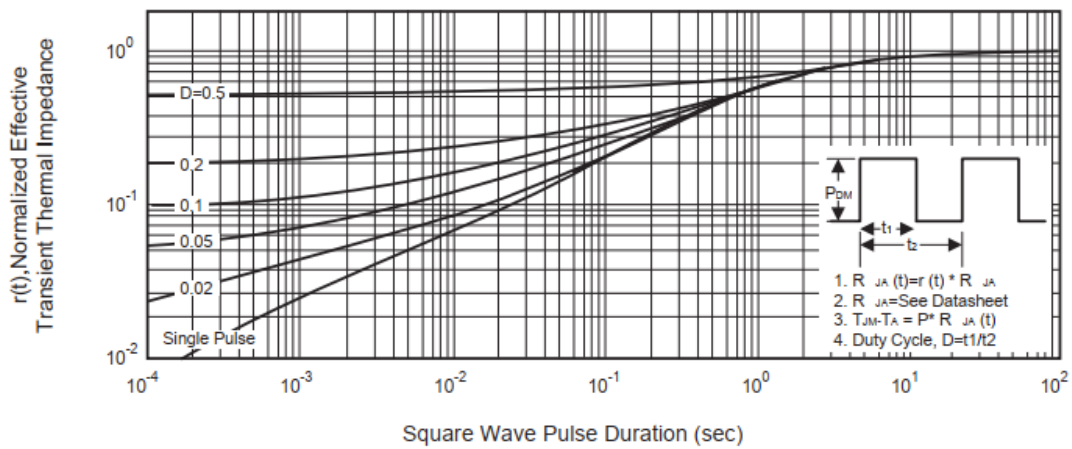
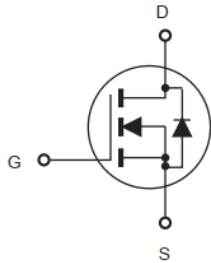
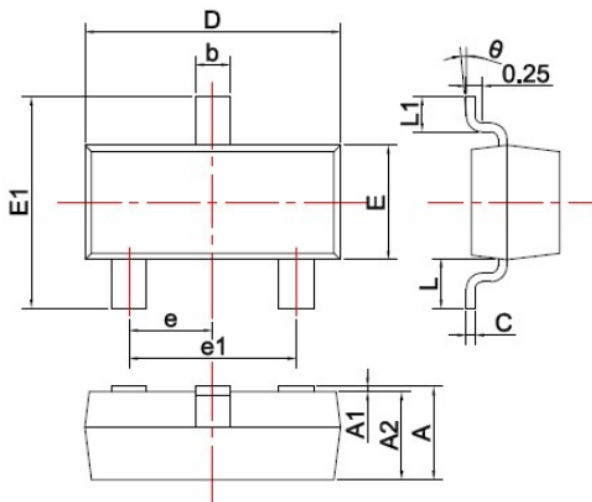


Figure 10. Normalized Thermal Transient Impedance Curve

- Circuit diagram



- SOT-23 Package outlines : Dimensions in (mm)



DIM	MILLIMETERS	
	MIN	MAX
A	0.90	1.15
A1	0.00	0.10
A2	0.90	1.05
b	0.30	0.50
c	0.08	0.15
D	2.80	3.00
E	1.20	1.40
E1	2.25	2.55
e	0.95 Typ.	
e1	1.80	2.00
L	0.55 Ref.	
L1	0.30	0.50
θ	0°	8°

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