100V N-channel Power SGT MOSFET

DESCRIPTION：

·Excellent $R_{DS(ON)}$ and Low Gate Charge

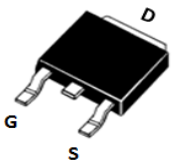
·100% UIS Tested

·100%ΔVds Tested

·Halogen-free

·RoHS-compliant

V_{DSS}	100V
I_D @V_{GS}=10V	29A
$R_{DS(ON)_Typ.}$ @V_{GS}=10V	25mΩ



TO-252

TYPICAL APPLICATIONS：

·Power Management

·Load Switch

·PWM Application

MAXIMUM RATINGS (at T_C = 25 °C, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	Tc=25℃ Tc=100℃	I_D	29 19	A
Pulsed Drain Current (1)		I_{DM}	Refer to Fig.4	A
Single Pulsed Avalanche Energy (2)		E_{AS}	36	mJ
Power dissipation	Tc=25℃ Tc=100℃	P_D	52 21	W
Junction & Storage temperature Range		T_J, T_{STG}	-55~+150	℃

Notes： 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} condition: Starting T_J=25C, V_{DD}=50V, V_{GS}=10V, R_G=25ohm, L=3mH, I_{AS}=4.9A, V_{DD}=0V during time in avalanche.

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient Junction to Case	$R_{\theta JA}$ $R_{\theta JC}$	47 2.4	℃/W

ELECTRICAL CHARATERISTICS (at T_J = 25 °C, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage VGS = 0V, ID = 250uA	$V_{(BR)DSS}$	100			V
Zero Gate Voltage Drain Current VDS = 80V, VGS = 0 V	I_{DSS}			1.0	uA
Gate-Source Leakage Current VGS = ±20V, VDS = 0V	I_{GSS}			±100	nA
Gate-Source threshold voltage VDS = VGS, ID = 250uA	$V_{GS(th)}$	1.2	1.7	2.2	V
Drain-Source On-State Resistance VGS = 10V, ID = 10A VGS = 4.5V, ID = 6A	$R_{DS(on)}$		25 33	33 43	mΩ
Gate Resistance f=1MHz	R_g		1.2		Ω
Input capacitance f=1MHz, VDS=50V, VGS=0 V	C_{iss}		513	770	pF
Output capacitance f=1MHz, VDS=50V, VGS=0 V	C_{oss}		182	273	pF
Reverse transfer capacitance f=1MHz, VDS=50V, VGS=0 V	C_{rss}		12		pF
Total Gate Charge VDS= 50V, ID= 10A,VGS= 0 to 10V	Q_G		10		nC
Gate to Source Charge VDS= 50V, ID= 10A,VGS= 0 to 10V	Q_{GS}		2.1		nC
Gate to Drain Charge VDS= 50V, ID= 10A,VGS= 0 to 10V	Q_{GD}		2.3		nC
Turn-on delay time VDD=50 V, VGS= 10V, ID=10A, R_{GEN}=3Ω	$td_{(ON)}$		5.3		ns
Rise time VDD=50 V, VGS= 10V, ID=10A, R_{GEN}=3Ω	tr		12		ns
Turn-off delay time VDD=50 V, VGS= 10V, ID=10A, R_{GEN}=3Ω	$td_{(OFF)}$		13		ns
Fall time VDD=50 V, VGS= 10V, ID=10A, R_{GEN}=3Ω	tf		3.1		ns
Maximum Continuous Body Diode Forward Current	IS			29	A
Maximum Pulsed Body Diode Forward Current	ISM			117	A
Diode Forward Voltage VGS = 0V, I_S =30A	V_{SD}			1.2	V
Revers Recovery Time IF=10A, di/dt = 100A/us	Trr	16	27	41	ns
Revers Recovery Charge IF=10A, di/dt = 100A/us	Qrr		20		nC

Typical Performance Characteristics

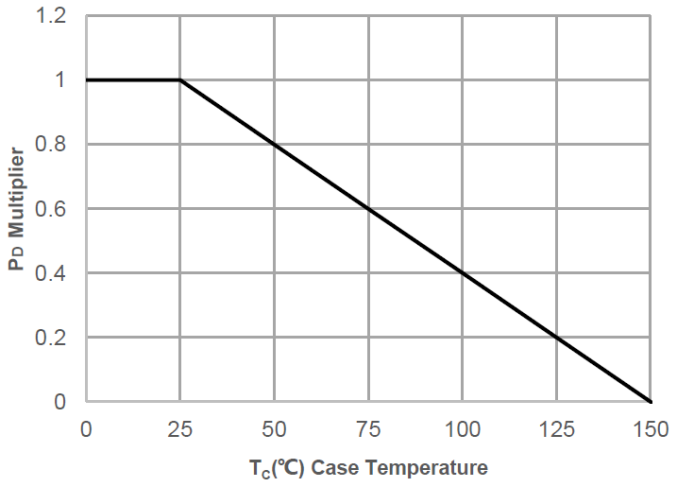


Figure 1. Power De-rating

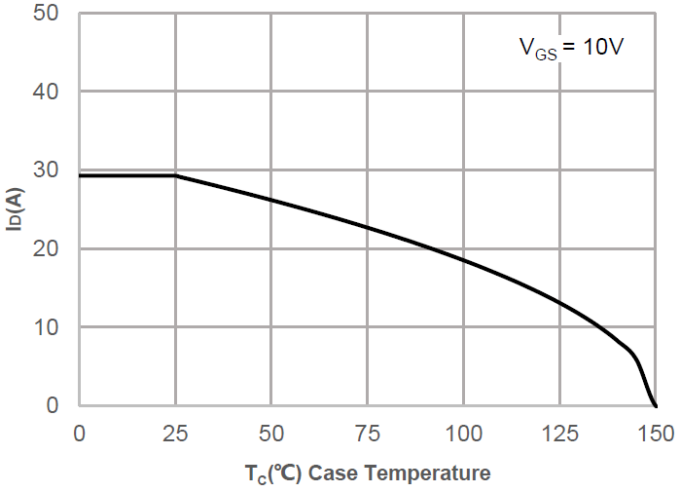


Figure 2. Current De-rating

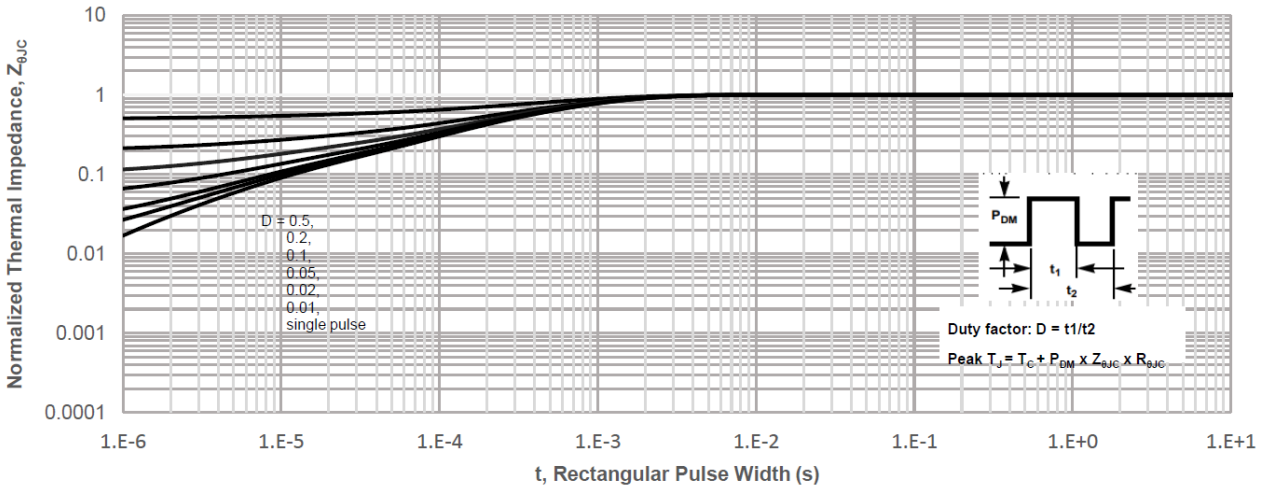


Figure 3. Normalized Maximum Transient Thermal Impedance

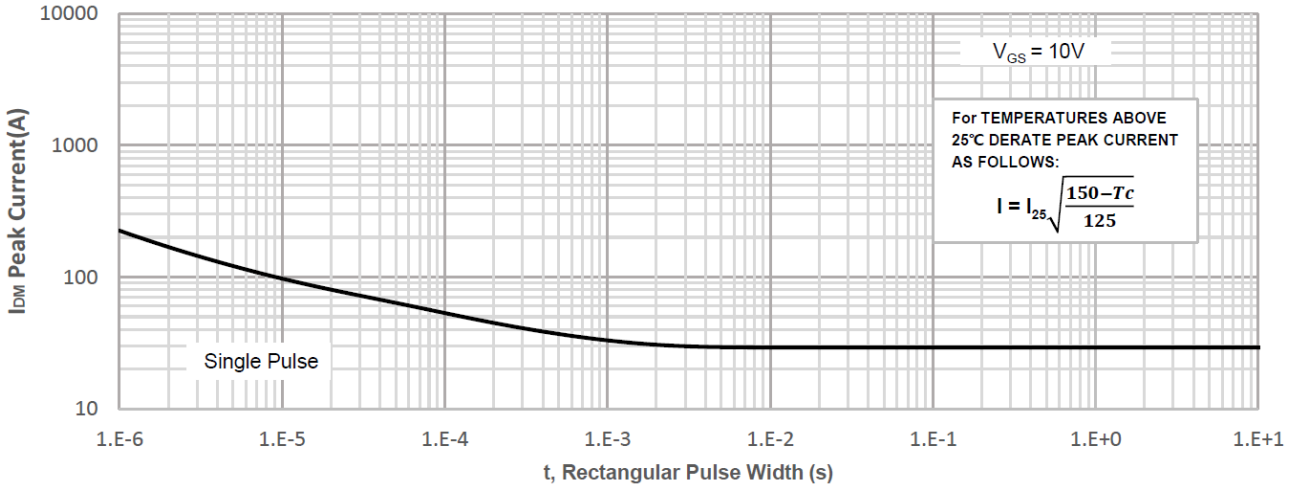


Figure4. Peak Current Capacity

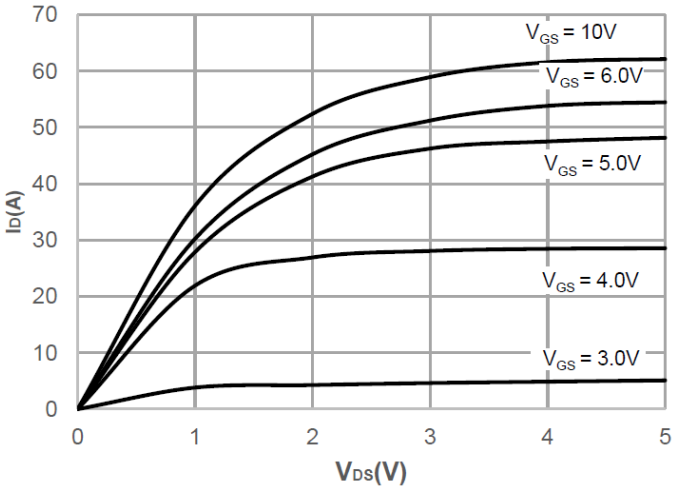


Figure 5. Output Characteristics

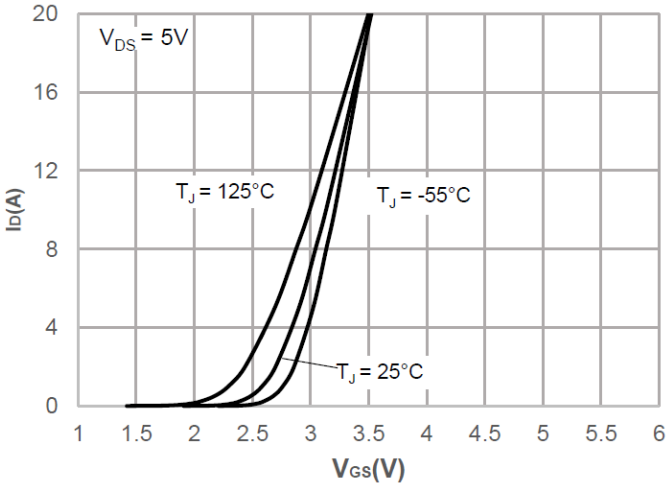


Figure 6. Typical Transfer Characteristics

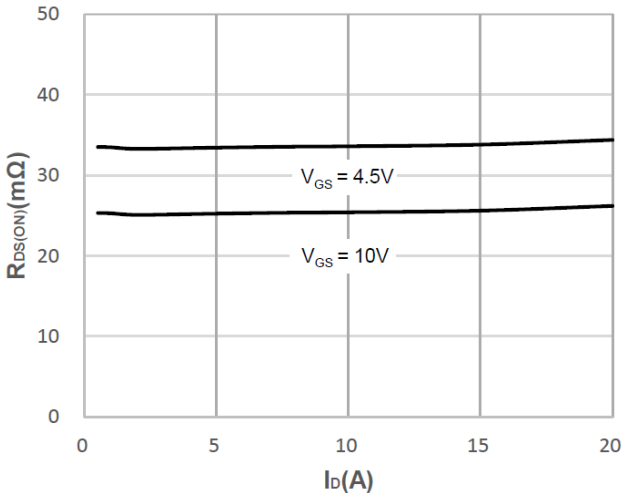


Figure 7. On-resistance vs. Drain Current

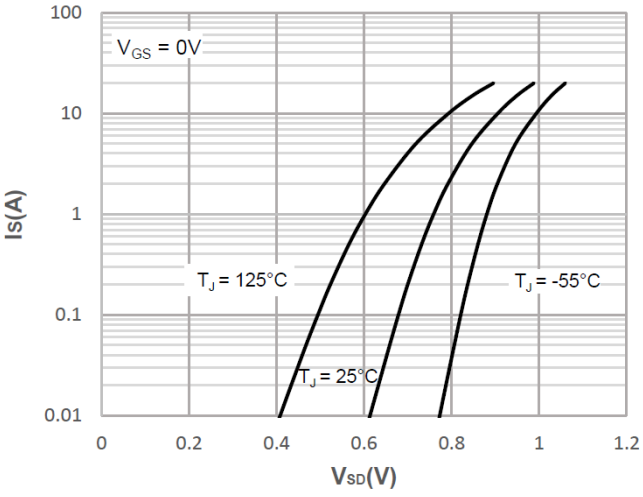


Figure8. Body Diode Characteristics

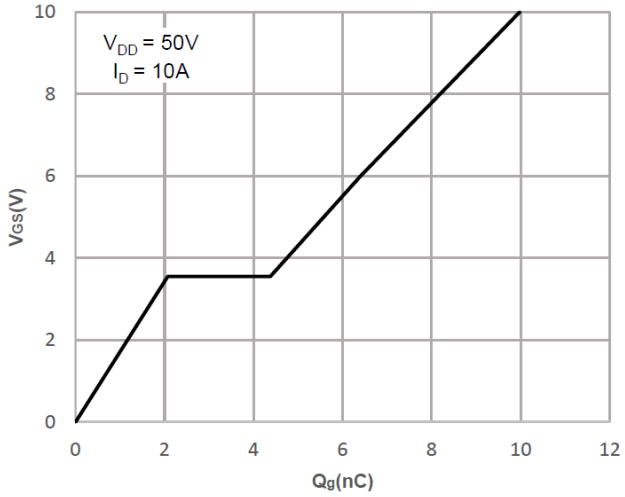


Figure 9. Gate Charge Characteristics

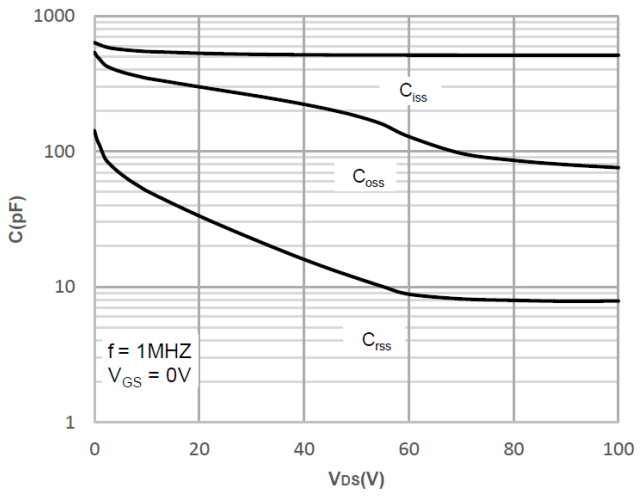


Figure 10. Capacitance Characteristics

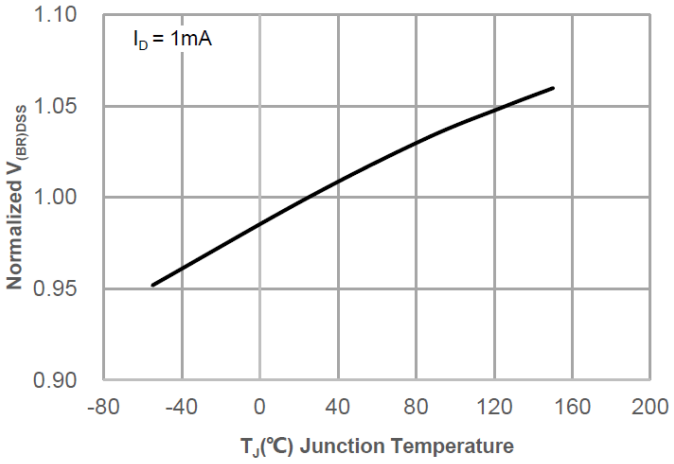


Figure 11. Normalized Breakdown voltage vs. Junction Temperature

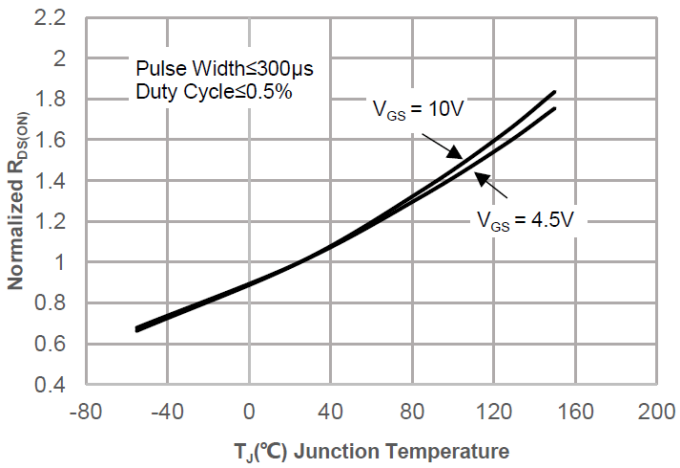


Figure 12. Normalized on Resistance vs. Junction Temperature

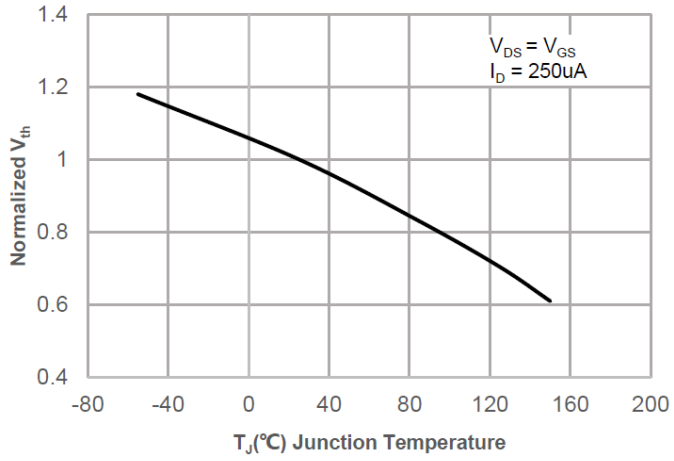


Figure 13. Normalized Threshold Voltage vs. Junction Temperature

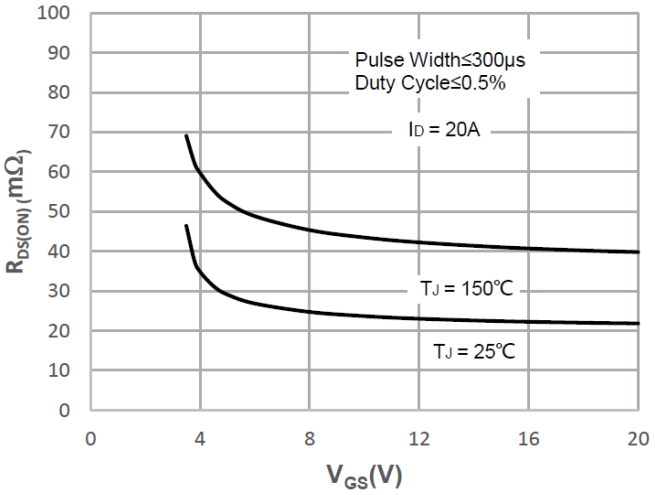


Figure14. $R_{DS(ON)}$ vs. V_{GS}

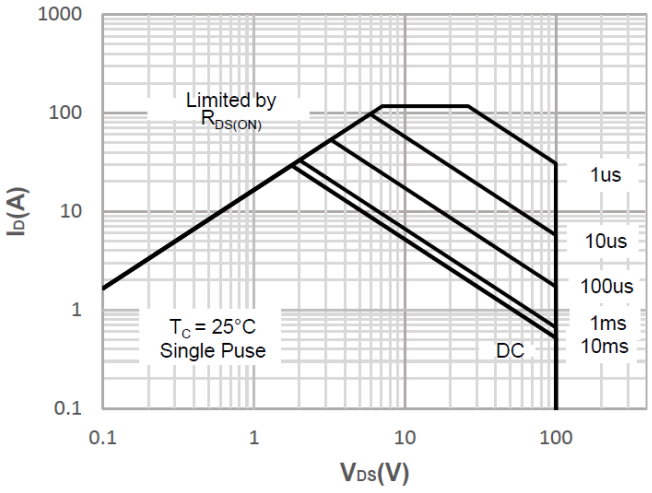
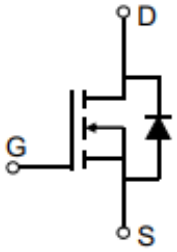
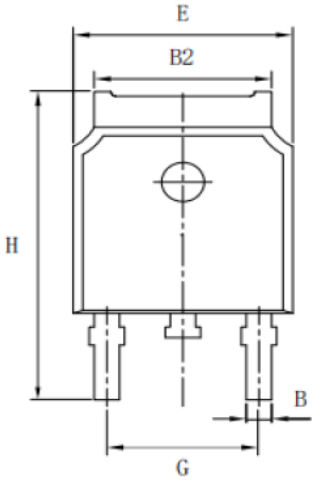


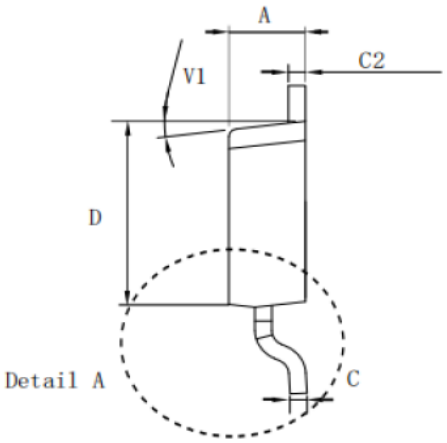
Figure 15. Maximum Safe Operating Area

·Circuit diagram

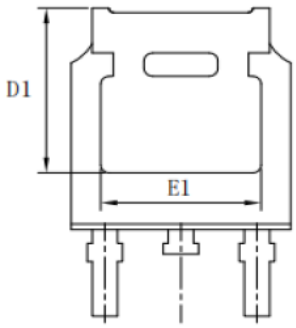


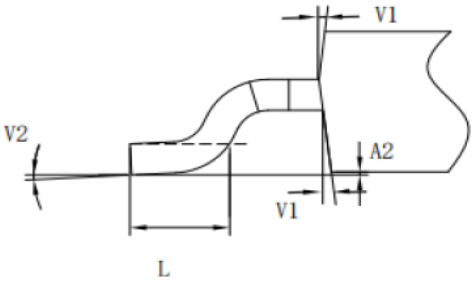
·Package outlines : Dimensions in (mm)





DIM.	MILLIMETER		
	MIN.	TYP.	MAX.
A	2.275	2.3	2.325
A2	0	0.05	0.1
B	0.72	0.76	0.85
B2	5.234	5.334	5.434
C	-	0.508	-
C2	0.507	0.508	0.517
D	6.075	6.1	6.125
D1	-	5.399	-
E	6.575	6.6	6.625
E1	-	4.826	-
G	4.472	4.572	4.672
H	9.9	10.1	10.3
L	1.4	1.5	1.6
V1	6°	7°	8°
V2	1°	3°	5°





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