

-20V, -15A P-Channel Power MOSFET

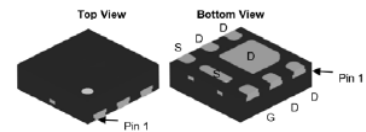
DESCRIPTION :

- Excellent $R_{DS(ON)}$
- Low Gate Charge
- 100% UIS Tested, 100% ΔV_{ds} Tested
- Pb-Free Lead Plating
- RoHS compliant

V_{DS}	-20V
$I_D (T_A=25^\circ C)$	-15A
$R_{DS(ON_Typ.) @ V_{GS}=-4.5V}$	15.1m Ω

TYPICAL APPLICATIONS :

- Power Management
- Load Switch
- PMW Application



DFN2020-6L

MAXIMUM RATINGS (at $T_A = 25^\circ C$, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	± 12	V
Continuous Drain Current	$T_A=25^\circ C$ $T_A=100^\circ C$	I_D	-15 -9.5	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	-60	A
Avalanche Energy ⁽²⁾		E_{AS}	25	mJ
Power dissipation	$T_A=25^\circ C$	P_D	2.6	W
Junction & Storage temperature Range		T_J, T_{STG}	-55~+150	$^\circ C$

Notes : 1. Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature.

2. E_{AS} Condition : Starting $T_J = 25^\circ C$, $L = 0.5mH$, $I_{AS} = -10A$, $V_G = -10V$, $V_{DD} = -15V$; $R_G=25ohm$

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient	$R_{\theta JA}$	49	$^\circ C/W$

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0\text{V}$, $I_D = -250\mu\text{A}$	$V_{(BR)DSS}$	-20			V
Zero Gate Voltage Drain Current $V_{DS} = -20\text{V}$, $V_{GS} = 0\text{V}$	I_{DSS}			-1	μA
Gate-Source Leakage Current $V_{GS} = \pm 12\text{V}$, $V_{DS} = 0\text{V}$	I_{GSS}			± 100	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}$, $I_D = -250\mu\text{A}$	$V_{GS(th)}$	-0.4	-0.8	-1.0	V
Drain-Source On-State Resistance $V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$ $V_{GS} = -2.5\text{V}$, $I_D = -5\text{A}$	$R_{DS(on)}$		11.6 15.9	15.1 20.7	m Ω
Input capacitance $f = 1\text{MHz}$, $V_{DS} = -10\text{V}$, $V_{GS} = 0\text{V}$	C_{iss}		1797		pF
Output capacitance $f = 1\text{MHz}$, $V_{DS} = -10\text{V}$, $V_{GS} = 0\text{V}$	C_{oss}		213		pF
Reverse transfer capacitance $f = 1\text{MHz}$, $V_{DS} = -10\text{V}$, $V_{GS} = 0\text{V}$	C_{rss}		108		pF
Total Gate Charge $V_{DS} = -10\text{V}$, $I_D = -10\text{A}$, $V_{GS} = 0$ to -4.5V	Q_G		16		nC
Gate to Source Charge $V_{DS} = -10\text{V}$, $I_D = -10\text{A}$, $V_{GS} = 0$ to -4.5V	Q_{GS}		4		nC
Gate to Drain Charge $V_{DS} = -10\text{V}$, $I_D = -10\text{A}$, $V_{GS} = 0$ to -4.5V	Q_{GD}		3		nC
Turn-on delay time $V_{DD} = -10\text{V}$, $V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$, $R_{GEN} = 3\Omega$	$t_{d(ON)}$		8		ns
Rise time $V_{DD} = -10\text{V}$, $V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$, $R_{GEN} = 3\Omega$	t_r		35		ns
Turn-off delay time $V_{DD} = -10\text{V}$, $V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$, $R_{GEN} = 3\Omega$	$t_{d(OFF)}$		71		ns
Fall time $V_{DD} = -10\text{V}$, $V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$, $R_{GEN} = 3\Omega$	t_f		70		ns

Body Diode

ELECTRICAL CHARACTERISTICS (at $T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Diode Forward Voltage $V_{GS} = 0V, I_S = -15A$	V_{SD}			-1.2	V
Maximum Continuous Body Diode Forward Current	I_S			-15	A
Maximum Pulsed Body Diode Forward Current	I_{SM}			-60	A
Revers Recovery Time $I_F = -10A, di/dt = 100A/\mu s$	T_{rr}		10		ns
Revers Recovery Charge $I_F = -10A, di/dt = 100A/\mu s$	Q_{rr}		3		nC

Typical Performance Characteristics

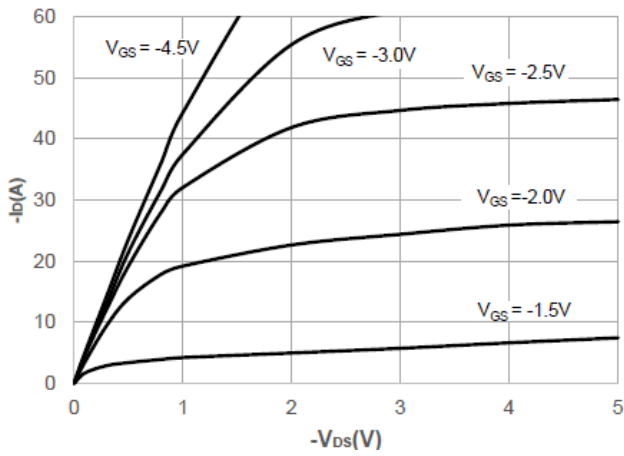


Figure 1. Output Characteristics

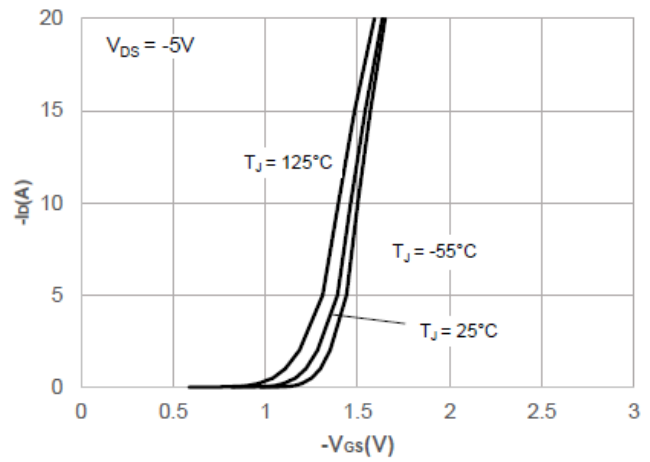


Figure 2. Transfer Characteristics

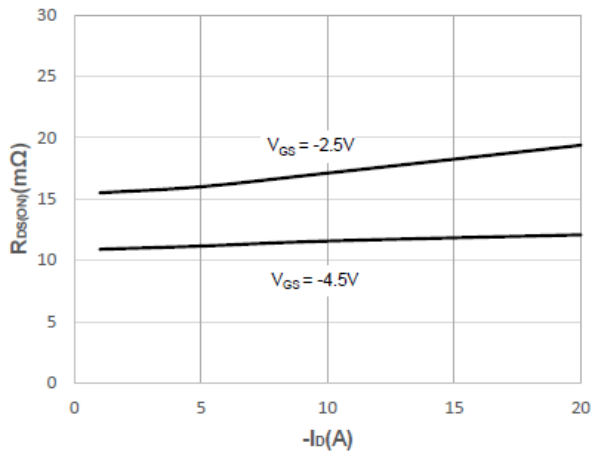


Figure 3. On-resistance vs. Drain Current

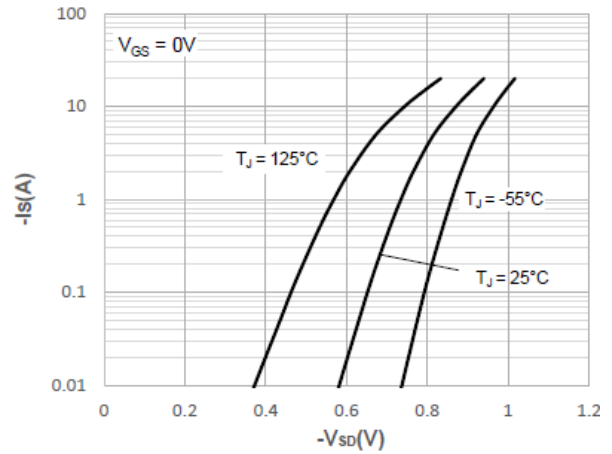


Figure 4. Body-Diode Characteristics

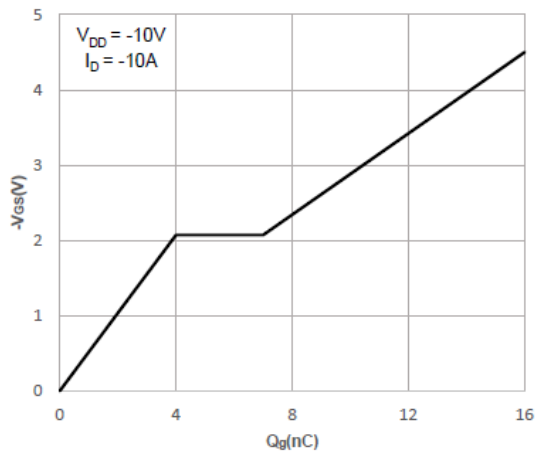


Figure 5. Gate Charge Characteristics

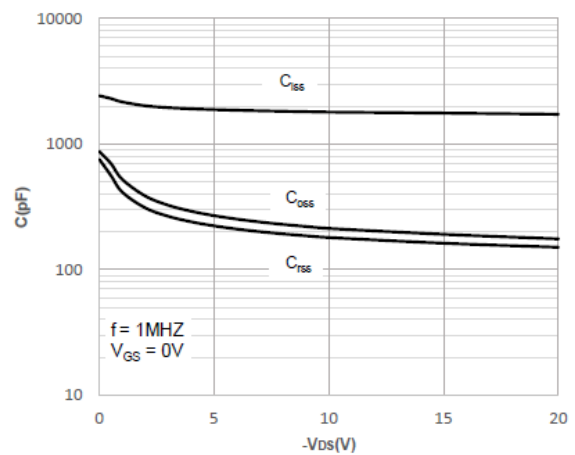


Figure 6. Capacitance Characteristics

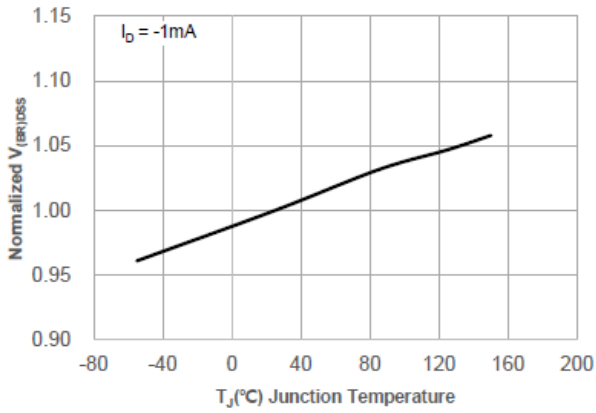


Figure 7. Normalized Breakdown voltage vs. Junction Temperature

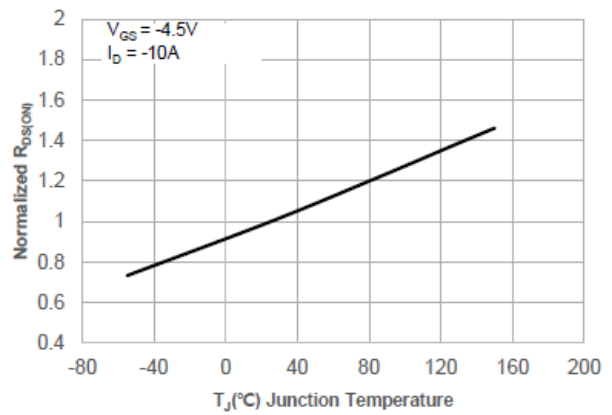


Figure 8. Normalized on Resistance vs. Junction Temperature

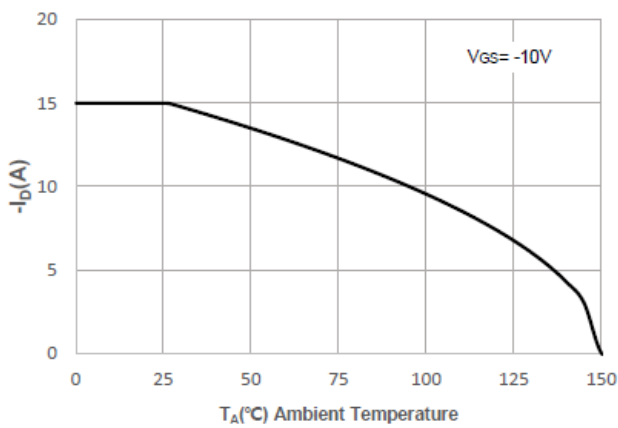


Figure 9. Maximum Continuous Drian Current vs. Case Temperature

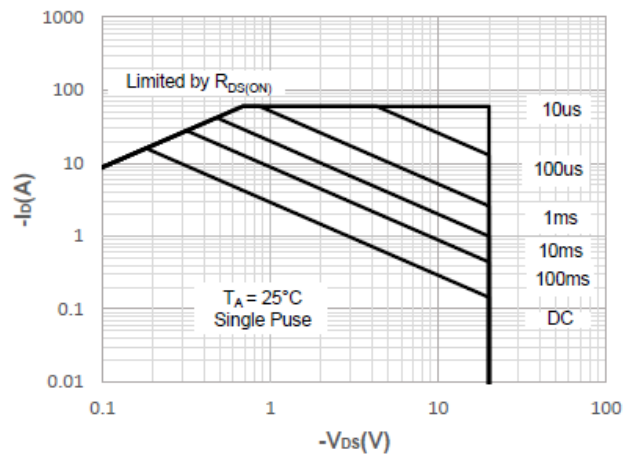


Figure 10. Maximum Safe Operating Area

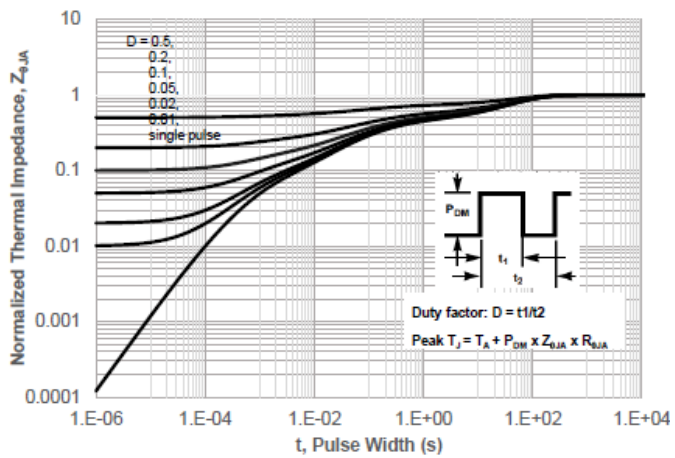


Figure 11. Normalized Maximum Transient Thermal Impedance

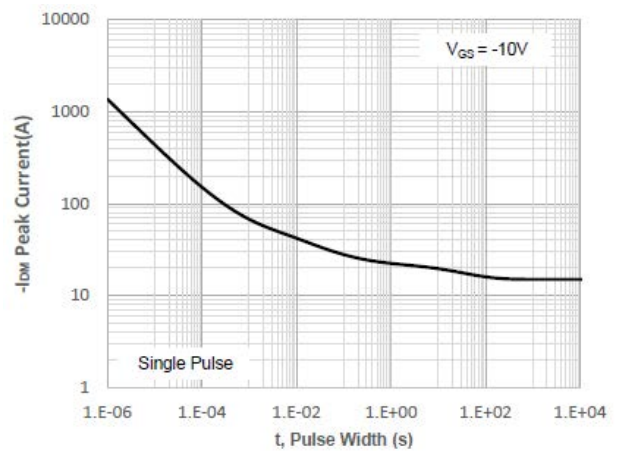
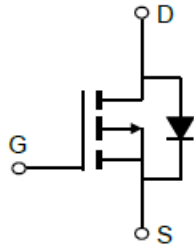
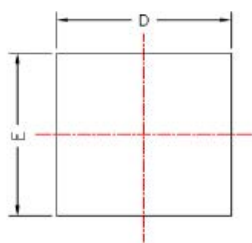


Figure 12. Peak Current Capacity

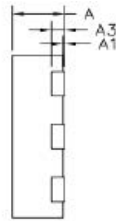
• Circuit diagram



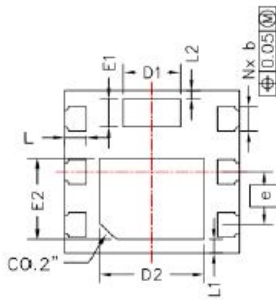
• Package outlines :



Top View



Side View



Bottom View

SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	---	---	0.050	----	----	0.002
A3	0.195	0.203	0.211	0.008	0.008	0.008
b	0.250	0.300	0.350	0.010	0.012	0.014
e	0.65BSC			0.026 BSC		
D	1.900	2.000	2.100	0.075	0.079	0.083
E	1.900	2.000	2.100	0.075	0.079	0.083
D1	0.560	0.660	0.760	0.022	0.026	0.030
E1	0.250	0.350	0.450	0.010	0.014	0.018
D2	1.100	1.200	1.300	0.043	0.047	0.051
E2	0.900	1.000	1.100	0.035	0.039	0.043
L	0.150	0.250	0.350	0.006	0.010	0.014
L1	0.065	0.165	0.265	0.003	0.006	0.010
L2	0.000	0.100	0.200	0.000	0.004	0.008

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