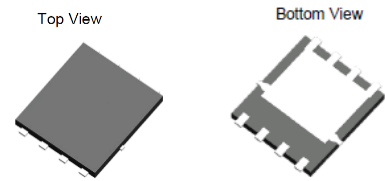


## 60V N-Channel Power MOSFET

### DESCRIPTION :

- Ultra-low ON-resistance,  $R_{DS(ON)}$
- Low Gate Charge
- 100% UIS Tested, 100% Rg Tested
- Halogen-free and RoHS compliant
- Pb-Free Lead Plating
- AEC-Q101 Qualified

$V_{DS}$	60V
$I_D @ V_{GS}=10V$	103A
$R_{DS(ON)}_{Typ.} @ V_{GS}=10V$	4.0m $\Omega$



PDFN5x6-8L

### MAXIMUM RATINGS (at $T_A = 25^\circ\text{C}$ , unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$	$I_D$	103 73	A
Pulsed Drain Current <sup>(1)</sup>		$I_{DM}$	412	A
Avalanche Current <sup>(2)</sup>		$I_{AS}$	25	A
Avalanche Energy <sup>(2)</sup>		$E_{AS}$	94	mJ
Power dissipation	$T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$	$P_D$	94 47	W
Junction & Storage temperature Range		$T_J, T_{STG}$	-55~+150	$^\circ\text{C}$

Notes : 1. This single-pulse measurement was taken under  $T_{J\_Max} = 175^\circ\text{C}$ ..

2. This single-pulse measurement was taken under the following condition [ $L = 300\mu\text{H}$ ,  $V_{GS} = 10\text{V}$ ,  $V_{DD} = 30\text{V}$ ] while its value is limited by  $T_J \text{ Max} = 175^\circ\text{C}$ .

### THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal resistance,	Junction to Ambient Junction to Case	$R_{\theta JA}$ $R_{\theta JC}$	52 1.6	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS (at  $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0V, I_D = 250\mu A$	$V_{(BR)DSS}$	60			V
Zero Gate Voltage Drain Current $V_{DS} = 48V, V_{GS} = 0V$ ( $T_J = 25\text{ }^\circ\text{C} / T_J = 55\text{ }^\circ\text{C}$ )	$I_{DSS}$			1.0/5.0	$\mu A$
Gate-Source Leakage Current $V_{GS} = \pm 20V, V_{DS} = 0V$	$I_{GSS}$			$\pm 100$	nA
Gate-Source threshold voltage $V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(th)}$	1.2	1.6	2.5	V
Drain-Source On-State Resistance $V_{GS} = 10V, I_D = 20A$ $V_{GS} = 4.5V, I_D = 15A$	$R_{DS(on)}$		4.0 5.2	5.0 6.5	m $\Omega$
Forward Transconductance $V_{DS} = 5V, I_D = 20A$	$G_{FS}$		95		S
Input capacitance $f = 1MHz, V_{DS} = 30V, V_{GS} = 0V$	$C_{iss}$		2030		pF
Output capacitance $f = 1MHz, V_{DS} = 30V, V_{GS} = 0V$	$C_{oss}$		445		pF
Reverse transfer capacitance $f = 1MHz, V_{DS} = 30V, V_{GS} = 0V$	$C_{rss}$		4.4		pF
Gate Resistance $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	$R_g$		1.8		$\Omega$
Total Gate Charge $V_{DS} = 30V, I_D = 20A, V_{GS} = 10V$ $V_{DS} = 30V, I_D = 20A, V_{GS} = 4.5V$	$Q_G$		32 14.9		nC
Gate to Source Charge $V_{DS} = 30V, I_D = 20A, V_{GS} = 0$ to 10V	$Q_{GS}$		4.4		nC
Gate to Drain Charge $V_{DS} = 30V, I_D = 20A, V_{GS} = 0$ to 10V	$Q_{GD}$		4.9		nC
Turn-on delay time $V_{DS} = 30V, V_{GS} = 10V, R_L = 1.5\Omega, R_{GEN} = 6\Omega$	$t_{d(ON)}$		6.3		ns
Rise time $V_{DS} = 30V, V_{GS} = 10V, R_L = 1.5\Omega, R_{GEN} = 6\Omega$	$t_r$		7.8		ns
Turn-off delay time $V_{DS} = 30V, V_{GS} = 10V, R_L = 1.5\Omega, R_{GEN} = 6\Omega$	$t_{d(OFF)}$		39		ns
Fall time $V_{DS} = 30V, V_{GS} = 10V, R_L = 1.5\Omega, R_{GEN} = 6\Omega$	$t_f$		15.5		ns

## Body Diode

ELECTRICAL CHARACTERISTICS (at  $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Diode Forward Voltage $V_{GS} = 0V, I_S = 1A$	$V_{SD}$		0.70	1.0	V
Diode Continuous Current, $T_c=25^\circ\text{C}$	$I_S$			103	A
Revers Recovery Time $I_F=20A, dI_F/dt = 100A/us$	$T_{rr}$		39		ns
Revers Recovery Charge $I_F=20A, dI_F/dt = 100A/us$	$Q_{rr}$		45		nC

Typical Performance Characteristics

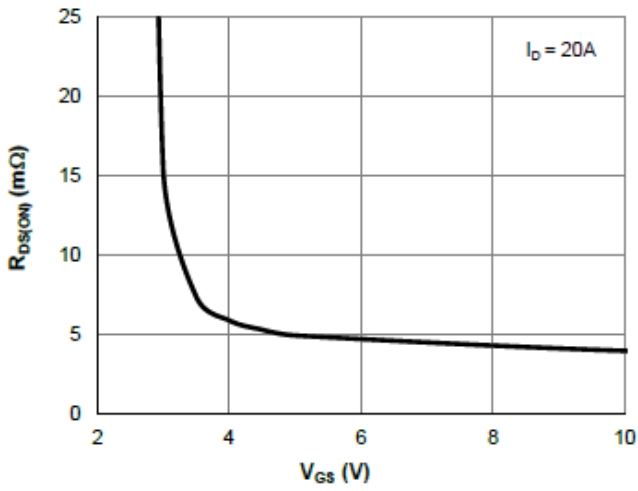


Figure 1.  $R_{DS(ON)}$  vs.  $V_{GS}$

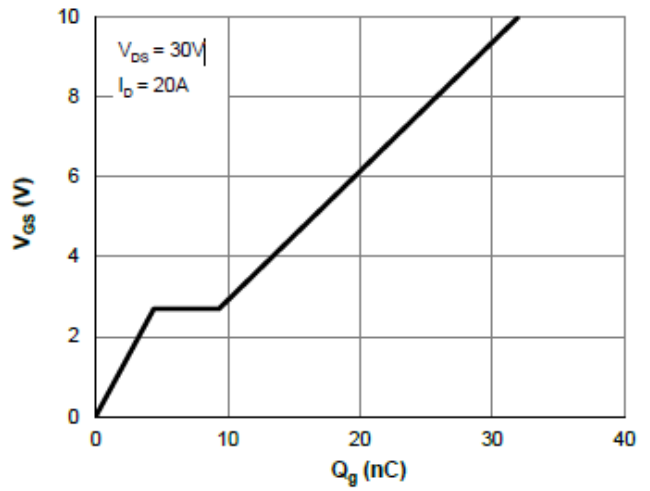


Figure 2. Gate Charge Characteristics

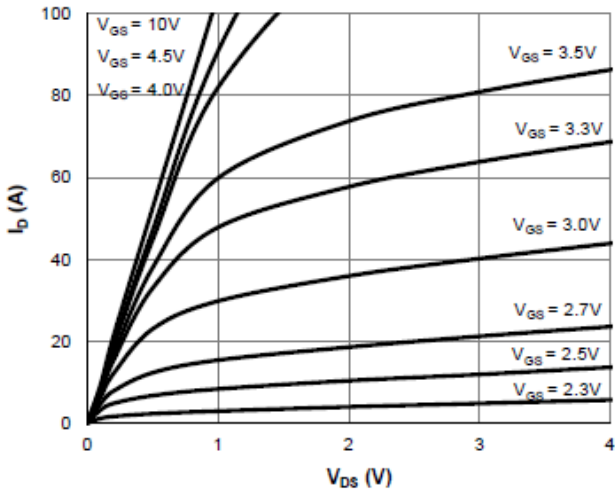


Figure 3. Saturation Characteristics

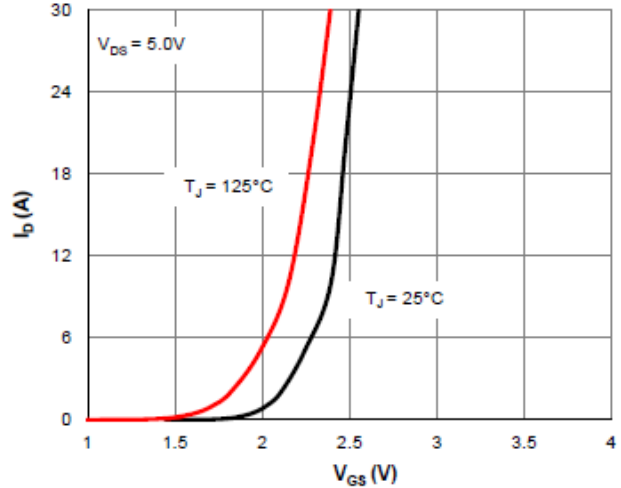


Figure 4. Transfer Characteristics

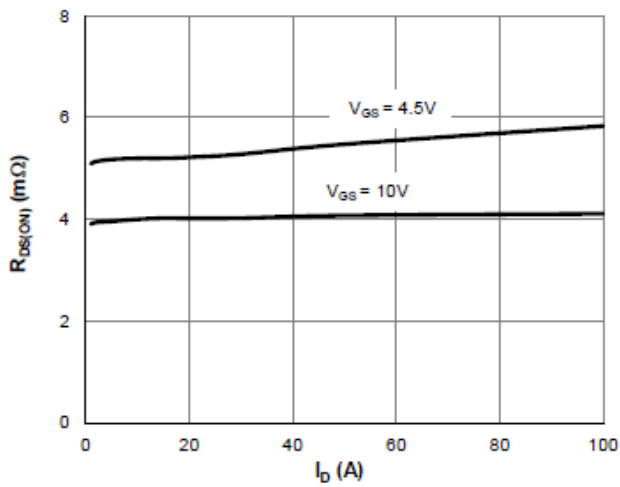


Figure 5.  $R_{DS(ON)}$  vs. Drain Current

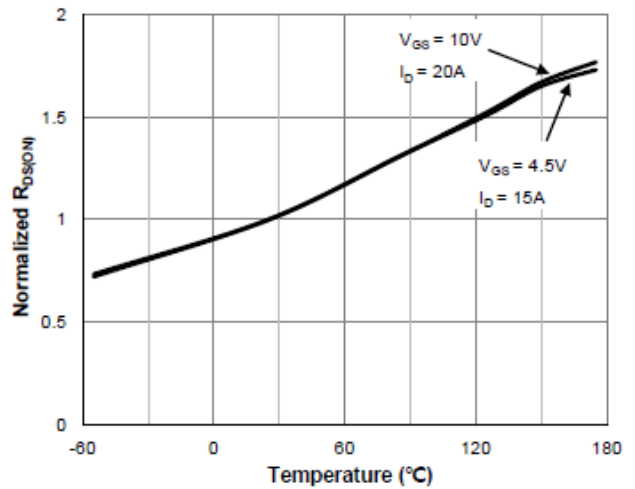


Figure 6.  $R_{DS(ON)}$  vs. Junction Temperature

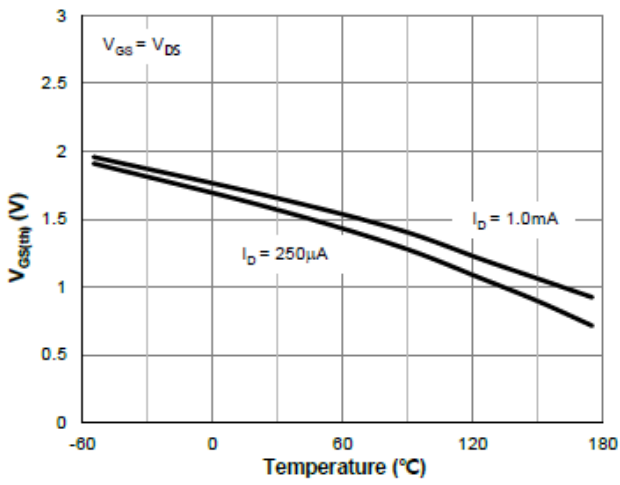


Figure 7.  $V_{GS(th)}$  vs. Junction Temperature

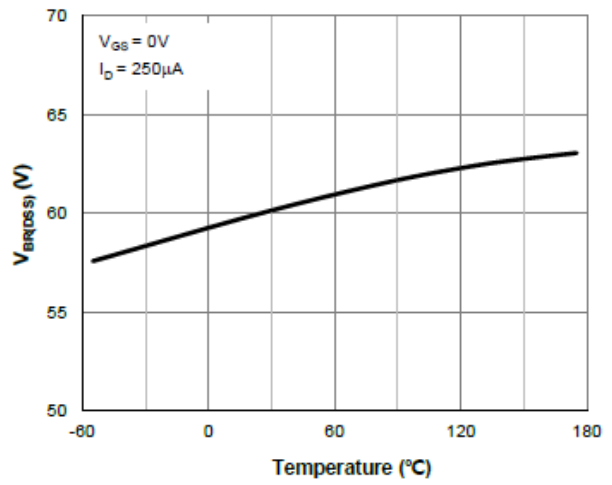


Figure 8.  $V_{BR(DSS)}$  vs. Junction Temperature

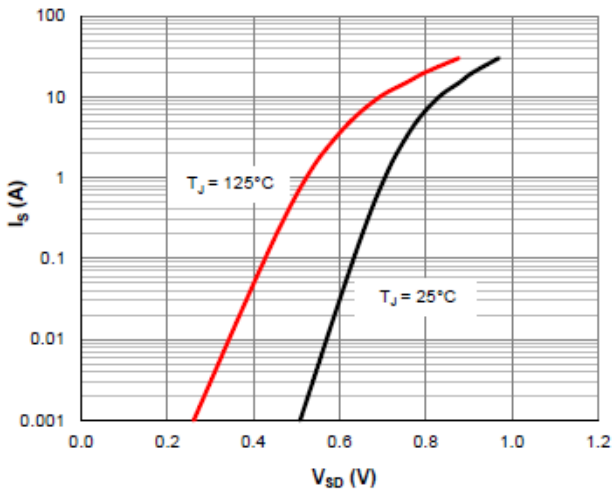


Figure 9. Body-Diode Characteristics

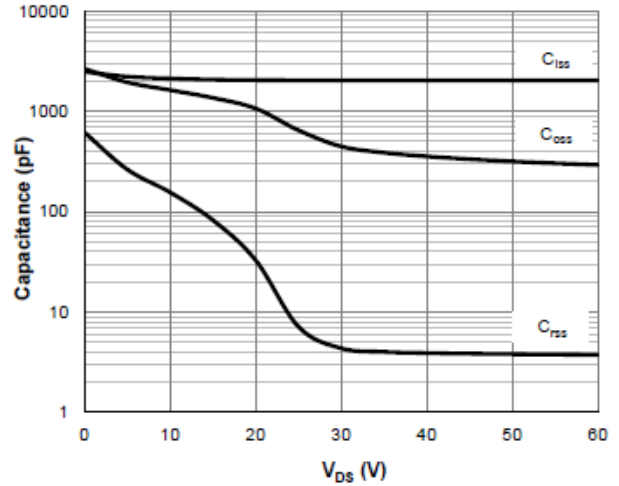


Figure 10. Capacitance Characteristics

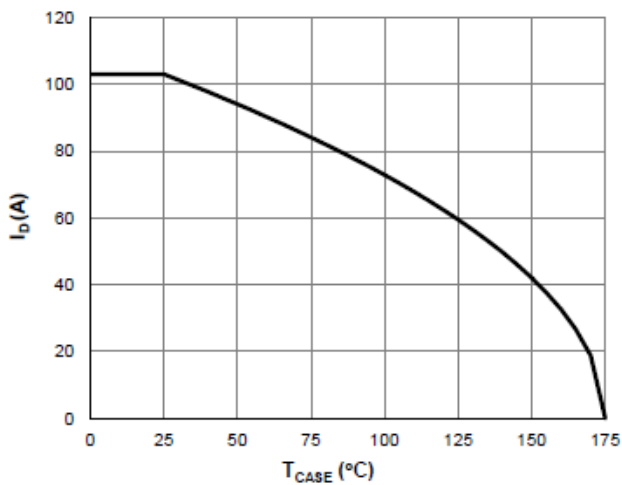


Figure 11. Current De-rating

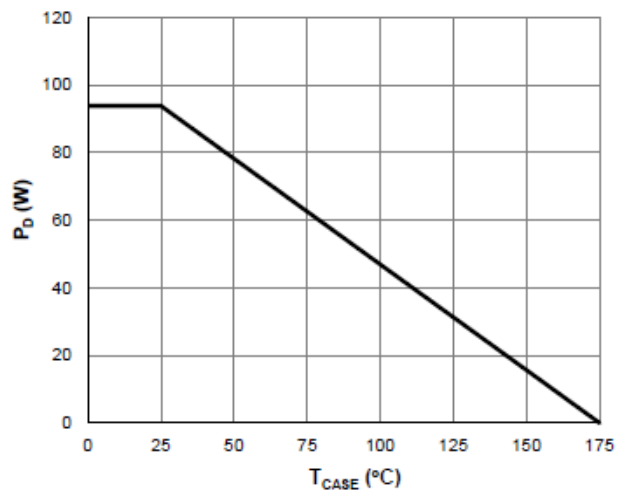


Figure 12. Power De-rating

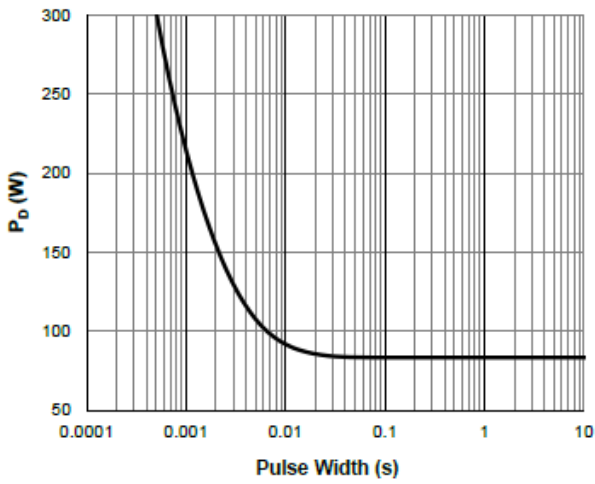


Figure 13. Single Pulse Power Rating, Junction-to-Case

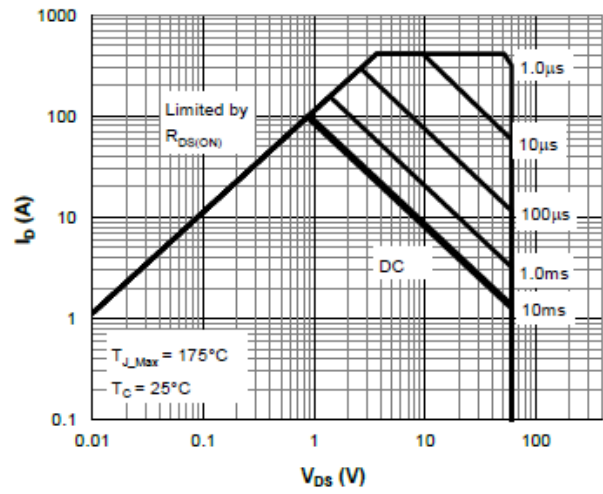


Figure 14. Maximum Safe Operating Area

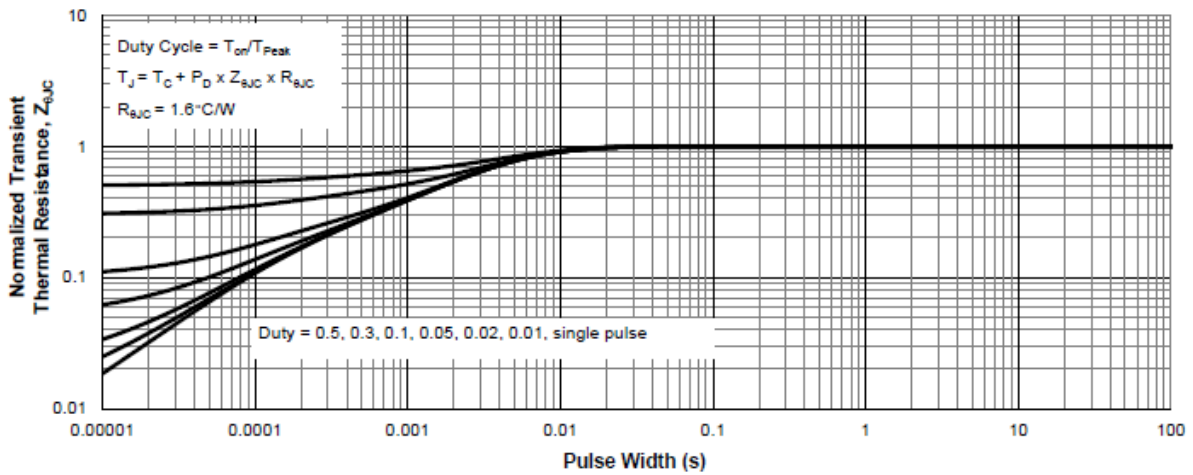
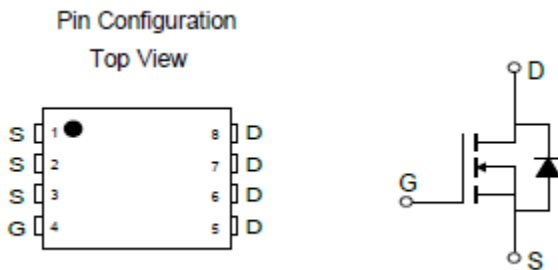
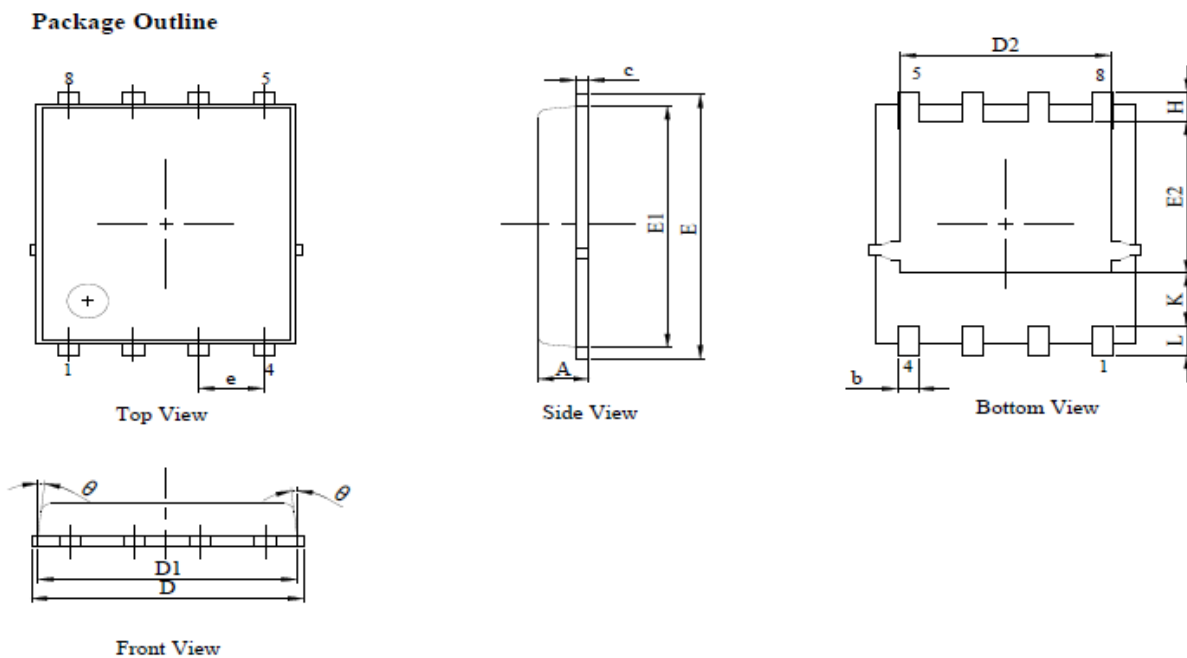


Figure 15. Normalized Maximum Transient Thermal Impedance

• Circuit diagram



• Package outlines : Dimensions in (mm)



NOTES:

1. Dimension and tolerance per ASME Y14.5M, 1994.
2. All dimensions in millimeter (angle in degree).
3. Dimensions D1 and E1 do not include mold flash protrusions or gate burrs.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.31	0.41	0.51
c	0.20	0.25	0.30
D	5.00	5.20	5.40
D1	4.95	5.05	5.15
D2	4.00	4.10	4.20
E	6.05	6.15	6.25
E1	5.50	5.60	5.70
E2	3.42	3.53	3.63
e	1.27BSC		
H	0.60	0.70	0.80
L	0.50	0.70	0.80
K	1.23 REF		
θ	-	-	10°

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