

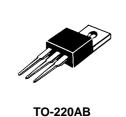
UF20C60

Utra Fast Recovery Rectifier Diodes

Designed for use in switching power supplies. inverters and as free wheeling diodes. These state-of-the-art devices have the following features:

- $\star\,\text{Low}\,T_{\text{RR}}$
- * High Surge Capacity
- * Low Power Loss, High efficiency
- * 175 Operating Junction Temperature
- * Low Forward Voltage , High Frequency
- * High-Switching Speed 21(typ.) Nanosecond Recovery Time
- * Plastic Material used Carries Underwriters Laboratory

* In compliance with EU RoHs 2002/95/EC directives



ULTRA FAST RECTIFIERS

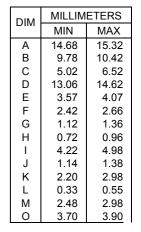
20 AMPERES 600 VOLTS

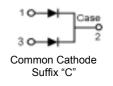
MAXIMUM	RATINGS
	INATINOU.

Characteristic	Symbol	UF20C60	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	600	V
RMS Reverse Voltage	V _{R(RMS)}	420	V
Average Rectifier Forward Current (per diode) Total Device (Rated V _R),T _C =55	I _{F(AV)}	10 20	А
Peak Repetitive Forward Current (Rate V _R , Square Wave, 20kHz,T _C =125)	I _{FM}	20	А
Non-Repetitive Peak Surge Current (Surge applied at rate load conditions half-wave, single phase, 60Hz)	I _{FSM}	175	А
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +175	

ELECTRIAL CHARACTERISTICS

Characteristic	Symbol	Min	TYPE	MAX.	Unit
Maximum Instantaneous Forward Voltage ($I_F = 10 \text{ Amp } T_C = 25$) ($I_F = 10 \text{ Amp } T_C = 125$)	V _F		2.2 1.95	2.5 2.3	V
Maximum Instantaneous Reverse Current (Rated DC Voltage, $T_C = 25$) (Rated DC Voltage, $T_C = 125$)	I _R			25 10	uA mA
Reverse Recovery Time (I _F = 0.5 A, I _R =1.0,I _{rr} =0.25 A)	Trr		20	25	ns
Typical Thermal Resistance junction to case	R _{θ j-c}		3.6		/w





UF20C60

FIG-1 TYPICAL FORWARD CHARACTERISITICS

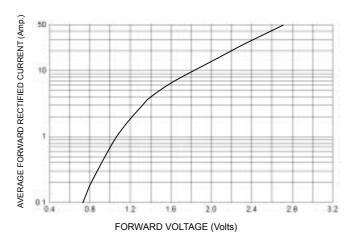
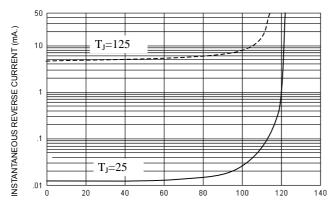
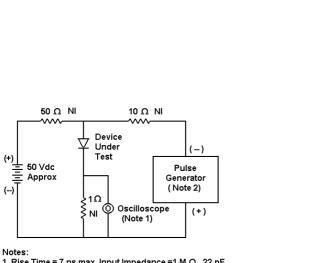


FIG-2 TYPICAL REVERSE CHARACTERISTICS



PERCENT OF PEAK REVERSE VOLTAGE (%)



1. Rise Time = 7 ns max. Input Impedance =1 M Ω , 22 pF 2. Rise Time = 10 ns max. Input Impedance = 50 Ω

(+)

(--)

FIG-3 FORWARD CURRENT DERATING CURVE

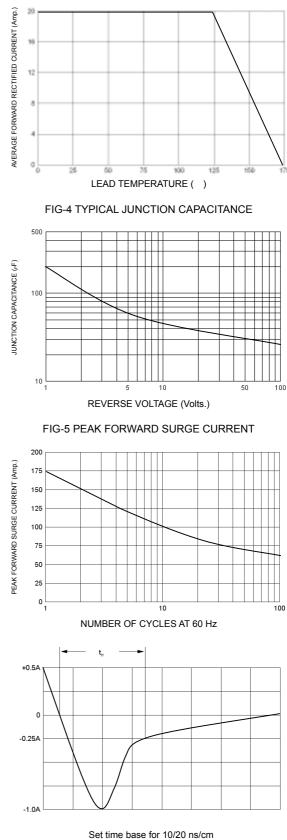


FIG-6 Reverse Recovery Time Characteristic and Test Circuit Diagram



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