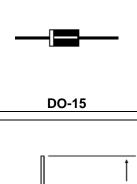
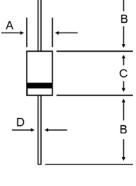


SF21 Thru SF26

Switchmode Power Rectifiers ULTRAFAST RECTIFIERS Designed for use in switching power supplies. inverters and as free wheeling diodes. These state-of-the-art devices have the following 2.0 AMPERES features: 50-400 VOLTS * High Surge Capacity *Low Power Loss, High efficiency * Glass Passivated chip junctions *150°C Operating Junction Temperature * Low Stored Charge Majority Carrier Conduction *Low Forward Voltage, High Current Capability * Ultrafast 35 & 50 Nanosecond Recovery Time * Plastic Material used Carries Underwriters Laboratory Flammability Classification 94V-O DO-15 * In compliance with EU RoHs 2002/95/EC directives **MAXIMUM RATINGS** SF Characteristic Symbol Unit 21 22 23 24 25 26 Peak Repetitive Reverse Voltage V_{RRM} V_{RWM} 50 100 150 200 300 400 V Working Peak Reverse Voltage **DC Blocking Voltage** V_{R50} **RMS Reverse Voltage** VR_(RMS) 35 70 105 140 210 280 V D Average Rectifier Forward Current 2.0 А lo Non-Repetitive Peak Surge Current (Surge applied at rate load conditions I_{FSM} 50 35 А halfware, single phase,60Hz) **Operating and Storage Junction** -65 to +150 °C T_J , T_{STG} **Temperature Range ELECTRIAL CHARACTERISTICS**

Characteristic	Symbol	SF						Unit
		21	22	23	24	25	26	Unit
Maximum Instantaneous Forward Voltage (I _F =2.0 Amp, T _C = 25 $^{\circ}$ C)	V _F	0.95				1.30		V
Maximum Instantaneous Reverse Current (Rated DC Voltage, $T_c = 25^{\circ}C$) (Rated DC Voltage, $T_c = 125^{\circ}C$)	I _R			5 5			uA	
Reverse Recovery Time ($I_F = 0.5 \text{ A}$, $I_R = 1.0$, $I_{rr} = 0.25 \text{ A}$)	Trr	35		50		ns		
Typical Junction Capacitance (Reverse Voltage of 4 volts & f=1 MHz)	CP	40		3	5	₽F		





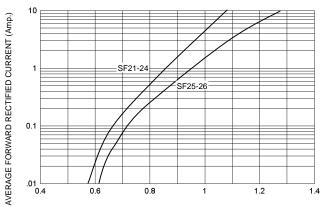
DIM	MILLIMETERS					
DIM	MIN	MAX				
А	2.60	3.60				
В	25.40					
С	5.80	7.60				
D	0.70	0.90				

CASE
Transfer molded
plastic

POLARITY---Cathode indicated polarity band

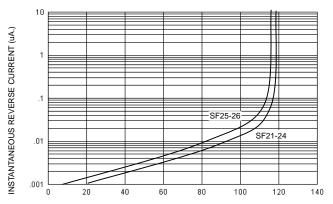
SF21 Thru SF26

FIG-1 TYPICAL FORWARD CHARACTERISITICS

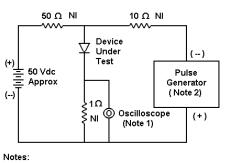


FORWARD VOLTAGE (Volts)





PERCENT OF PEAK REVERSE VOLTAGE (%)



1. Rise Time = 7 ns max. Input Impedance =1 M Ω , 22 pF 2. Rise Time = 10 ns max. Input Impedance = 50 Ω

FIG-3 FORWARD CURRENT DERATING CURVE

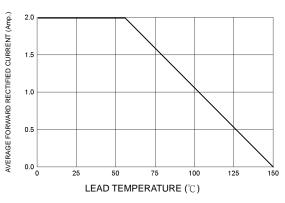


FIG-4 TYPICAL JUNCTION CAPACITANCE

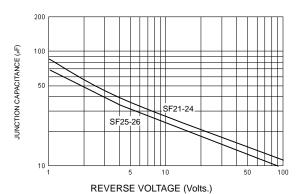
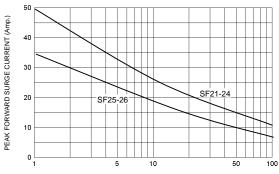
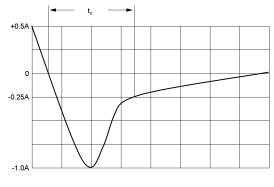


FIG-5 PEAK FORWARD SURGE CURRENT



NUMBER OF CYCLES AT 60 Hz



Set time base for 10/20 ns/cm FIG-6 Reverse Recovery Time Characteristic and Test Circuit Diagram



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