

650V Silicon Carbide Schottky Diode

DESCRIPTION:

- Positive temperature Coefficient
- · High Speed Switching
- Negligible reverse recovery
- Temperature Independent Switching
- RoHS Compliant

TYPICAL	APPL	ICAT	IONS	:
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- Uninterruptible power supplies (UPS)
- · Data Center
- · Switch mode power supplies
- · Solar inverters

V_{RRM}	650V	
I _F	10A (TC=135°C)	
Qc	30nC	



ITO-220AC

MAXIMUM RATINGS (at T_C = 25 °C, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Repetitive Peak Reverse Voltage		V_{RRM}	650	V
Continuous Forward Current	Tc=25°C Tc=135°C	I _F	21 10	А
Non-Repetitive Forward Surge Current	Tc=25°C , t_P =10ms, Half sine pulse Tc=110°C , t_P =10ms, Half sine pulse	I _{FSM}	92 86	А
Repetitive Peak Forward Surge Current	Tc=25°C , t_P =10ms, Half sine pulse	I _{FRM}	90	А
i ² t value	Tc=25 $^{\circ}$ C , t _P =10ms Tc=110 $^{\circ}$ C , t _P =10ms	∫ i ² dt	42 37	A ² S
Power dissipation	Tc=25°C Tc=110°C Tc=150°C	P tot	66 28 10	W
Operation Junction temperature		Tj	-55~+175	$^{\circ}\!\mathbb{C}$
Storage temperature		T _{STG}	-55~+175	$^{\circ}\!\mathbb{C}$

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Typical	Unit
Thermal resistance, junction - case		$R_{th(j-C)}$	2.25	°C/W

ELECTRICAL CHARATERISTICS (at $T_C = 25$ °C, unless otherwise specified)

Characteristic	Symbol	Min.	Тур.	Max.	Unit
DC Blocking Voltage	V _{DC}	650			V
Forward Voltage IF = 5A IF = 10A, Tc =25°C IF = 10A, Tc =175°C	V _F		1.17 1.37 1.66	1.6	V
Reverse Current VR = 650V, Tc =25 $^{\circ}$ C VR = 650V, Tc =175 $^{\circ}$ C	I _R		5 12	60	uA
Total Capacitive Charge VR = 400V	Q _C		30		nC
Total capacitance VR = 1V, f =1MHz VR = 200V, f =1MHz VR = 400V, f =1MHz	С		455 57 56		pF
Capacitance Stored Energy VR = 400 V	Ec		4.8		uJ

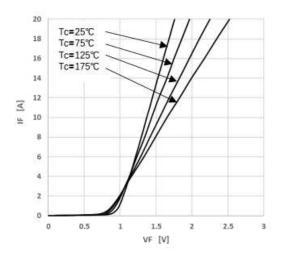


Figure 1. Forward characteristics

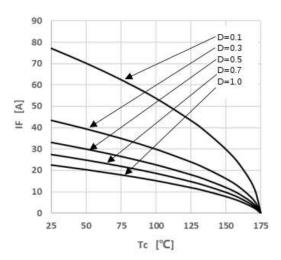


Figure 3. Peak Forward Current Derating

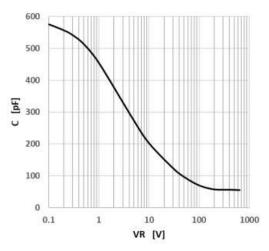


Figure 5. Capacitance vs. Reverse Voltage

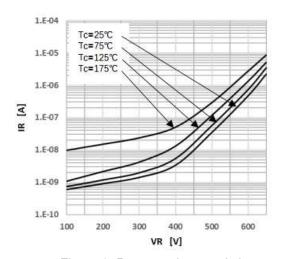


Figure 2. Reverse characteristics

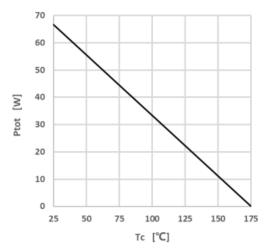


Figure 4. Power Dissipation

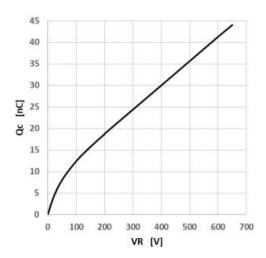


Figure 6. Capacitance Charge vs. Reverse Voltage

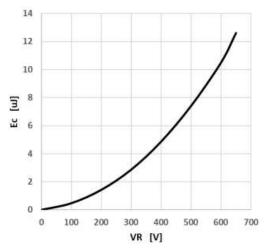


Figure 7. Capacitance Stored Energy

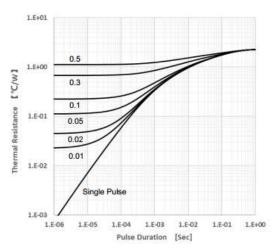
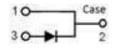
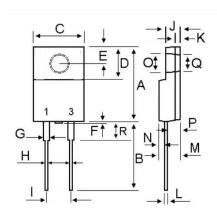


Figure 8. Transient Thermal Impedance

· Circuit diagram



• ITO-220AC Package outlines : Dimensions in (mm)



DIM	MILLIMETERS		
DIM	MIN	MAX	
Α	14.80	16.10	
A B C	12.65	14.40	
O	9.70	10.36	
D	4.60	6.80	
E	2.50	3.50	
F		2.00	
G	0.90	1.45	
Η	0.50	0.90	
	4.80	5.40	
J	2.34	3.30	
K	0.55	1.30	
L	0.36	0.80	
М	4.20	4.90	
N	1.10	1.80	
0	2.90	3.50	
Р	2.30	3.15	
Q	2.90	3.50	
R	2.80	4.85	



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