

650V Silicon Carbide Schottky Diode

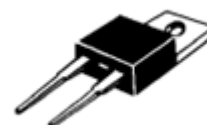
DESCRIPTION :

- Negligible reverse recovery
- High Surge Current
- Positive temperature Coefficient
- High frequency
- RoHS Compliant

V_{RRM}	650V
I_F	10A (TC=154°C)
Q_C	30nC

TYPICAL APPLICATIONS :

- Uninterruptible power supplies (UPS)
- Data Center
- Switch mode power supplies (SMPS)
- Solar inverter



TO-220AC

MAXIMUM RATINGS (at $T_C = 25^\circ\text{C}$, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Repetitive Peak Reverse Voltage		V_{RRM}	650	V
Continuous Forward Current	$T_C=25^\circ\text{C}$ $T_C=135^\circ\text{C}$ $T_C=154^\circ\text{C}$	I_F	32 15 10	A
Non-Repetitive Forward Surge Current	$T_C=25^\circ\text{C}$, $t_P=10\text{ms}$, Half sine pulse $T_C=110^\circ\text{C}$, $t_P=10\text{ms}$, Half sine pulse	I_{FSM}	96 83	A
Repetitive Peak Forward Surge Current	$T_C=25^\circ\text{C}$, $t_P=10\text{ms}$, Half sine pulse	I_{FRM}	85	A
i^2t value	$T_C=25^\circ\text{C}$, $t_P=10\text{ms}$ $T_C=110^\circ\text{C}$, $t_P=10\text{ms}$	$\int i^2 dt$	60.5 44	A^2S
Power dissipation	$T_C=25^\circ\text{C}$ $T_C=110^\circ\text{C}$ $T_C=150^\circ\text{C}$	P_{tot}	127 55 21	W
Operation Junction temperature		T_J	-55~+175	$^\circ\text{C}$
Storage temperature		T_{STG}	-55~+175	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Typical	Unit
Thermal resistance, junction - case		$R_{th(j-c)}$	1.175	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS (at $T_c = 25^{\circ}\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
DC Blocking Voltage	V_{DC}	650			V
Forward Voltage IF = 5A IF = 10A, $T_c = 25^{\circ}\text{C}$ IF = 10A, $T_c = 175^{\circ}\text{C}$	V_F		1.17 1.37 1.66	1.6	V
Reverse Current VR = 650V, $T_c = 25^{\circ}\text{C}$ VR = 650V, $T_c = 175^{\circ}\text{C}$	I_R		5 12	60	μA
Total Capacitive Charge VR = 400V	Q_C		30		nC
Total capacitance VR = 1V, f = 1MHz VR = 200V, f = 1MHz VR = 400V, f = 1MHz	C		455 57 56		pF
Capacitance Stored Energy VR = 400 V	E_C		4.8		μJ

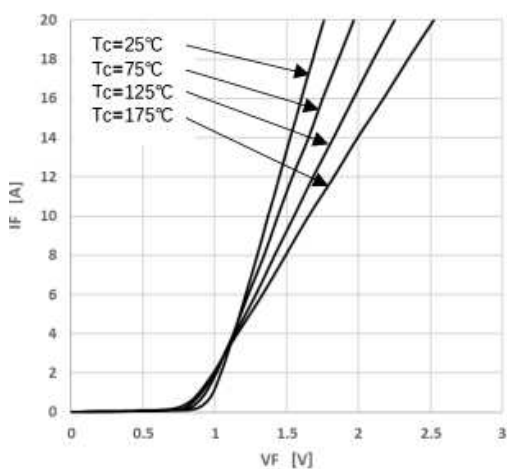


Figure 1. Forward characteristics

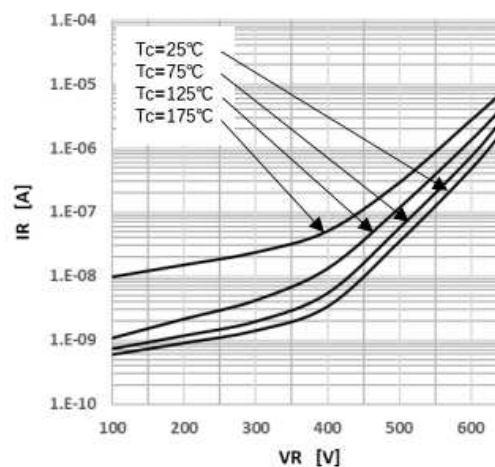


Figure 2. Reverse characteristics

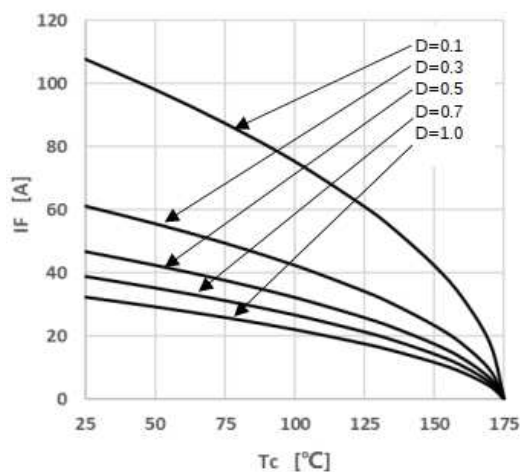


Figure 3. Peak Forward Current Derating

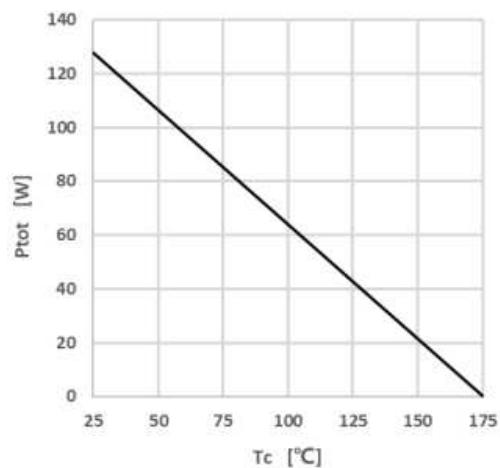


Figure 4. Power Dissipation

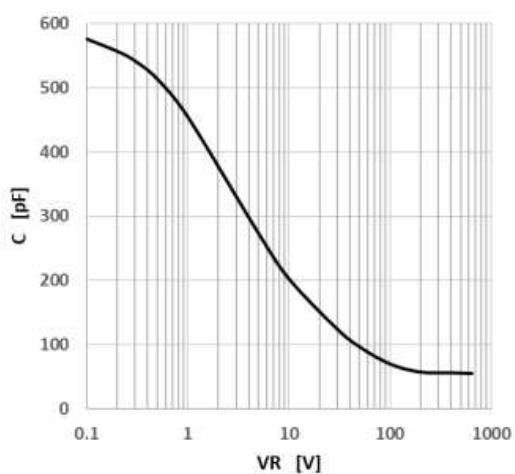


Figure 5. Capacitance vs. Reverse Voltage

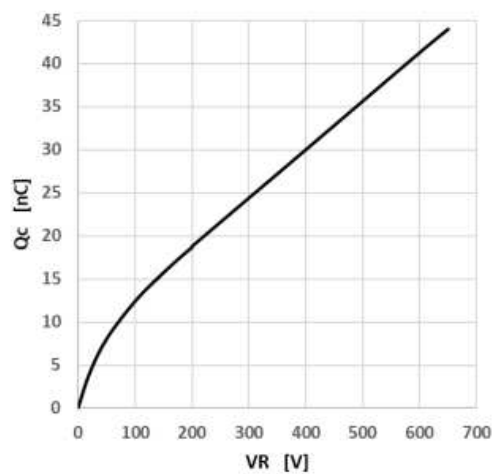


Figure 6. Capacitance Charge vs. Reverse Voltage

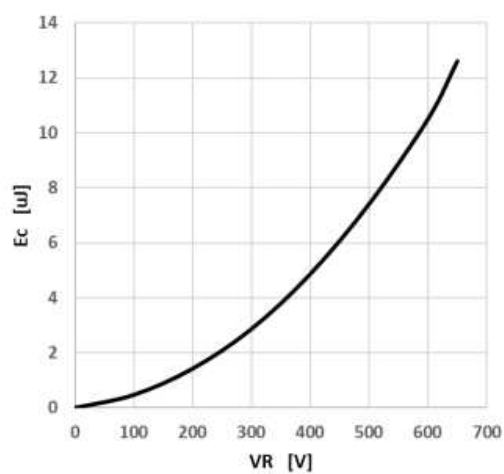


Figure 7. Capacitance Stored Energy

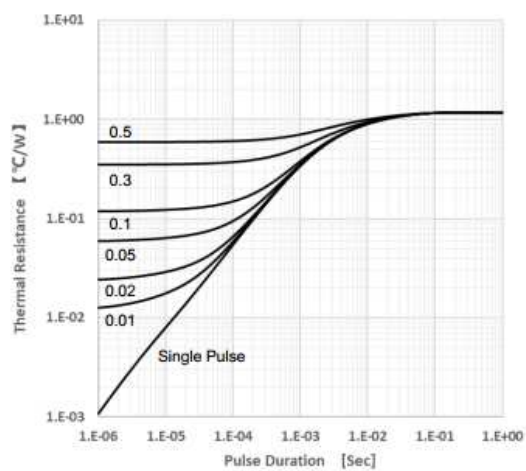
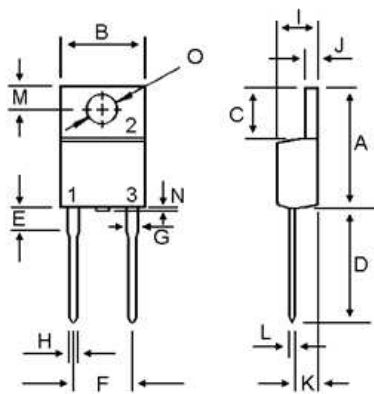


Figure 8. Transient Thermal Impedance

- Circuit diagram



- TO-220AC Package outlines : Dimensions in (mm)



DIM	MILLIMETERS	
	MIN	MAX
A	14.68	16.00
B	9.78	10.42
C	5.02	6.60
D	13.00	14.62
E	3.10	4.19
F	4.82	5.34
G	1.10	1.67
H	0.69	1.01
I	4.22	4.98
J	1.14	1.40
K	2.20	3.30
L	0.28	0.61
M	2.48	3.00
N	---	2.00
O	3.50	4.00

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