

# COMPLEMENTARY SILICON POWER DARLINGTON TRANSISTORS

..designed for use as output devices in complementary general purpose amplifier applications.

#### **FEATURES:**

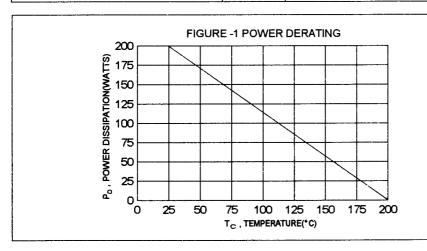
- \* High Gain Darlington Performance
- \* High DC Current Gain hFE = 1000(Min) @ I<sub>C</sub> = 20 A
- \* Monolithic Construction with Built-in Base-Emitter Shunt Resistor

# **MAXIMUM RATINGS**

Characteristic	Symbol	MJ11011 MJ11012	MJ11013 MJ11014	MJ11015 MJ11016	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	60	90	120	٧
COllector-Base Voltage	V <sub>сво</sub>	60	90	120	<b>v</b>
Emitter-Base Voltage	V <sub>EBO</sub>	5.0		>	
Collector Current-Continuous -Peak	I <sub>C</sub>	30 50		Α	
Base Current	l <sub>B</sub>	1.0		Α	
Total Power Dissipation <b>@</b> T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	200 1.15		W/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	- 65 to +200		°C	

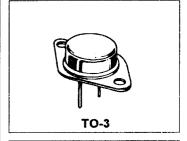
#### THERMAL CHARACTERISTICS

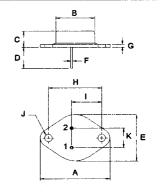
Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	0.87	°C/W



PNP NPN MJ11011 MJ11012 MJ11013 MJ11014 MJ11015 MJ11016

30 AMPERE
COMPLEMENTARY
SILICON POWER
DARLINGTON TRANSISTOR
60-120 VOLTS
200 WATTS





PIN 1.BASE 2.EMITTER COLLECTOR(CASE)

DIM	MILLIMETERS			
Dilvi	MIN	MAX		
Α	38.75	39.96		
В	19.28	22.23		
С	7.96	9.28		
D	11.18	12.19		
E	25.20	26.67		
F	0.92	1.09		
G	1.38	1.62		
l H	29.90	.30.40		
1	16.64	17.30		
J	3.88	4.36		
K	10.67	11.18		

# **ELECTRICAL CHARACTERISTICS** ( $T_c = 25$ °C unless otherwise noted )

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector - Emitter Sustaining Voltage (1) ( I <sub>C</sub> = 100 mA, I <sub>B</sub> = 0 )	V <sub>CEO(sus)</sub>	60 90 120		V
Collector Cutoff Current ( V <sub>CE</sub> = 50 V, I <sub>B</sub> = 0.0 )	I <sub>CEO</sub>		1.0	mA
	l <sub>cer</sub>		1.0 1.0 1.0 5.0 5.0 5.0	mA
Emitter Cutoff Current ( V <sub>EB</sub> = 5.0 V,I <sub>C</sub> = 0 )	I <sub>EBO</sub>		5.0	mA

# **ON CHARACTERISTICS (1)**

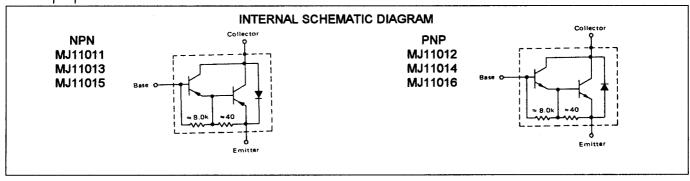
DC Current Gain	hFE		
$(I_c = 20 \text{ A}, V_{cE} = 5.0 \text{ V})$ $(I_c = 30 \text{ A}, V_{cE} = 5.0 \text{ V})$		1000 200	
Collector-Emitter Saturation Voltage ( I <sub>c</sub> = 20 A, I <sub>g</sub> = 200 mA ) ( I <sub>c</sub> = 30 A, I <sub>g</sub> = 300 mA )	V <sub>CE(sat)</sub>	3.0 4.0	V
Base-Emitter Saturation Voltage ( i <sub>c</sub> = 20 A, I <sub>g</sub> = 200 mA ) ( I <sub>c</sub> = 30 A, I <sub>g</sub> = 300 mA )	V <sub>BE(sat)</sub>	3.5 5.0	V

### **DYNAMIC CHARACTERISTICS**

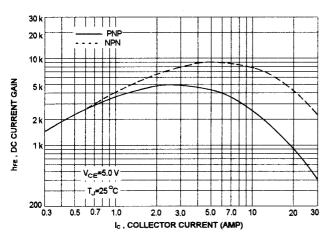
	I		1	
Small-Signal Current Gain	h <sub>fe</sub>			
$(I_C = 10 \text{ A}, V_{CE} = 3.0 \text{ V}, f = 1.0 \text{ MHz})$	,,	4.0		

(1) Pulse Test: Pulse width = 300 us , Duty Cycle  $\leq 2.0\%$ 

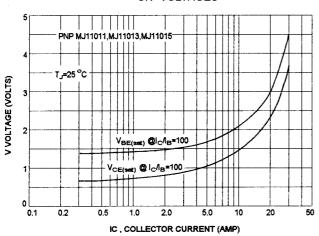
(2)  $f_T = |h_{fe}| \circ f_{test}$ 



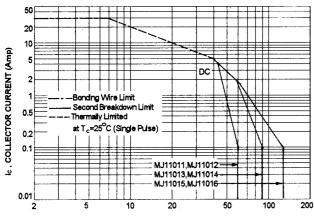
#### DC CURRENT GAIN



#### "ON" VOLTAGES

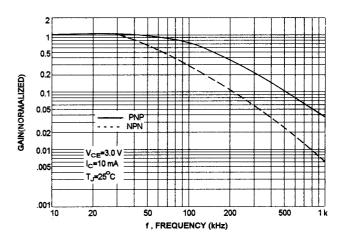


#### ACTIVE-REGION SAFE OPERATING AREA (SOA)

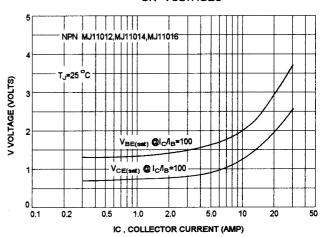


VCE, COLLECTOR EMITTER VOLTAGE (VOLTS)

#### SMALL-SIGNAL CURRENT GAIN



#### "ON" VOLTAGES



There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate  $\rm I_{C^{-}}V_{CE}$  limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on  $T_{J(PK)}$ =200 °C; $T_C$  is variable depending on conditions, second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(PK)}$ <200°C,At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



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