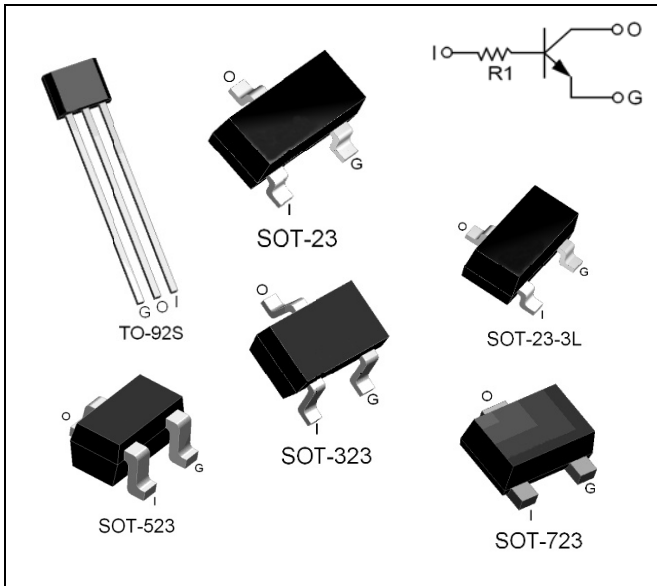


DIGITAL TRANSISTORS NPN Silicon with Built-in Resistors



FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, marking the device design easy.

MECHANICAL DATA

- Available in SOT-523, SOT-323, SOT-23, SOT-23-3L, SOT-723 and TO-92S Package
- Solderability : MIL-STD-202, Method 208
- Full RoHS Compliance

ORDERING INFORMATION

PART NUMBER	PACKAGE	SHIPPING	MARKING CODE
DTC143T□-5T3R	SOT-523	Tape Reel	<div style="border: 1px solid black; padding: 5px; width: 40px; margin: 0 auto;">03</div>
DTC143T□-3T3R	SOT-323	Tape Reel	
DTC143T□-T3R	SOT-23	Tape Reel	
DTC143T□-T3LR	SOT-23-3L	Tape Reel	
DTC143T□-7T3R	SOT-723	Tape Reel	
DTC143T□-T92SB	TO-92S	Tape Box	<div style="border: 1px solid black; padding: 5px; width: 60px; margin: 0 auto;">DTC143T LS yww</div>

Notes:

1. □: none is for Lead Free package;
"G" is for Halogen Free package.
2. Marking Code: yww: y: Year code; ww: Week code.

THERMAL DATA

PARAMETER	SYMBOL	VALUES	UNIT
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	1250	°C/W
		833	
		625	
		625	
		625	
		417	

Notes:

3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Measured with device mounted on 1 in² FR-4 board with 2 oz copper.

ABSOLUTE MAXIMUM RATINGS

$T_A = 25^\circ\text{C}$, unless otherwise specified. (Note 4)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		$V_{(BR)CBO}$	50	V
Collector-Emitter Voltage		$V_{(BR)CEO}$	50	V
Emitter-Base Voltage		$V_{(BR)EBO}$	5	V
Collector Current		I_C	100	mA
Power Dissipation	SOT-723	P_D	100	mW
	SOT-523		150	
	SOT-323		200	
	SOT-23		200	
	SOT-23-3L		200	
	TO-92S		300	
Maximum Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature Range		T_{stg}	- 55 ~ +150	$^\circ\text{C}$

Notes:

4. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

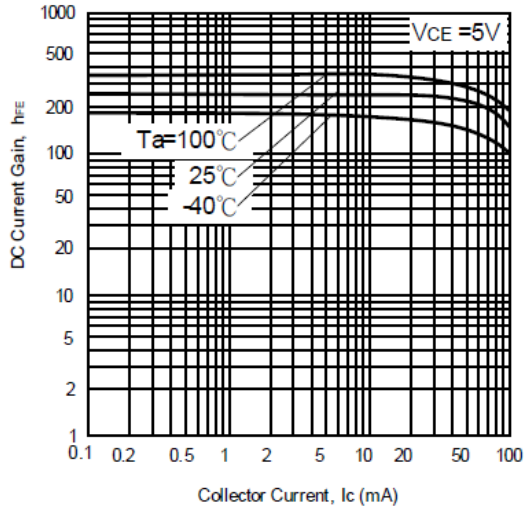
ELECTRICAL CHARACTERISTICS

$T_A = 25^\circ\text{C}$, unless otherwise noted.

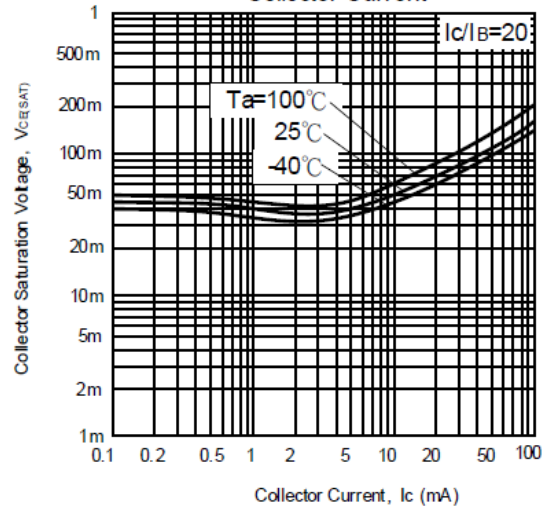
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown	$V_{(BR)CBO}$	$I_C = 50 \mu\text{A}$	50			V
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = 1\text{mA}$	50			V
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = 50 \mu\text{A}$	5			V
Collector Cut-off Current	I_{CBO}	$V_{CB} = 50\text{V}$			0.5	μA
Emitter Cut-off Current	I_{EBO}	$V_{EB} = 4\text{V}$			0.5	μA
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 5\text{mA}, I_B = 0.25\text{mA}$			0.3	V
DC Current Transfer Ratio	h_{FE}	$V_{CE} = 5\text{V}, I_C = 1\text{mA}$	100		600	
Input Resistance	R1		3.29	4.7	6.11	$\text{K}\Omega$
Transition Frequency	f_T	$V_O = 10\text{V}, I_O = 5\text{mA}, f = 100\text{MHz}$		250		MHz

TYPICAL PERFORMANCE CHARACTERISTICS

DC Current Gain vs. Collector Current

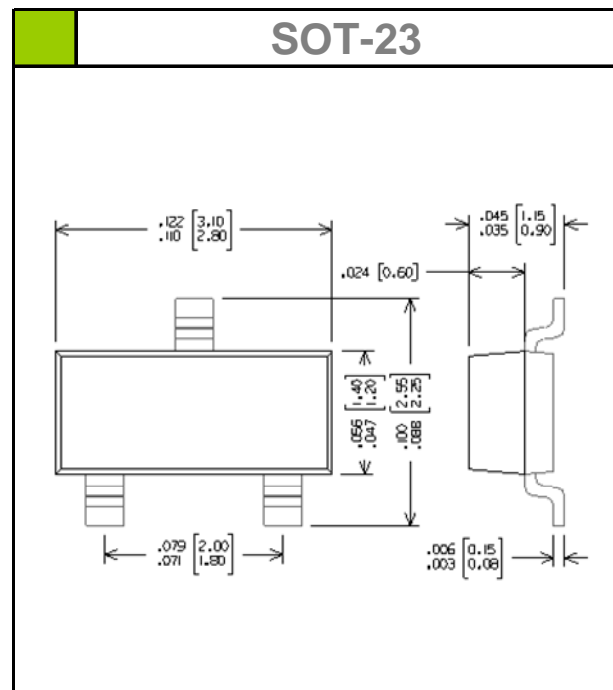
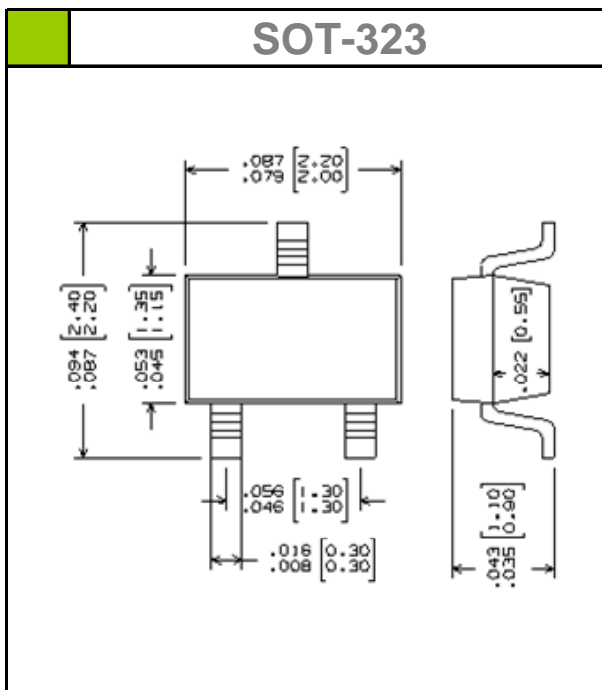
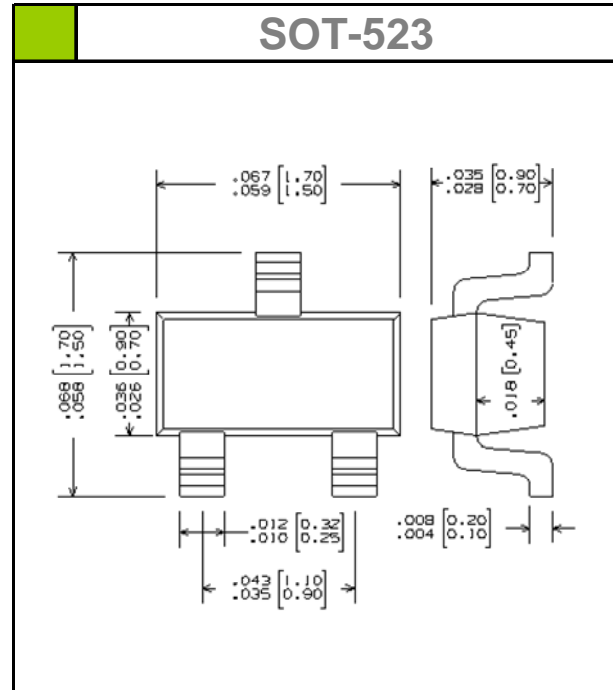
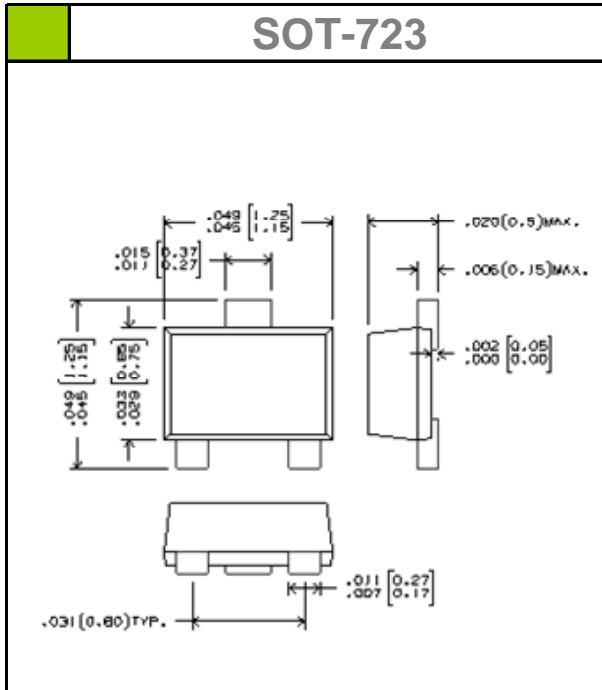


Collector-Emitter Saturation Voltage vs. Collector Current

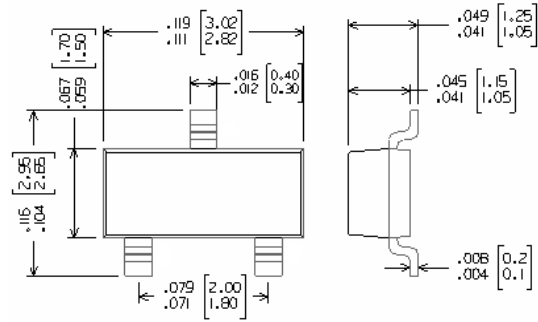


PHYSICAL DIMENSION

Unit : Inch (Millimeter)



SOT-23-3L



TO-92S

