

HIGH-POWER NPN SILICON POWER TRANSISTORS

 \dots designed for use in general-purpose amplifier and switching application .

FEATURES:

- * Recommend for 125W High Fiderity Audio Frequency Amplifier Output stage
- * Complementary to 2SA1215

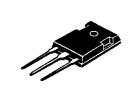
NPN 2SC2921

15 AMPERE POWER TRANASISTOR

160 VOLTS 150 WATTS

MAXIMUM RATINGS

Characteristic	Symbol	2SC2921	Unit
Collector-Emitter Voltage	V _{CEO}	160	٧
Collector-Base Voltage	V _{CBO}	160	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Collector Current - Continuous - Peak	I _C	15 20	А
Base current	I _B	4.0	А
Total Power Dissipation @T _C = 25°C Derate above 25°C	P _D	150 1.2	W/°C
Operating and Storage Junction Temperature Range	T _J ,T _{STG}	-55 to +150	°C



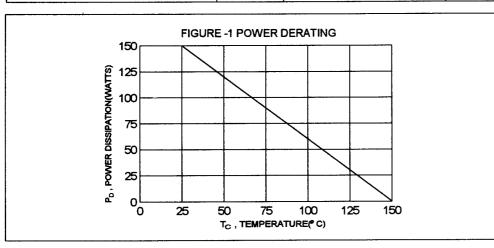
TO-247(3P)

PIN 1.BASE 2.COLLECTOR 3.EMITTER

DIM	MILLIMETERS			
DIM	MIN	MAX		
Α	20.63	22.38		
В	15.38	16.20		
С	1.90	2.70		
D	5.10	6.10		
E	14.81	15.22		
F	11.72	12.84		
G	4.20	4.50		
H	1.82	2.46		
1 1	2.92	3.23		
J	0.89	1.53		
K	5.26	5.66		
L	18.50	21.50		
M	4.68	5.36		
N .	2.40	2.80		
0	3.25	3.65		
Р	0.55	0.70		

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	0.83	°C/W



ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

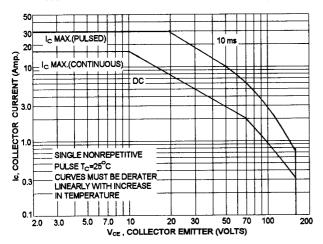
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage (I _C = 25 mA, I _B = 0)	V _{(BR)CEO}	160		V
Collector Cutoff Current (V _{CB} = 160 V, I _E = 0)	Ісво		100	uA
Emitter Cutoff Current (V _{EB} = 5.0 V, I _C = 0)	EBO		100	uA
ON CHARACTERISTICS (1)				
DC Current Gain (I _C = 5.0 A,V _{CE} = 4.0 V)	hFE	50		
Collector-Emitter Saturation Voltage (I _C = 5.0 A, I _B = 500 mA)	V _{CE(sat)}		2.0	V
DYNAMIC CHARACTERISTICS				
Current-Gain-Bandwidth Product (I _C = 2.0 A, V _{CE} = 12 V, f = 1.0 MHz)	f _T	10		MHz

SWITCHING CHARATERISTICS

	V _{CC} = 60 V,I _C = 5.0 A I _{B1} = -I _{B2} = 500 mA R _L = 12 ohm	ton	0.25(typ)	us
		t s	1.95(typ)	us
Fall Time		t,	0.30(typ)	us

⁽¹⁾ Pulse Test: Pulse Width =300 us, Duty Cycle ≤ 2.0%

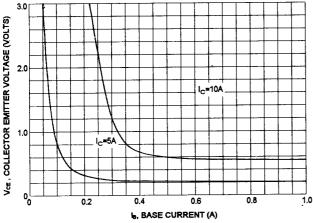
ACTIVE-REGION SAFE OPERATING AREA (SOA)



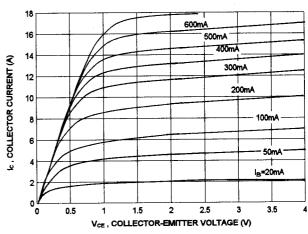
There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate $I_{\text{C}^{-}}V_{\text{CE}}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on $T_{J(PK)}$ =150 °C; T_C is variable depending on conditions, second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)}$ ≤150°C,At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

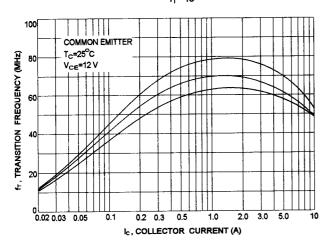




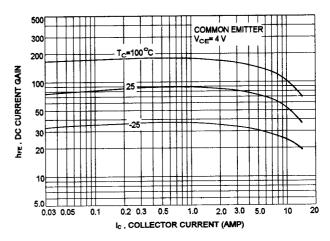




f_T - Ic



DC CURRENT GAIN





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