

PNP SILICON POWER TRANSISTORS

...designed for the output stage of 15W to 25W AF power amplifier

FEATURES:

- * Low Collector-Emitter Saturation Voltage V_{CE(sat)}= 1.0V(Max) @I_C=2.0A,I_B=0.2A * DC Current Gain
- hFE= 40-320@I_C= 1.0A
- * Complementary to NPN 2SD313

PNP 2SB507

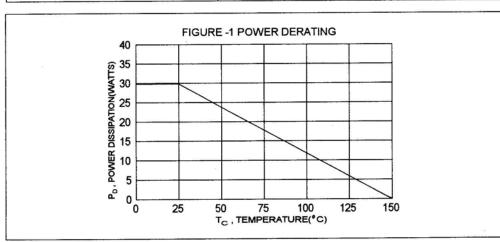
3.0 AMPERE **POWER TRANASISTORS** 60 VOLTS 30 WATTS

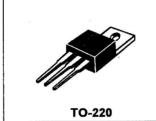
MAXIMUM RATINGS

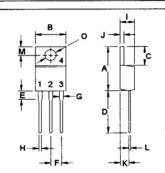
Characteristic	Symbol	2SB507	Unit
Collector-Emitter Voltage	V _{CEO}	60	V
Collector-Base Voltage	V _{CBO}	60	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Collector Current - Continuous - Peak	I _C	3.0 6.0	A
Base current	l _B	1.0	Α
Total Power Dissipation @T _C = 25°C Derate above 25°C	P _D	30 0.24	W/°C
Operating and Storage Junction Temperature Range	T _J ,T _{STG}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	4.16	°C/W







PIN 1.BASE 2.COLLECTOR 3.EMITTER 4.COLLECTOR(CASE)

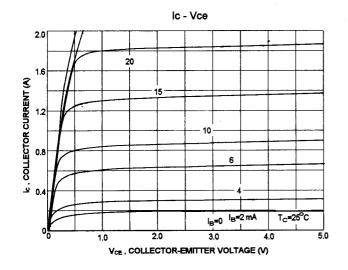
DIM	MILLIMETERS					
DIM	MIN	MAX				
Α	14.68	16.00				
В	9.78	10.42				
С	5.02	6.60				
D	13.00	14.62				
E	3.10	4.19				
F	2.41	2.67				
G	1.10	1.67				
н	0.69	1.01				
I	3.21	4.98				
J	1.14	1.40				
K	2.20	3.30				
L	0.28	0.61				
М	2.48	3.00				
0	3.50	4.00				

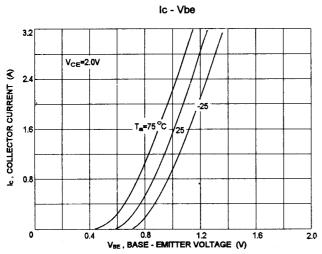
ELECTRICAL CHARACTERISTICS	(T _c :	= 25°C unless	otherwise noted)
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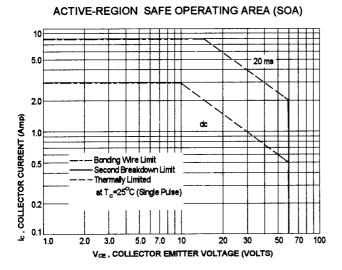
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Voltage (I _C = 10 mA, I _B = 0)	V _{CEO}	60		V
Collector Cutoff Current (V _{CB} = 20 V, I _E = 0)	I _{CBO}		100	uA
Collector Cutoff Current (V _{CE} = 60 V, I _B = 0)	I _{CEO}		5.0	mA
Emitter Cutoff Current (V _{EB} =4.0 V, I _C = 0)	I _{EBO}		1.0	mA
ON CHARACTERISTICS (1)				
DC Current Gain (I _C = 1.0 A, V _{CE} = 2.0 V) * (I _C = 0.1 A, V _{CE} = 2.0 V)	hFE(2) hFE	40 40	320	
Collector-Emitter Saturation Voltage (I _C = 2.0 A, I _B = 200 mA)	V _{CE(sat)}		1.0	V
Base-Emitter On Voltage (I _C = 1.0 A, V _{CE} =2.0 V)	V _{BE(on)}		1.5	V
DYNAMIC CHARACTERISTICS				
Current-Gain-Bandwidth Product (I _C = 0.5 A, V _{CE} = 5.0 V, f = 1.0 MHz)	f _τ	5.0		MHz

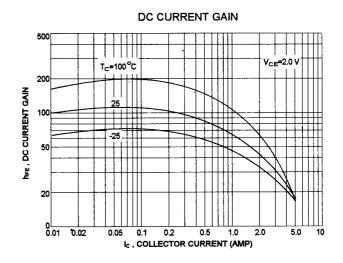
⁽¹⁾ Pulse Test: Pulse Width =300 us,Duty Cycle $\leq 2.0\%$ * hFE(2) Classification :

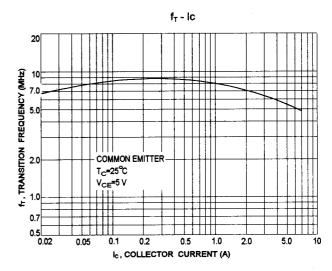
=(=)											
40	С	80	60	D	120	100	E	200	160	F	320











There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate $I_{\text{C}}\text{-V}_{\text{CE}}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on $T_{J(PK)}$ =150 °C; T_{c} is variable depending on conditions, second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)}$ ≤150°C,At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



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