

SWITCHMODE SERIES NPN POWER TRANSISTORS

... designed for use in high-voltage, high-speed, power switching in inductive circuit, they are particularly suited for 115 and 220 V switchmode applications such as switching regulator's, inverters, DC -DC and conveter

FEATURES:

*Collector-Emitter Sustaining Voltage-

V_{CEX} = 350 V to 450 V

* Collector-Emitter Saturation Voltage -

 $V_{CE(sat)} = 2.0 \text{ V (Max.)} @ I_{C} = 5.0 \text{ A}, I_{B} = 1.0 \text{ A}$ * Switching Time - t_{f} =0.5 us (Max.) @ I_{C} =5.0 A

NPN 2N6738 2N6739 2N6740

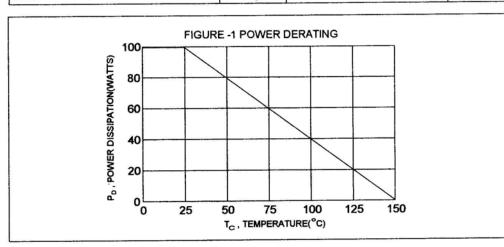
8.0 AMPERE SILICON POWER **TRANASISTORS** 300-400 VOLTS 100 WATTS

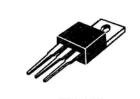
MAXIMUM RATINGS

Characteristic	Symbol	2N6738	2N6739	2N6740	Unit
Collector-Emitter Voltage V _{BE} =-1.5V	V _{CEV}	450	550	650	V
Collector-Emitter Voltage V _{BE} =-1.5V	V _{CEX}	350	400	450	V
Collector-Emitter Voltage	V _{CEO}	300	350	400	٧
Emitter-Base Voltage	V _{EBO}	8.0			V
Collector Current - Continuous - Peak	I _C	8.0 10			Α
Base current	l _B	4.0			А
Total Power Dissipation @T _C = 25°C Derate above 25°C	P _D	100 0.8			W/°C
Operating and Storage Junction Temperature Range	T _J ,T _{STG}	-65 to 150		°C	

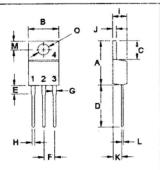
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	1.25	°C/W





TO-220



PIN 1.BASE 2.COLLECTOR 3.EMITTER 4.COLLECTOR(CASE)

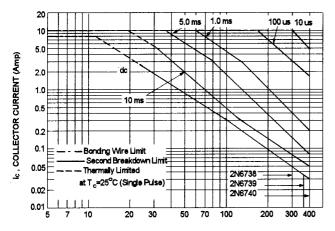
DIM	MILLIMETERS			
DIN	MIN	MAX		
Α	14.68	16.00		
В	9.78	10.42		
С	5.02	6.60		
D	13.00	14.62		
E	3.10	4.19		
F	2.41	2.67		
G	1.10	1.67		
Н	0.69	1.01		
I	3.21	4.98		
J	1.14	1.40		
K	2.20	3.30		
L	0.28	0.61		
M	2.48	3.00		
0	3.50	4.00		

ELECTRICAL CHARACTERISTICS (T_c = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTIC	:S				
Collector-Emitter Voltage (I _C = 200 mA, I _B = 0)	2N6738 2N6739 2N6740	V _{CEO(sus)}	300 350 400		V
Collector Cutoff Current (V _{CEV} = Rated Value, V _{BE(} (V _{CEV} = Rated Value, V _{BE(}	_{off)} =-1.5 V) _{off)} =-1.5 V , T _C =100 °C)	I _{CEV}		0.1 1.0	mA
Emitter Cutoff Current (V _{EB} = 8.0 V,I _C = 0)		I _{EBO}		2.0	mA
ON CHARACTERISTICS	3 (1)				
DC Current Gain (I _C = 5.0 A, V _{CE} = 3.0 V)		hFE	10	40	
Collector-Emitter Saturatio (I_C = 5.0 A, I_B = 1.0 A) (I_C = 8.0 A, I_B = 4.0 A)	n Voltage	V _{CE(sat)}		1.0 2.0	V
Base-Emitter Saturation Vo (I _C = 5.0 A, I _B = 1.0 A)	oltage	V _{BE(sat)}		1.6	V
DYNAMIC CHARACTER	RISTICS				
Current-Gain-Bandwidth P (I _C = 200 mA , V _{CE} = 10 \		f _T	10	60	MHz
SWTCHING CHARACT	ERISTICS				
Delay Time	V _{cc} = 125 V, I _c = 5.0 A	t _d	6.101	0.1	us
Rise Time	I _{B1} = -I _{B2} =1.0A	tr		0.4	us
Storage Time	t _n = 20 us,Duty Cycle ≦1.0%	t _s		2.5	us
Fall Time		t,		0.5	us

⁽¹⁾ Pulse Test: Pulse width = 300 us , Duty Cycle \leq 2.0% (2) $f_T = |h_{f_e}| \circ f_{test}$

FIG-2 ACTIVE REGION SAFE OPERATING AREA



VCE , COLLECTOR EMITTER VOLTAGE (VOLTS)

FIG-3 "ON" VOLTAGES

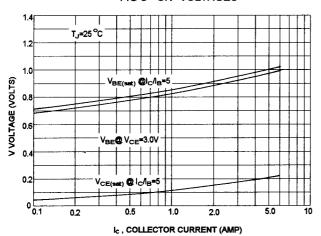
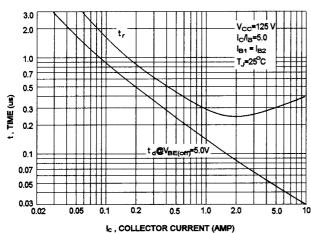


FIG-5 TURN-ON TIME



There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate $I_{\text{C}^{-}}V_{\text{CE}}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of FIG-2 is base on T_c =25 °C; $T_{J(PK)}$ is variable depending on power level.second breakdown pulse limi -ts are valid for duty cycles to 10% provided $T_c \ge 25$ °C,At high case temperatures,thermal limitation will reduce the power that can be handled to values less than the limita-tions imposed by second breakdown.

FIG-4 DC CURRENT GAIN

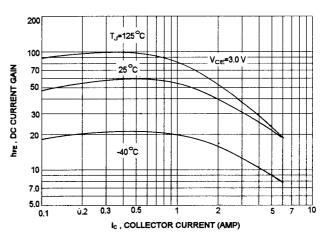
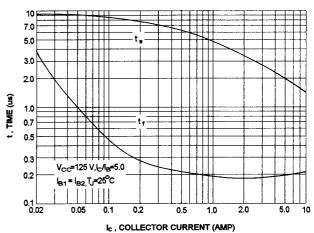


FIG-6 TURN-OFF TIME





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