MOSPEC

HIGH VOLTAGE NPN SILICON POWER TRANSISTORS

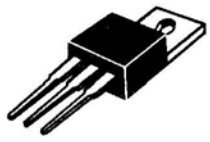
... designed for high voltage inverters,switching regulators and line operated amplifier applications.

NPN
2N6497
2N6498
2N6499

5 AMPERE
POWER TRANSISTORS

250-350 Volts
80 Watts

FEATURES:
* Collector-Emitter Sustaining Voltage-
  $V_{CEO(SUS)}$ = 250 V (Min) -2N6497
  = 300 V (Min) -2N6498
  = 350 V (Min) -2N6499
* DC Current Gain
  hFE = 10-75 @ I_C =2.5 A



TO-220

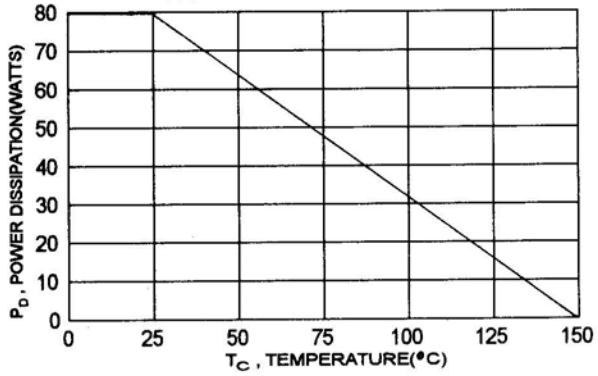
MAXIMUM RATINGS

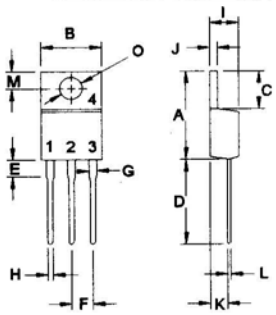
Characteristic	Symbol	2N6497	2N6498	2N6499	Unit
Collector-Emitter Voltage	V_{CEO}	250	300	350	V
Collector-Base Voltage	V_{CBO}	350	400	450	V
Emitter-Base Voltage	V_{EBO}	6.0			V
Collector Current - Continuous - Peak	I_C	5.0 10			A
Base Current	I_B	2.0			A
Total Power Dissipation@T_C = 25°C Derate above 25°C	P_D	80 0.64			W W/°C
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-65 to +150			°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	1.56	°C/W

FIGURE -1 POWER DERATING





PIN 1.BASE
2.COLLECTOR
3.EMITTER
4.COLLECTOR(CASE)

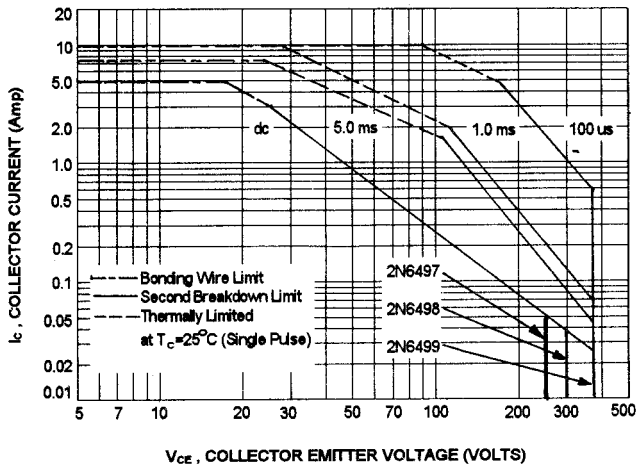
DIM	MILLIMETERS	
	MIN	MAX
A	14.68	16.00
B	9.78	10.42
C	5.02	6.60
D	13.00	14.62
E	3.10	4.19
F	2.41	2.67
G	1.10	1.67
H	0.69	1.01
I	3.21	4.98
J	1.14	1.40
K	2.20	3.30
L	0.28	0.61
M	2.48	3.00
O	3.50	4.00

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector - Emitter Sustaining Voltage (1) (I_C = 25 mA, I_B = 0)	2N6497 2N6498 2N6499	$V_{CEO(SUS)}$	 250 300 350		V
Collector Cutoff Current (V_{CE} = 350 V, $V_{BE(off)}$ = 1.5 V) (V_{CE} = 400 V, $V_{BE(off)}$ = 1.5 V) (V_{CE} = 450 V, $V_{BE(off)}$ = 1.5 V) (V_{CE} = 175 V, $V_{BE(off)}$ = 1.5 V, T_C = 100°C) (V_{CE} = 200 V, $V_{BE(off)}$ = 1.5 V, T_C = 100°C) (V_{CE} = 225 V, $V_{BE(off)}$ = 1.5 V, T_C = 100°C)	 2N6497 2N6498 2N6499 2N6497 2N6498 2N6499	I_{CEX}		 1.0 1.0 1.0 10 10 10	mA
Emitter Cutoff Current (V_{EB} = 6.0 V , I_C = 0)		I_{EBO}		1.0	mA
ON CHARACTERISTICS (1)					
DC Current Gain (I_C = 2.5 A, V_{CE} = 10 V) (I_C = 5.0 A, V_{CE} = 10 V)		h_{FE}	 10 3.0	75	
Collector-Emitter Saturation Voltage (I_C = 2.5 A, I_B = 0.5 A) (I_C = 5.0 A, I_B = 2.0 A)	 2N6497 2N6498 2N6499 All Devices	$V_{CE(sat)}$		 1.0 1.25 1.5 5.0	V
Base-Emitter Saturation Voltage (I_C = 2.5 A, I_B = 0.5 A) (I_C = 5.0 A, I_B = 2.0 A)		$V_{BE(sat)}$		 1.5 2.5	V
DYNAMIC CHARACTERISTICS					
Current-Gain-Bandwidth Product (2) (I_C = 250 mA, V_{CE} = 10 V, f = 1.0 MHz)		f_T	5.0		MHz
SWITCHING CHARACTERISTICS					
Rise Time	V_{CC} = 125 V I_C = 2.5 A I_{B1} = -I_{B2} = 0.5 A t_p = 0.1 ms Duty Cycle ≤2.0%	t_r		1.0	us
Storage Time		t_s		2.5	us
Fall Time		t_f		1.0	us

(1) Pulse Test: Pulse width = 300μs , Duty Cycle ≦ 2.0%
(2) $f_T = |h_{fe}| \cdot f_{test}$

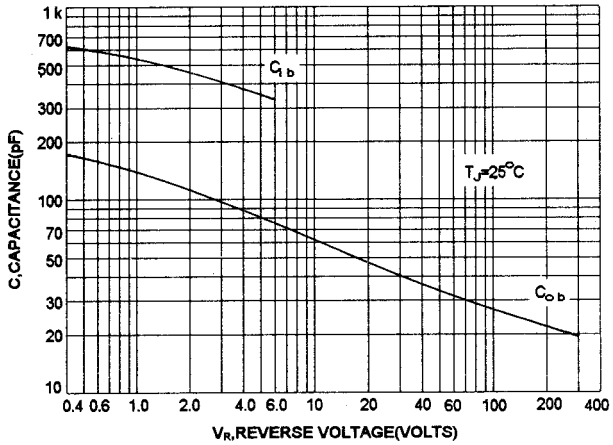
ACTIVE-REGION SAFE OPERATING AREA (SOA)



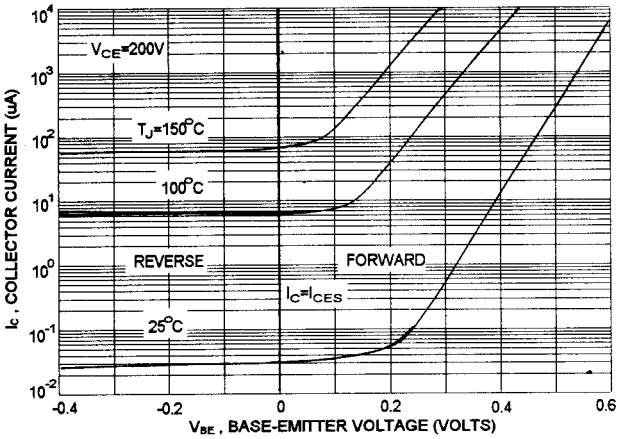
There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on $T_{J(PK)}$=150 °C;T_C is variable depending on conditions. second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)}$≤150°C,At high case temperatures, thermal limita - tion will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

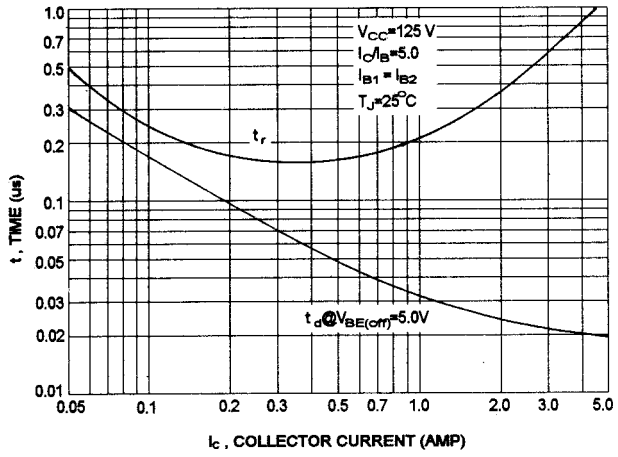
CAPACITANCES



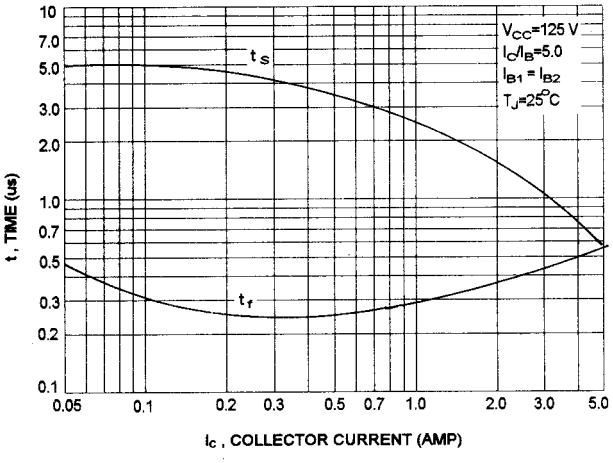
COLLECTOR CUT-OFF REGION



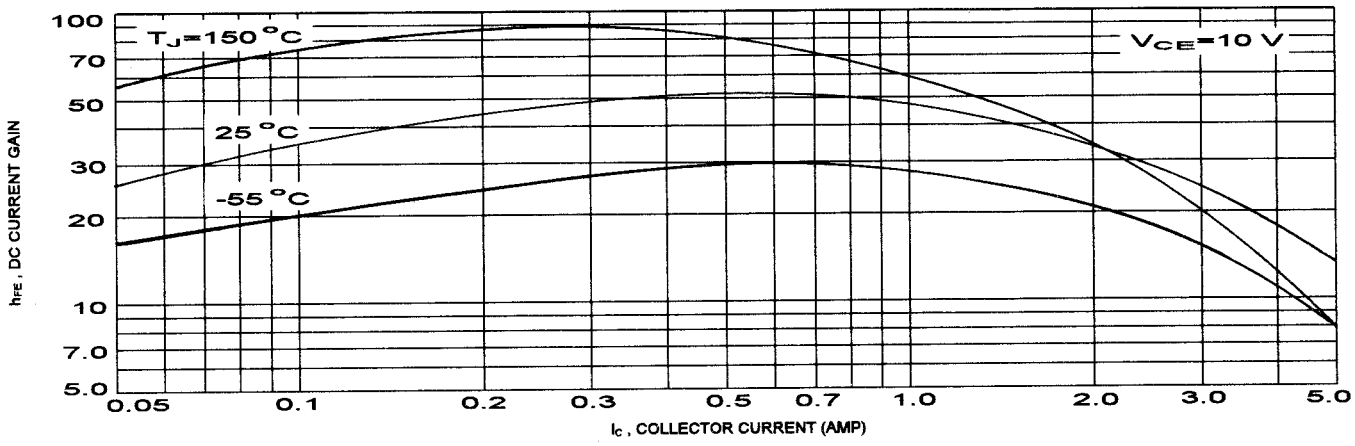
TURN-ON TIME



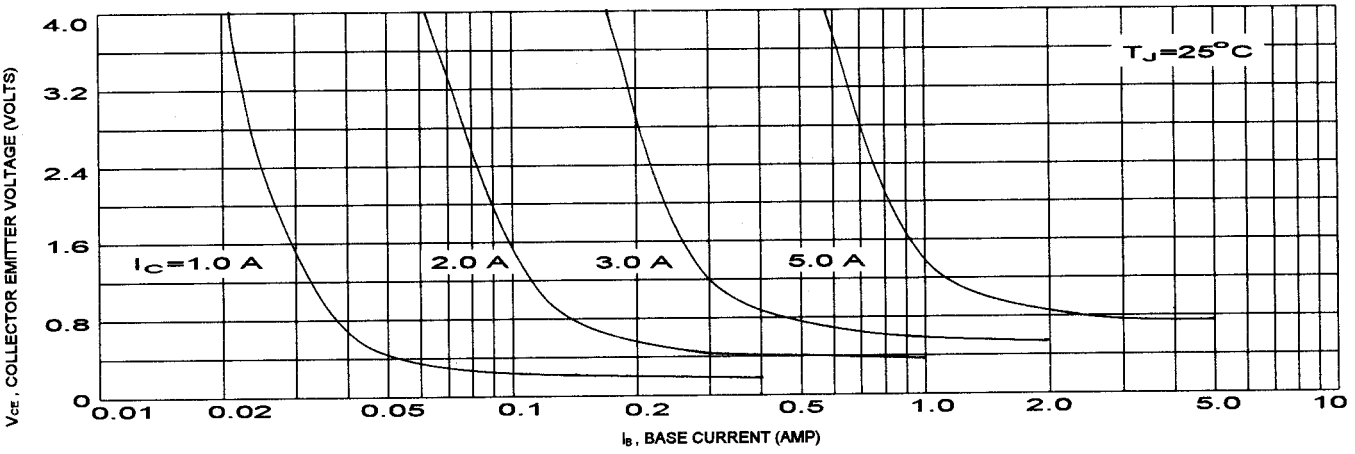
TURN-OFF TIME



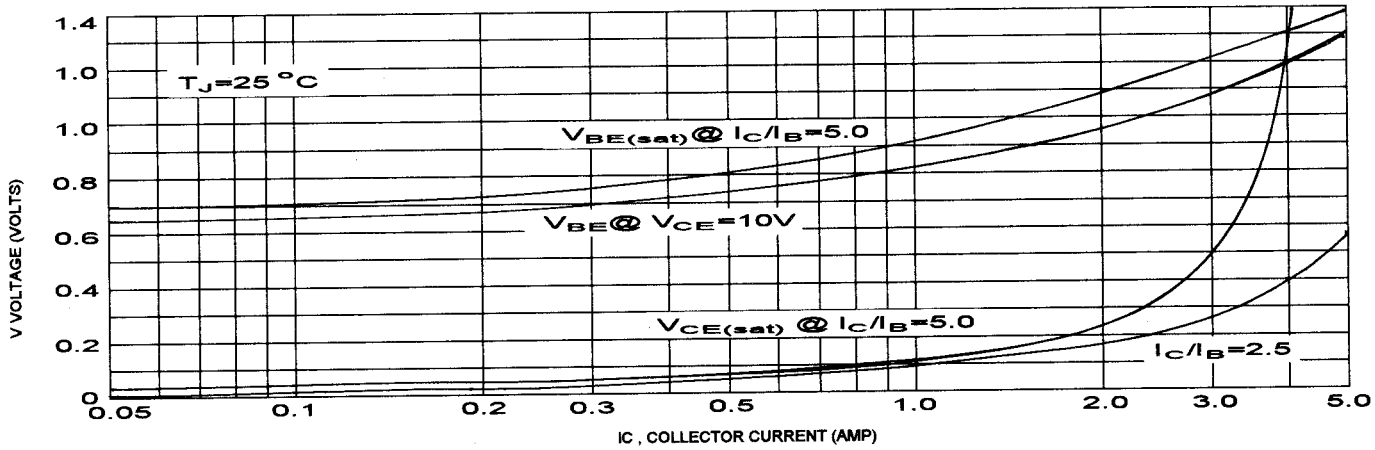
DC CURRENT GAIN



COLLECTOR SATURATION REGION



"ON"VOLTAGES



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