

MEDIUM-POWER PNP TRANSISTORS

...ideal for use as drivers, switches and medium- power amplifier and applications

FEATURES:

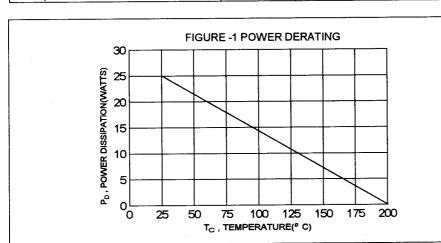
- * Low Collector-Emitter Saturation Voltage -
- V_{CE(SAT)}=0.6V(Max.)@I_C=1.0A * High Gain Characteristics -hFE = 30 ~ 100 @ I_C = 250mA * Excellent Safe Area Limits

MAXIMUM RATINGS

Characteristic	Symbol	2N3740	2N3741	Unit
Collector-Emitter Voltage	V _{CEO}	60 80		V
Collector-Base Voltage	V _{CBO}	60	80	V
Emitter-Base Voltage	V _{EBO}		V	
Collector Current-Continuous -Peak	I _C	4.0 10		A
Base Current	I _B	2.0		Α
Total Power Dissipation@T _C =25°C Derate above 25°C	P _D	25 0.143		W/°C
Operating and Storage Junction Temperature Range	T _J ,T _{STG}	- 65 to +200		°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	7.0	°C/W

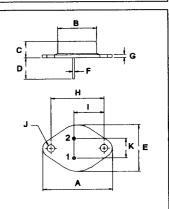


PNP 2N3740 2N3741

4 AMPERE **POWER TRANSISTORS** PNP SILICON 60 - 80 Volts 25 Watts



TO-66



PIN 1.BASE 2.EMITTER COLLECTOR(CASE)

MID	MILLIMETERS			
וויוט	MIN	MAX		
Α	30.60	32.52		
В	13.85	14.16		
С	6.54	7.22		
D	9.50	10.50		
E	17.26	18.46		
F	0.76	0.92		
G.	1.38	1.65		
H	24.16	24.78		
	13.84	15.60		
J	3.32	3.92		
K	4.86	5.34		

ELECTRICAL CHARACTERISTICS ($T_c = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector - Emitter Sustaining Voltage (1) (I _C = 100 mA, I _B = 0)	2N3740 2N3741	V _{CEO(SUS)}	60 80		V
Collector Cutoff Current (V _{CE} = 40 V, I _B = 0) (V _{CE} = 60 V, I _B = 0)	2N3740 2N3741	I _{CEO}		1.0 1.0	mA
Collector Cutoff Current ($V_{CE} = 60 \text{ V}, V_{BE(off)} = 1.5 \text{ V}$) ($V_{CE} = 80 \text{ V}, V_{BE(off)} = 1.5 \text{ V}$) ($V_{CE} = 40 \text{ V}, V_{BE(off)} = 1.5 \text{ V}, T_{C} = 150^{\circ}\text{C}$) ($V_{CE} = 60 \text{ V}, V_{BE(off)} = 1.5 \text{ V}, T_{C} = 150^{\circ}\text{C}$)	2N3740 2N3741 2N3740 2N3741	I _{CEX}		100 100 1.0 1.0	uA uA mA mA
Collector Cutoff Current (V _{CB} = 60 V, I _E = 0) (V _{CB} = 80 V, I _E = 0)	2N3740 2N3741	Ісво		100 100	uA
Emitter Cutoff Current (V _{EB} = 7.0 V,I _C = 0)		I _{EBO}		0.5	mA

ON CHARACTERISTICS (1)

DC Current Gain ($I_c = 100 \text{ mA}, V_{cE} = 1.0 \text{ V}$) ($I_c = 250 \text{ mA}, V_{cE} = 1.0 \text{ V}$) ($I_c = 500 \text{ mA}, V_{cE} = 1.0 \text{ V}$) ($I_c = 1.0 \text{ A}, V_{cE} = 1.0 \text{ V}$)	hFE	40 30 20 10	100	
Collector-Emitter On Voltage (I _C = 1.0 A, I _B = 125 mA)	V _{CE(sat)}		0.6	V
Base-Emitter On Voltage (I _C = 250 mA, V _{CE} = 1.0 V)	V _{BE(on)}		1.0	V

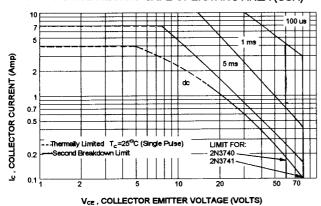
DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product (2) (I _C = 100 mA, V _{CE} = 10 V, f = 1.0 MHz)	f _T	3.0		MHz
Commom Base Output Capacitance (V _{CB} = 10 V ,I _C = 0 , f = 100 KHz)	C _{ob}		100	pF
Small-Signal Current Gain (I _C = 50 mA, V _{CE} = 10 V, f = 1.0 KHZ)	h _{fe}	25		

⁽¹⁾ Pulse Test: Pulse width \leq 300 us , Duty Cycle \leq 2.0%

⁽²⁾ f_= | h_{fe} | • f_{test}

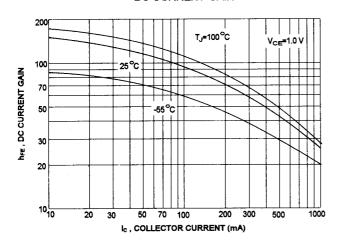
ACTIVE-REGION SAFE OPERATING AREA (SOA)



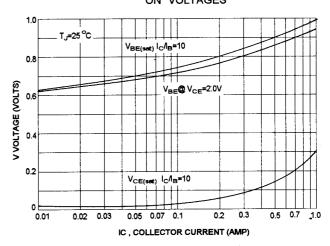
There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate $I_{\text{C}}\text{-V}_{\text{CE}}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on $T_{J(PK)}$ =200 °C; T_C is variable depending on conditions. second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)}$ ≤200°C,At high case temperatures,thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown

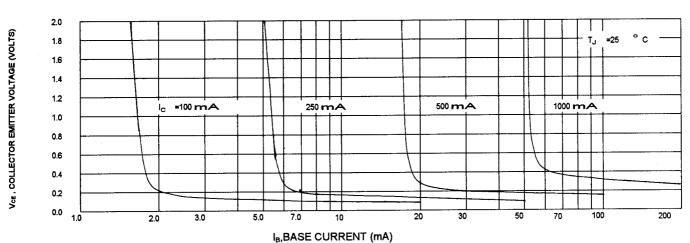
DC CURRENT GAIN



"ON" VOLTAGES



COLLECTOR SATURATION REGION





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