

1200V Silicon Carbide MOSFET

DESCRIPTION :

- Low gate charge
- Low Switching Losses
- Fast reverse recovery body diode
- Fast High frequency operation
- Tight variation of $R_{DS(on)}$ with temperature

V_{DS}	1200 V
$I_D(T_c = 25^\circ\text{C})$	108.4 A
$R_{DS(on)}$	30 m Ω

TYPICAL APPLICATIONS :

- Solar inverters
- EV Charge
- Switch mode power supplies
- Motor drives
- Energy Storage
- Uninterruptible power supplies (UPS)



HU3PAK

MAXIMUM RATINGS (at $T_J = 25^\circ\text{C}$, unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	1200	V
Gate-Source Voltage Recommend Drive Voltage	Max Transient Voltage, <1% duty cycle	V_{GSS} $V_{GS(OP)}$	-10/+22 -3/+18	V
Continuous Drain Current	$V_{GS}=18\text{V}$, $T_c=25^\circ\text{C}$ $V_{GS}=18\text{V}$, $T_c=110^\circ\text{C}$	I_D	108.4 78.4	A
Pulsed Drain Current	Pulse width tp limited by T_{jmax} , $V_{GS} = 18\text{V}$	$I_{D \text{ PULSE}}$	160	A
Total power dissipation	$T_c=25^\circ\text{C}$ $T_c=110^\circ\text{C}$	P_{tot}	555 246	W
Operation Junction temperature		T_j	-55~+175	$^\circ\text{C}$
Storage temperature		T_{STG}	-55~+150	$^\circ\text{C}$
Soldering Temperature	1.6mm (0.063") from case for 10s	T_L	260	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Typical	Unit
Thermal resistance, junction - case		$R_{th(j-c)}$	0.27	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS (at $T_J = 25^{\circ}\text{C}$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0\text{V}$, $I_D = 100\mu\text{A}$	$V_{(BR)DSS}$	1200			V
Zero Gate Voltage Drain Current $V_{DS} = 1200\text{V}$, $V_{GS} = 0\text{V}$ $T_J = 25^{\circ}\text{C}$ $V_{DS} = 1200\text{V}$, $V_{GS} = 0\text{V}$ $T_J = 175^{\circ}\text{C}$	I_{DSS}		0.1 10	100 100	μA
Gate-Source Leakage Current $V_{GS} = 22\text{V}$, $V_{DS} = 0\text{V}$ $V_{GS} = -10\text{V}$, $V_{DS} = 0\text{V}$	I_{GSS}			100 100	nA
Gate-Source Threshold Voltage $V_{DS} = V_{GS}$, $I_D = 15\text{mA}$ $T_J = 25^{\circ}\text{C}$ $V_{DS} = V_{GS}$, $I_D = 15\text{mA}$ $T_J = 175^{\circ}\text{C}$	$V_{GS(th)}$	2	3.2 2.2	5	V
Drain-Source On-State Resistance $V_{GS} = 18\text{V}$, $I_D = 40\text{A}$ $T_J = 25^{\circ}\text{C}$ $V_{GS} = 18\text{V}$, $I_D = 40\text{A}$ $T_J = 175^{\circ}\text{C}$	$R_{DS(on)}$		26 43	45	m Ω
Transconductance $I_D = 30\text{A}$ $T_J = 25^{\circ}\text{C}$ $I_D = 30\text{A}$ $T_J = 175^{\circ}\text{C}$	G_{fs}		16.2 17.0		S
Internal Gate Resistance $f = 1\text{MHz}$, $V_{AC} = 25\text{mV}$	$R_{G(int)}$		1.72		Ω
Input capacitance $f = 1\text{MHz}$, $V_{AC} = 25\text{mV}$, $V_{DS} = 800\text{V}$, $V_{GS} = 0\text{V}$	C_{iss}		3505		pF
Output capacitance $f = 1\text{MHz}$, $V_{AC} = 25\text{mV}$, $V_{DS} = 800\text{V}$, $V_{GS} = 0\text{V}$	C_{oss}		232		pF
Reverse transfer capacitance $f = 1\text{MHz}$, $V_{AC} = 25\text{mV}$, $V_{DS} = 800\text{V}$, $V_{GS} = 0\text{V}$	C_{rss}		39		pF
C_{oss} Stored Energy $f = 1\text{MHz}$, $V_{AC} = 25\text{mV}$, $V_{DS} = 800\text{V}$, $V_{GS} = 0\text{V}$	E_{oss}		161		μJ
Total Gate Charge $V_{DD} = 800\text{V}$, $I_D = 30\text{A}$, $V_{GS} = -3/18\text{V}$, turn-on pulse	Q_G		243		nC

Gate to Source Charge VDD= 800V, ID= 30A,VGS= -3/18V, turn-on pulse	Q_{GS}		37		nC
Gate to Drain Charge VDD= 800V, ID= 30A,VGS= -3/18V, turn-on pulse	Q_{GD}		113		nC
Turn-on delay time VDD=800 V, ID=60A, VGS= -5/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -5V (inductive load)	$t_{d(ON)}$		23.13		ns
Rise time VDD=800 V, ID=30A, VGS= -3/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -3V (inductive load)	t_r		38.25		ns
Turn-off delay time VDD=800 V, ID=30A, VGS= -3/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -3V (inductive load)	$t_{d(OFF)}$		59.94		ns
Fall time VDD=800 V, ID=30A, VGS= -3/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -3V (inductive load)	t_f		24.3		ns
Turn-on Switching Energy VDD=800 V, ID=30A, VGS= -3/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -3V (inductive load)	$E_{(ON)}$		681.03		μJ
Turn-off Switching Energy VDD=800 V, ID=30A, VGS= -3/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -3V (inductive load)	$E_{(OFF)}$		248.22		μJ
Total Switching Energy VDD=800 V, ID=30A, VGS= -3/18V, $R_{G(EXT)}=4.7\Omega$ $L_\sigma = 100\mu H$, Body diode at VGS = -3V (inductive load)	$E_{(TOT)}$		929.25		μJ

Body Diode

ELECTRICAL CHARACTERISTICS (at $T_J = 25^\circ C$, unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Diode Forward Voltage VGS = 0V, ISD = 25A $T_J=25^\circ C$ VGS = 0V, ISD = 25A $T_J=175^\circ C$	V_{SD}		3.79 3.36		V

Continuous Diode Forward Current VGS = -3V, Tj=25°C VGS = -3V, Tj=100°C	ISD			93 53.4	A
Revers Recovery Time VDD=800 V, ID=30A, VGS= -3V, di/dt = 1000A/us	Trr		32		ns
Revers Recovery Charge VDD=800 V, ID=30A, VGS= -3V, di/dt = 1000A/us	Qrr		344		nC
Peak Revers Recovery Current VDD=800 V, ID=30A, VGS= -3V, di/dt = 1000A/us	Irrm		18		A

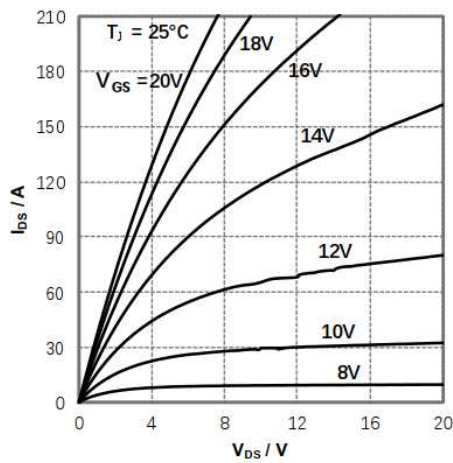


Figure 1. Typical output characteristics (Tj=25°C)

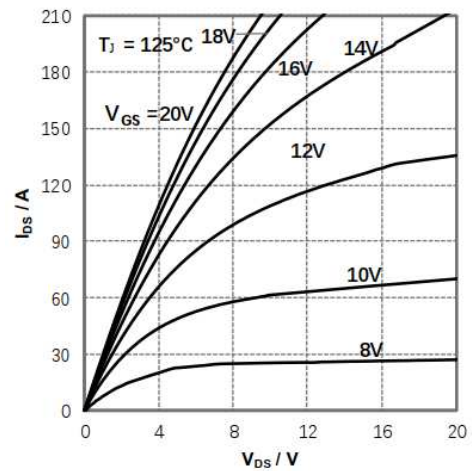


Figure 2. Typical output characteristics (Tj=125°C)

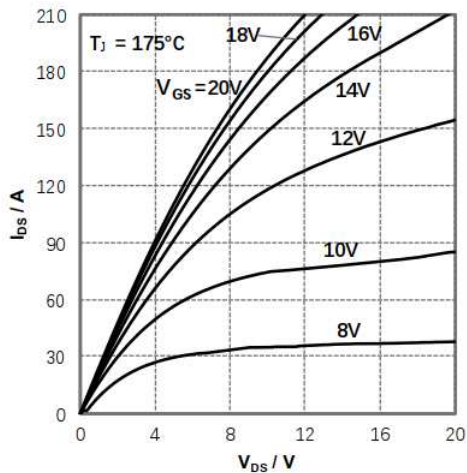


Figure 3. Typical output characteristics (Tj=175°C)

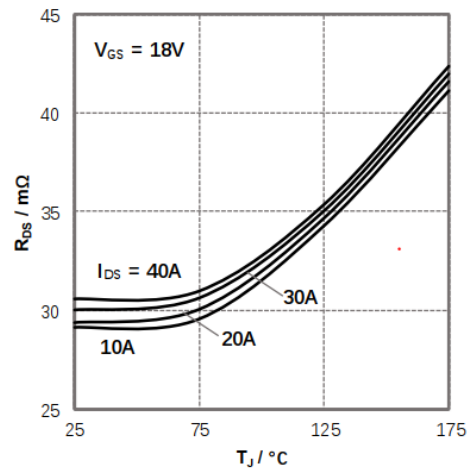


Figure 4. Typical On-Resistance vs. Temperature For Various Drain Current)

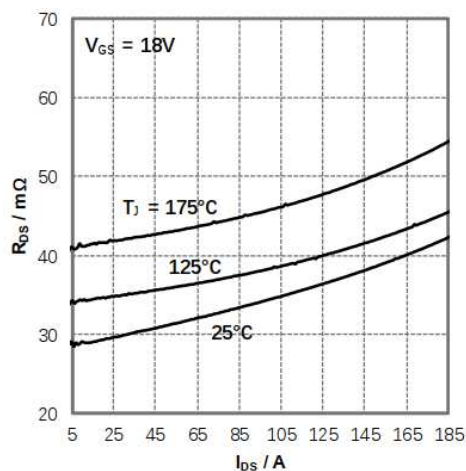


Figure 5. On-Resistance vs. Drain Current For Various Temperature

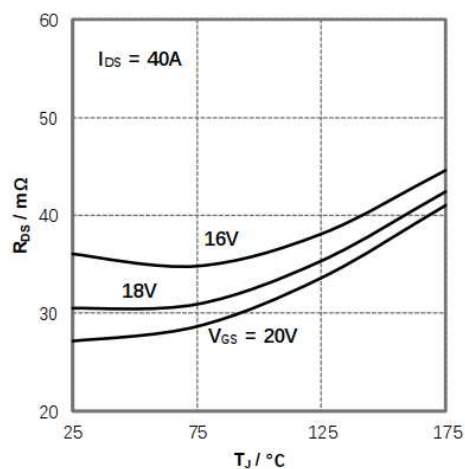


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

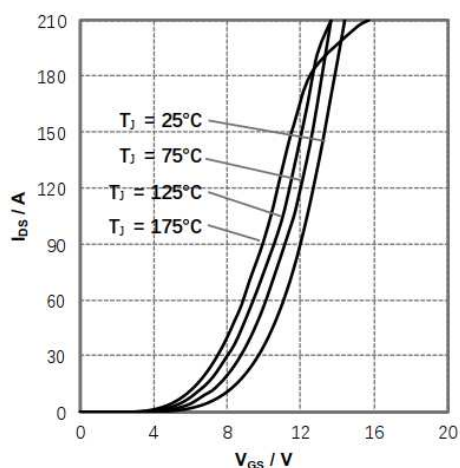


Figure 7. Transfer Characteristics

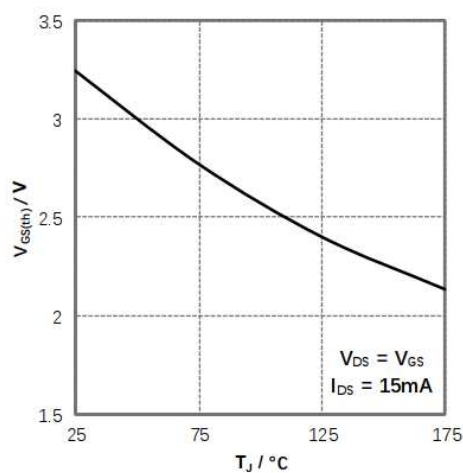


Figure 8. Threshold Voltage vs. Temperature

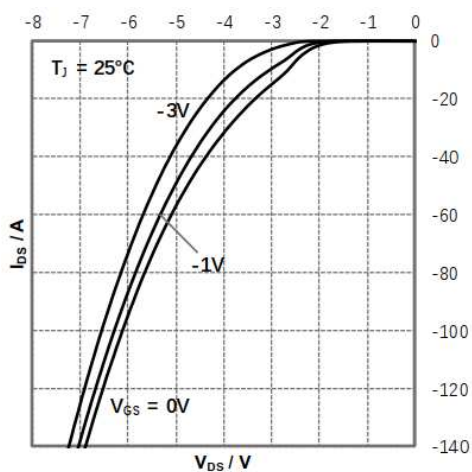


Figure 9. Body Diode Characteristics T_J=25°C

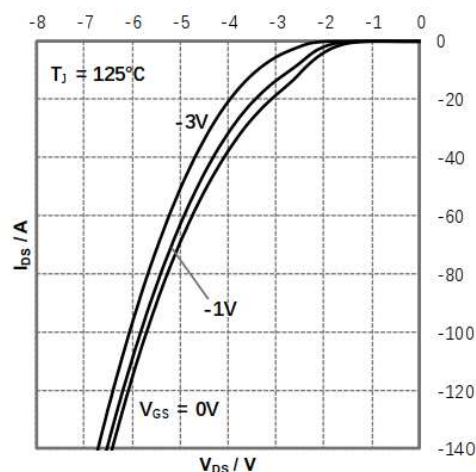


Figure 10. Body Diode Characteristics T_J=125°C

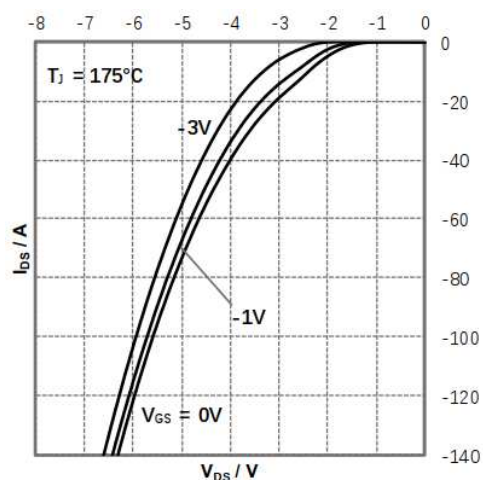
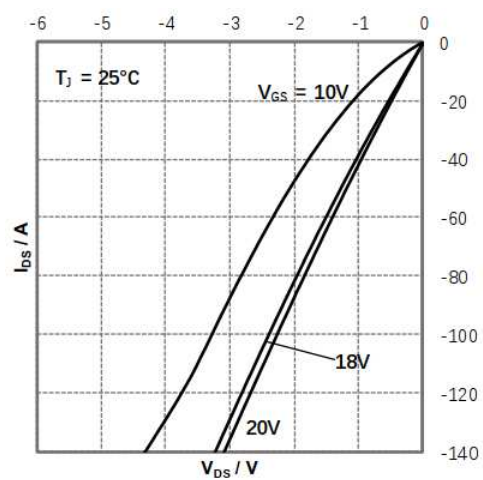
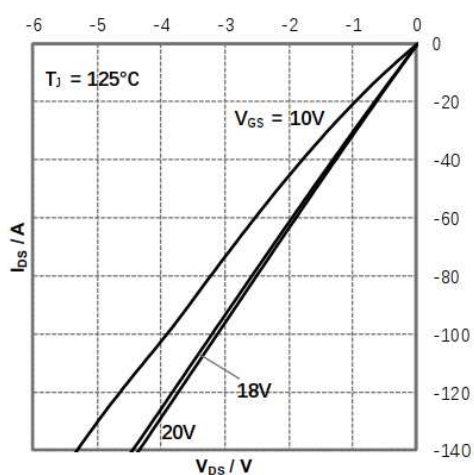
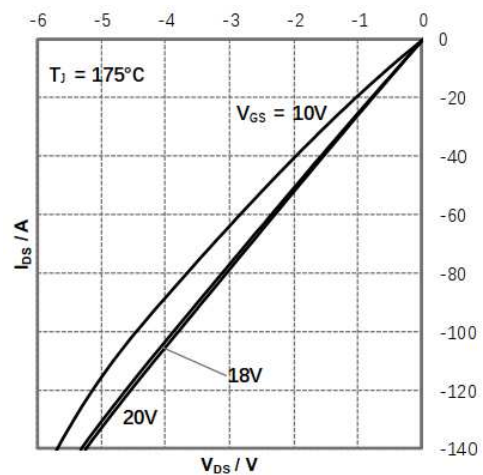
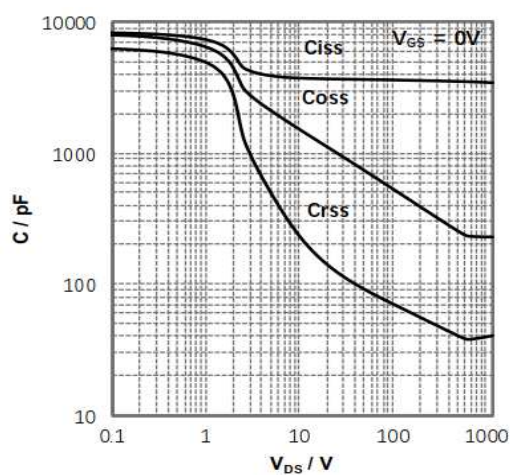
Figure 11. Body Diode Characteristics $T_J = 175^\circ\text{C}$ Figure 12. 3rd Quadrant Characteristics $T_J = 25^\circ\text{C}$ Figure 13. 3rd Quadrant Characteristics $T_J = 125^\circ\text{C}$ Figure 14. 3rd Quadrant Characteristics $T_J = 175^\circ\text{C}$ 

Figure 15. Capacitances vs. Drain-Source Voltage

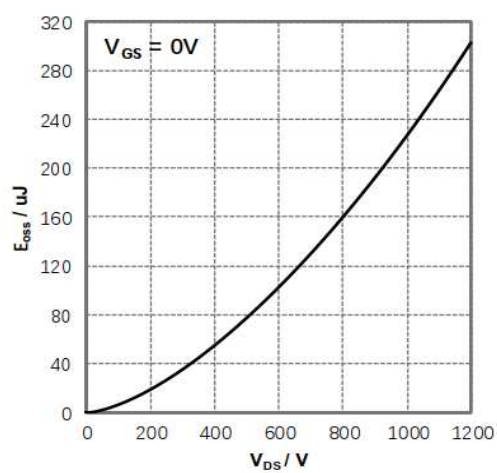


Figure 16. Output Capacitor Stored Energy

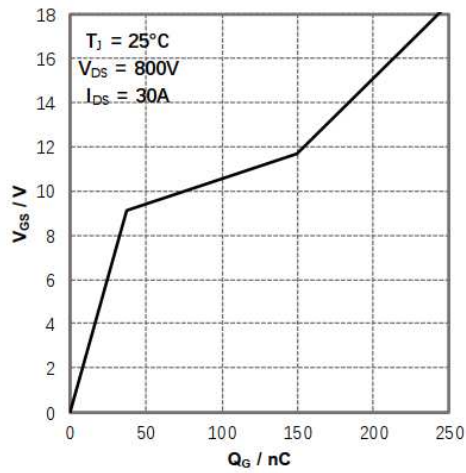


Figure 17. Gate Charge Characteristics

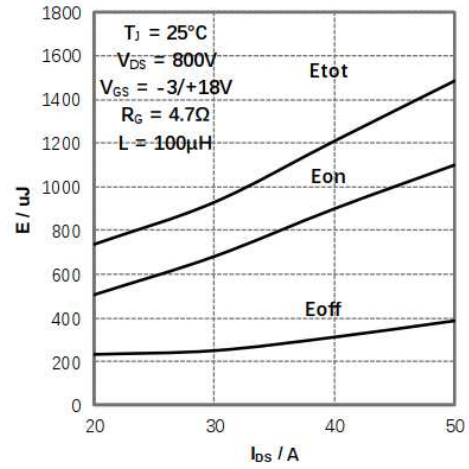


Figure 18. Switching Energy vs. Drain Current

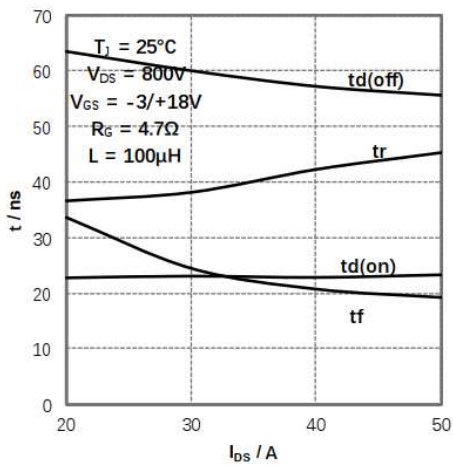


Figure 19. Switching Time vs. Drain Current

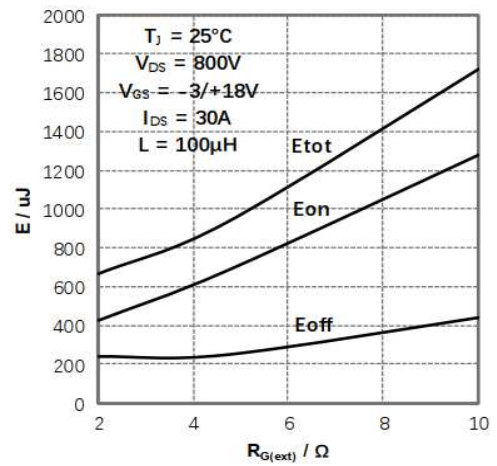


Figure 20. Switching Energy vs. $R_{G(ext)}$

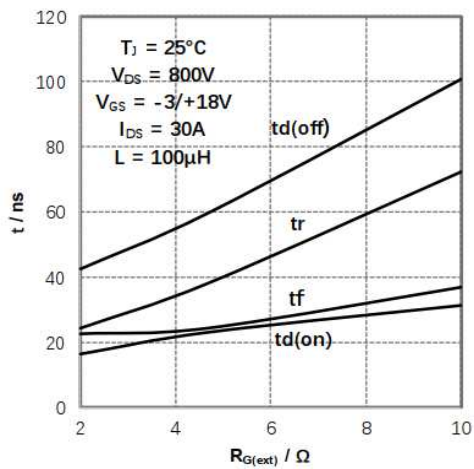


Figure 21. Switching Time vs. $R_{G(ext)}$

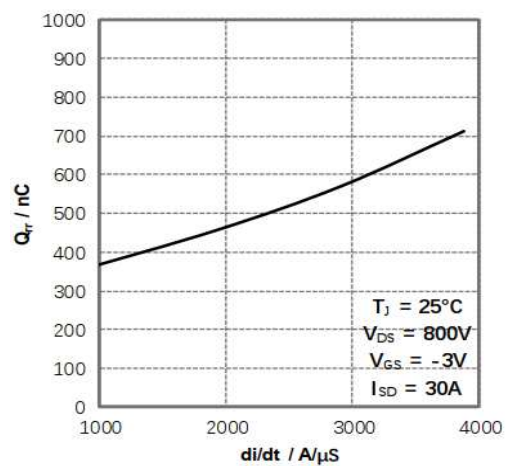


Figure 22. Reverse Recovery Charge vs. di/dt

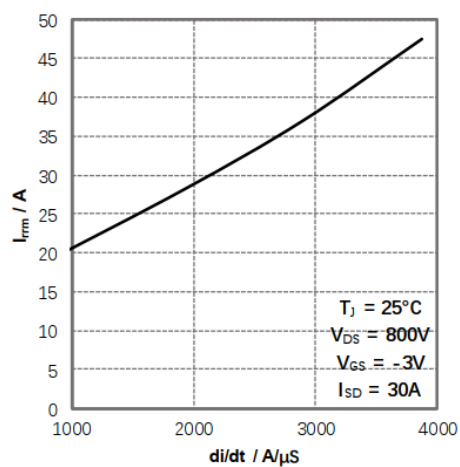


Figure 23. Reverse Recovery Current vs. di/dt

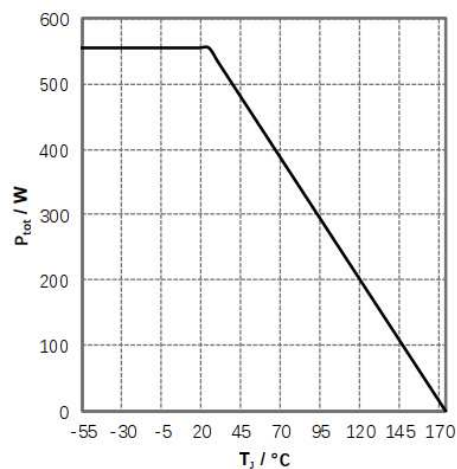


Figure 24. Power Dissipation Derating

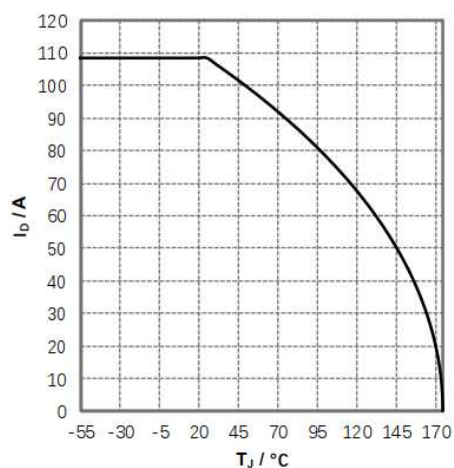


Figure 25. Continuous Drain Current Derating

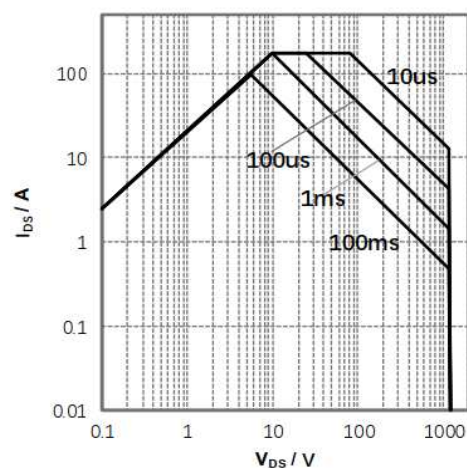


Figure 26. Safe Operating Area

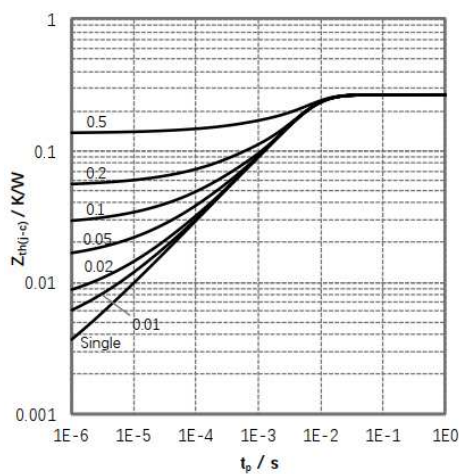
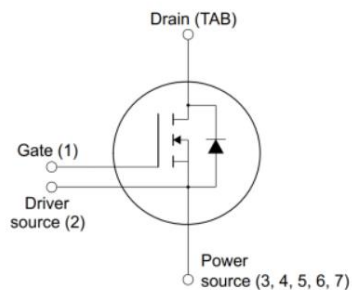
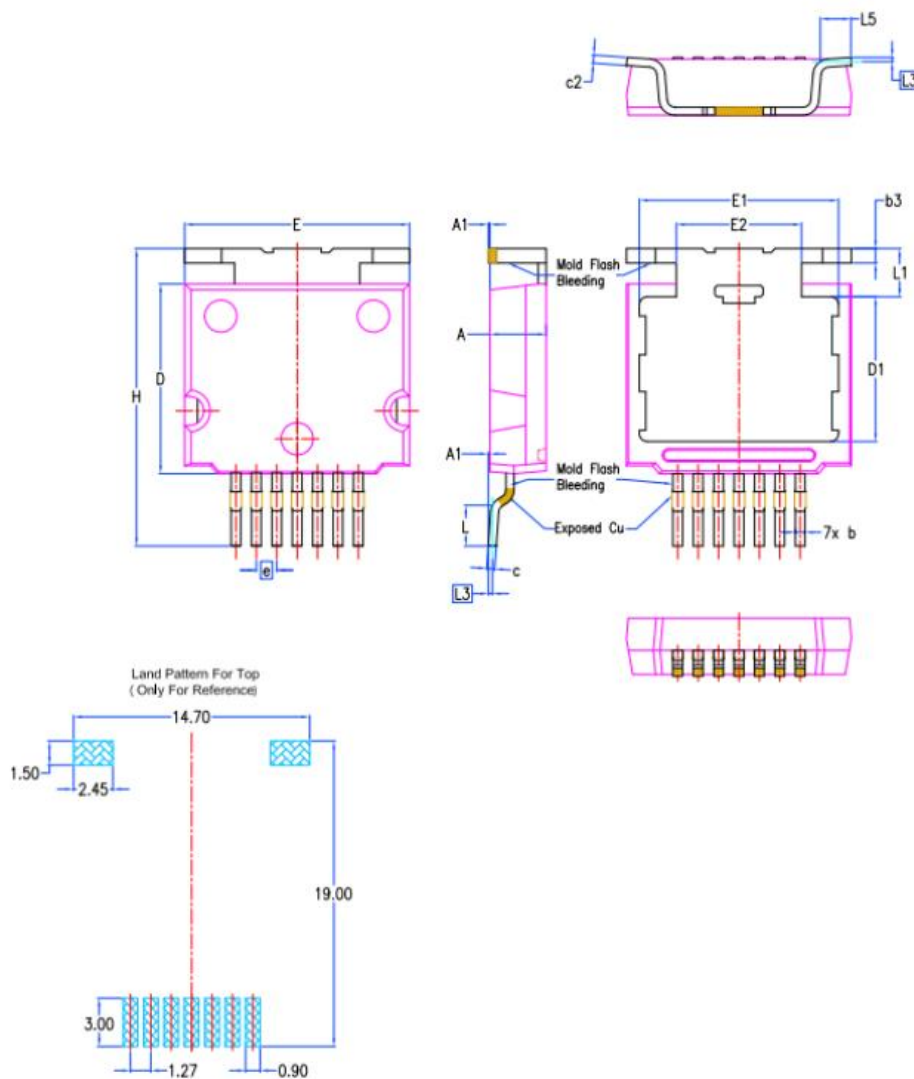


Figure 27. Transient Thermal Impedance (Junction-Case)

• Circuit diagram



• Package outlines : Dimensions in (mm)



SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
A	3.40	3.50	3.60
A1	0.00	0.10	0.25
b	0.50	0.60	0.70
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.10
E	13.90	14.00	14.10
E1	12.30	12.40	12.50
E2	7.75	7.80	7.85
e	1.27 BSC		
H	18.00	18.50	19.00
L	2.30	2.50	2.75
L1	—	3.05	—
L3	—	0.26	—
L5	1.70	1.90	2.15

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