

### 100V N-Channel Power MOSFET

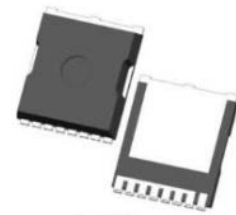
#### DESCRIPTION :

- High Speed Power Switching
- Low On-Resistance
- 100% UIS Tested, 100% Rg Tested
- RoHS compliant
- Halogen Free

$V_{DS}$	100V
$I_{D\_MAX}$	421A
$R_{DS(ON)\_MAX} @ V_{GS}=10V$	1.2m $\Omega$

#### TYPICAL APPLICATIONS :

- DC/DC in Telecoms and Industrial
- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit



TOLL

#### MAXIMUM RATINGS (at $T_C = 25^\circ\text{C}$ , unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	100	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$	$I_D$	421 297	A
Pulse Drain Current <sup>(1)</sup>		$I_{DM}$	1683	A
Single Pulse Avalanche Energy <sup>(2)</sup>		$E_{AS}$	2434	mJ
Single Pulse Avalanche Current	$L=0.3\text{mH}$	$I_{AS}$	84	A
Maximum Power Dissipation	$T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$	$P_D$	500 250	W
Junction & Storage Temperature Range		$T_J, T_{STG}$	-55~+175	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient		$R_{\theta JA}$	25	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case		$R_{\theta JC}$	0.3	$^\circ\text{C/W}$

#### Notes:

1.This current is calculated on single pulse with 10us Single Pulse.

2. Defined by design, not subject to production test, EAS condition:  $T_J=25^\circ\text{C}$ ,  $V_{DD}=50\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=1.0\text{mH}$ .

ELECTRICAL CHARACTERISTICS (at  $T_J = 25^\circ\text{C}$ , unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage $V_{GS} = 0\text{V}$ , $I_D = 250\mu\text{A}$	$V_{(BR)DSS}$	100			V
Zero Gate Voltage Drain Current $V_{DS} = 100\text{V}$ , $V_{GS} = 0\text{V}$ $T_J=25^\circ\text{C}$ $V_{DS} = 100\text{V}$ , $V_{GS} = 0\text{V}$ $T_J=125^\circ\text{C}$	$I_{DSS}$			1 100	$\mu\text{A}$
Gate-Source Leakage Current $V_{GS} = \pm 20\text{V}$ , $V_{DS} = 0\text{V}$	$I_{GSS}$			$\pm 100$	nA
Gate-Source Threshold Voltage $V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	$V_{GS(th)}$	2.0	3.0	4.0	V
Drain-Source On-State Resistance $V_{GS} = 10\text{V}$ , $I_D = 80\text{A}$	$R_{DS(ON)}$		0.98	1.2	$\text{m}\Omega$
Forward Transconductance $V_{DS} = 5\text{V}$ , $I_D = 20\text{A}$	$G_{fS}$		82		S
Input capacitance $f=1\text{MHz}$ , $V_{DS}=50\text{V}$ , $V_{GS}=0\text{V}$	$C_{iss}$		12889		pF
Output capacitance $f=1\text{MHz}$ , $V_{DS}=50\text{V}$ , $V_{GS}=0\text{V}$	$C_{oss}$		4379		pF
Reverse transfer capacitance $f=1\text{MHz}$ , $V_{DS}=50\text{V}$ , $V_{GS}=0\text{V}$	$C_{rss}$		150		pF
Gate Resistance $f=1\text{MHz}$ , $V_{DS}=0\text{V}$ , $V_{GS}=0\text{V}$	$R_g$		1.6		$\Omega$
Total Gate Charge $V_{DS}= 50\text{V}$ , $I_D= 80\text{A}$ , $V_{GS}= 10\text{V}$	$Q_G$		184		nC
Gate to Source Charge $V_{DS}= 50\text{V}$ , $I_D= 80\text{A}$ , $V_{GS}= 10\text{V}$	$Q_{GS}$		55		nC
Gate to Drain Charge $V_{DS}= 50\text{V}$ , $I_D= 80\text{A}$ , $V_{GS}= 10\text{V}$	$Q_{GD}$		43		nC
Turn-on delay time $V_{DS}=50\text{V}$ , $I_D=80\text{A}$ , $V_{GS}= 10\text{V}$ , $R_{GEN}=3\Omega$	$t_{d(ON)}$		23		ns
Rise time $V_{DS}=50\text{V}$ , $I_D=80\text{A}$ , $V_{GS}= 10\text{V}$ , $R_{GEN}=3\Omega$	$t_r$		57		ns
Turn-off delay time $V_{DS}=50\text{V}$ , $I_D=80\text{A}$ , $V_{GS}= 10\text{V}$ , $R_{GEN}=3\Omega$	$t_{d(OFF)}$		84		ns
Fall time $V_{DS}=50\text{V}$ , $I_D=80\text{A}$ , $V_{GS}= 10\text{V}$ , $R_{GEN}=3\Omega$	$t_f$		54		ns

## Body Diode

ELECTRICAL CHARACTERISTICS (at  $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Diode Forward Voltage $V_{GS} = 0V, I_S = 2.0A \quad T_J = 25^{\circ}\text{C}$	$V_{SD}$		0.7	1.2	V
Diode Forward Current $T_J = 25^{\circ}\text{C}$	$I_S$			421	A
Revers Recovery Time $I_F = 80A, dI/dt = 100A/\mu s \quad T_J = 25^{\circ}\text{C}$	$T_{rr}$		90		ns
Revers Recovery Charge $I_F = 80A, dI/dt = 100A/\mu s \quad T_J = 25^{\circ}\text{C}$	$Q_{rr}$		195		nC

## Typical Electrical and Thermal Characteristics

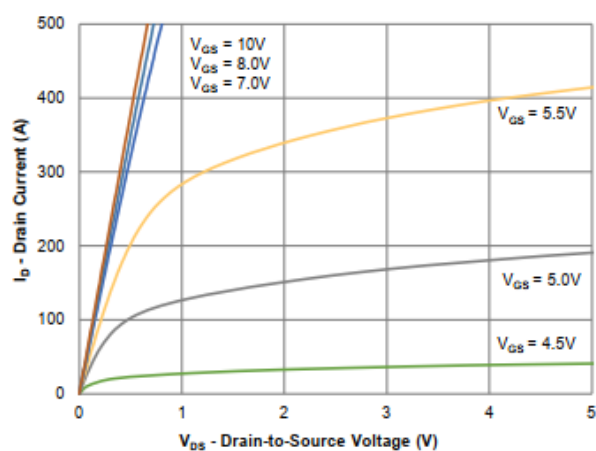
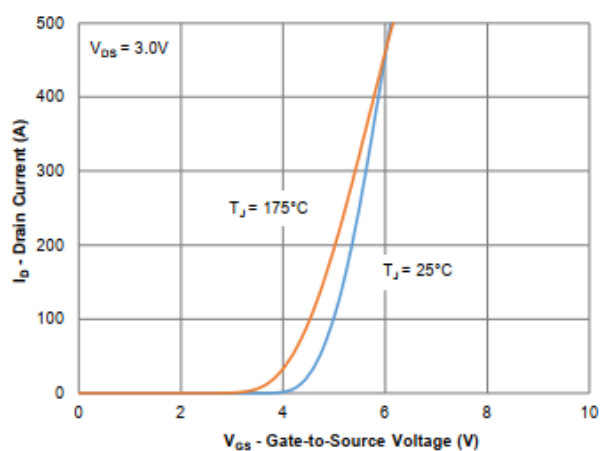
Figure 1. Typical output characteristics ( $T_J = 25^\circ\text{C}$ )

Figure 2. Typical Transfer Characteristics

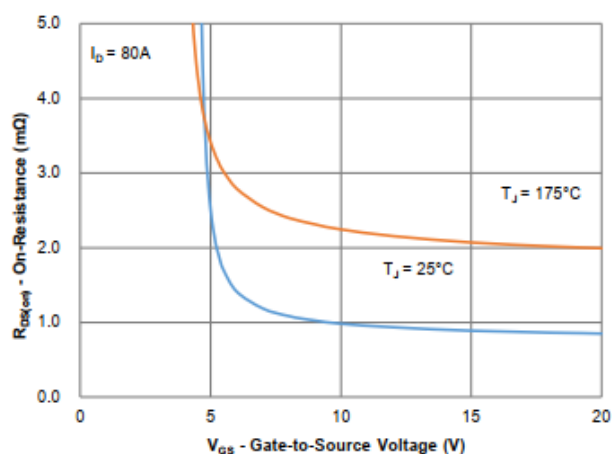


Figure 3. On-Resistance vs. Gate-Source Voltage

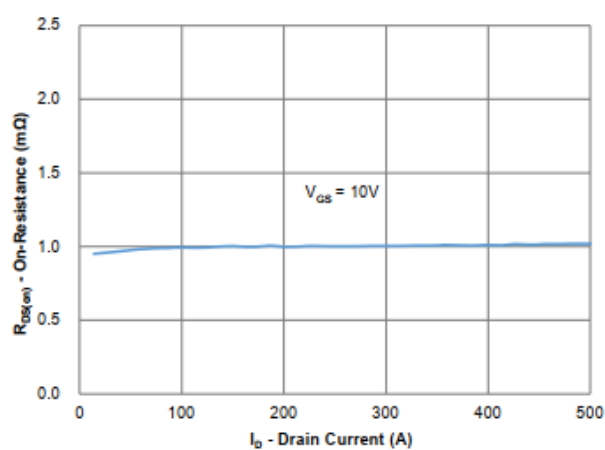


Figure 4. On-Resistance vs. Gate-Source Voltage

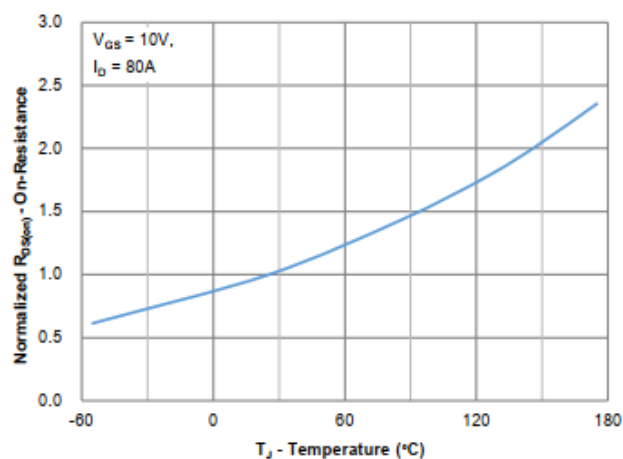


Figure 5. On-Resistance vs. Junction Temperature

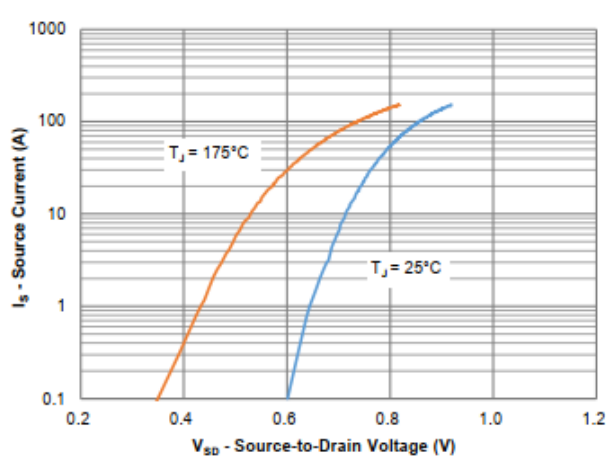


Figure 6. Source-Drain Diode Forward Voltage

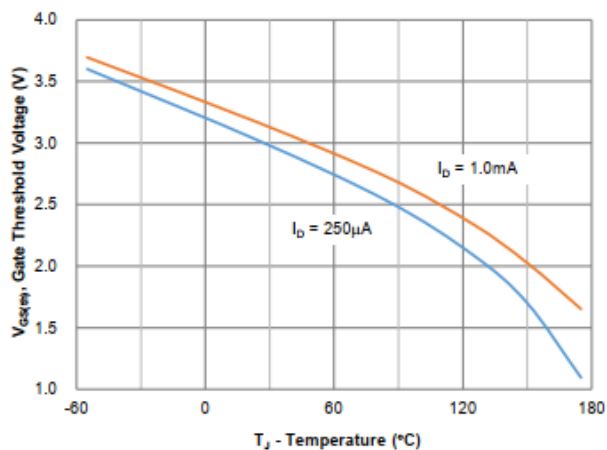


Figure 7. Gate Threshold Variation vs. Junction Temperature

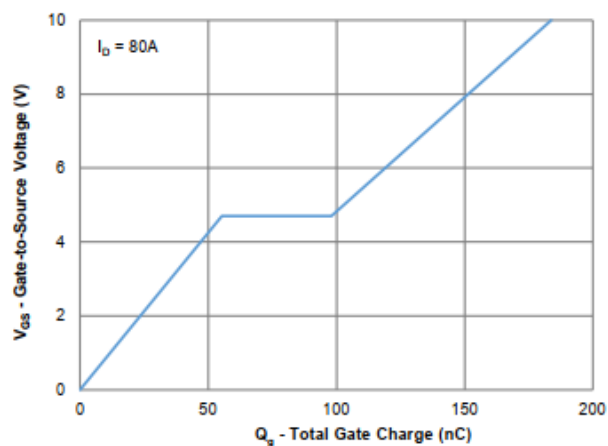


Figure 8. Gate Charge Characteristics

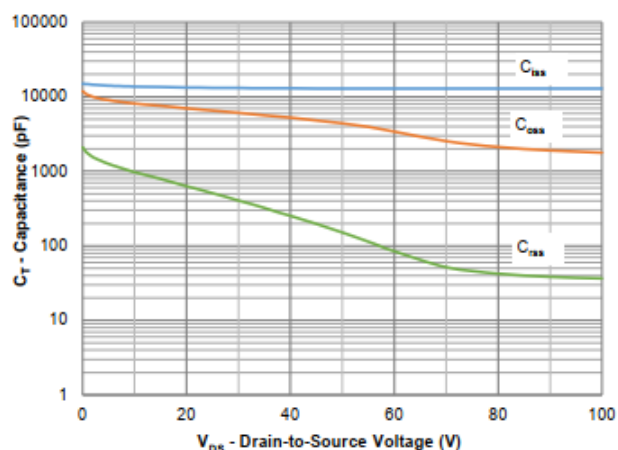


Figure 9. Capacitance Characteristics

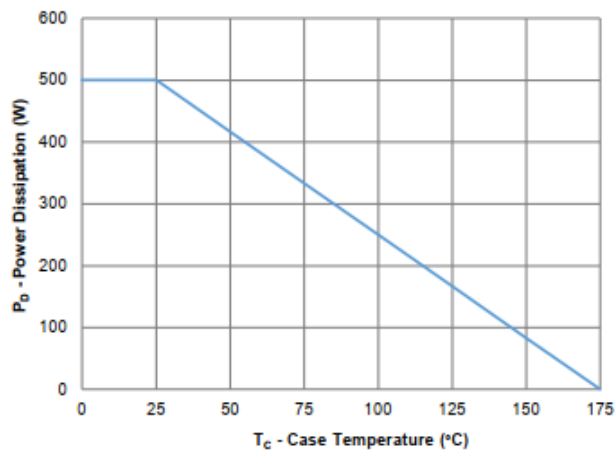


Figure 10. Power Derating

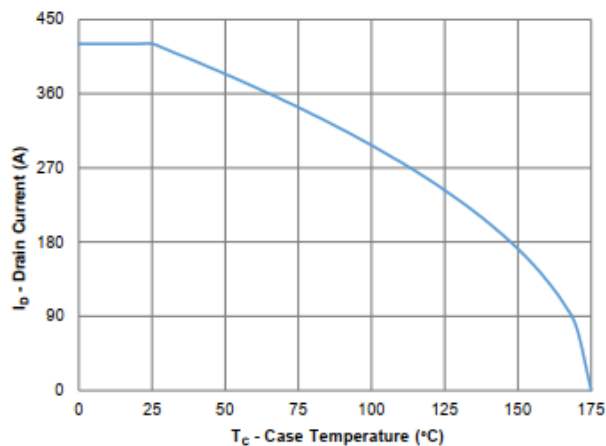


Figure 11. Current Derating

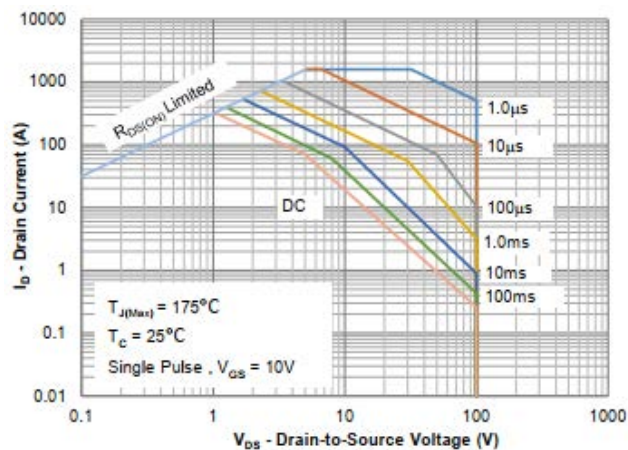


Figure 12. Safe Operating Area

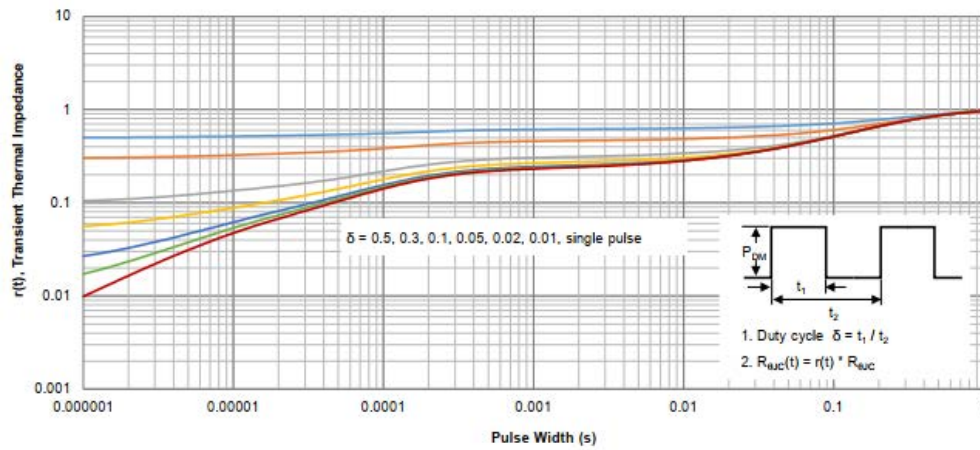
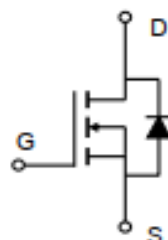
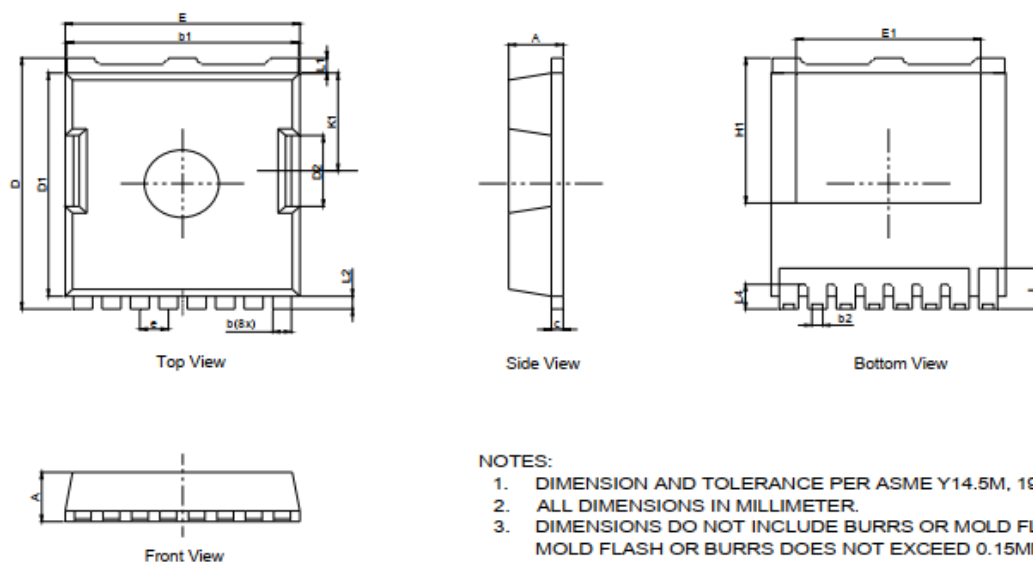


Figure 13. Max. Normalized Maximum Transient Thermal Impedance

- Circuit diagram



- TOLL Package outlines : Dimensions in (mm)



## NOTES:

1. DIMENSION AND TOLERANCE PER ASME Y14.5M, 1994.
2. ALL DIMENSIONS IN MILLIMETER.
3. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH. MOLD FLASH OR BURRS DOES NOT EXCEED 0.15MM.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
b	0.65	0.80	0.90
b1	9.65	9.80	9.95
c	0.40	0.50	0.60
D	11.48	11.68	11.95
D1	10.25	—	10.70
D2	2.85	—	3.40
E	9.70	9.90	10.10
E1	8.00	—	9.25
e	1.20 (BSC)		
H1	6.70	7.00	7.30
K1	4.55		
L	1.35	—	2.10
L1	0.70		
L2	0.60		
L4	0.95	1.20	1.35

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