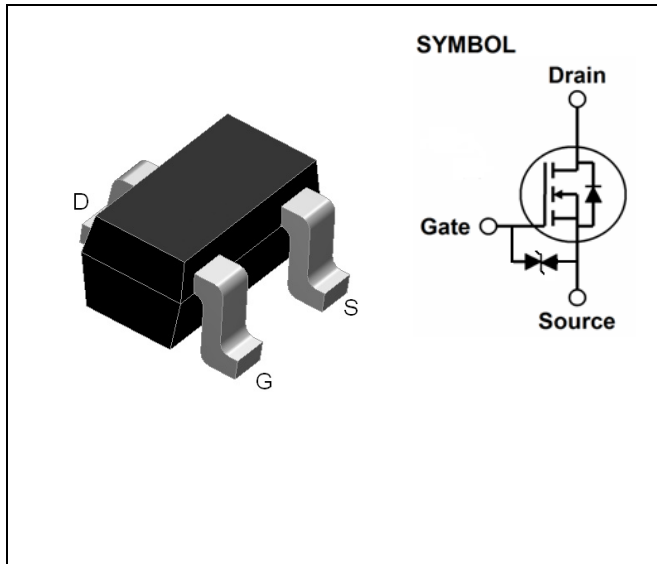


N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR



FEATURES

- N-channel Enhancement Mode Field Effect Transistor, Designed For High Speed Pulse Amplifier And Drive Application, Which Is Manufactured By The N-Channel DMOS Process.
- ESD MIL-STD 833, $\pm 2.5\text{KV}$ Contact Discharge Compliant Protection

MECHANICAL DATA

- Available in SOT-523 Package
- Solderability : MIL-STD-202, Method 208
- Full RoHS Compliance

ORDERING INFORMATION

PART NUMBER	PACKAGE	SHIPPING	MARKING CODE
2N7002T□-5T3R	SOT-523	Tape Reel	K72

Notes:

1. □: none is for Lead Free package;
"G" is for Halogen Free package.

THERMAL DATA

PARAMETER	SYMBOL	VALUES	UNIT
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	625	$^{\circ}\text{C/W}$

Notes:

2. The value of $R_{\theta JA}$ is measured with device mounted on 1 in² FR-4 board with 2 oz copper.

ABSOLUTE MAXIMUM RATINGS

$T_A = 25^{\circ}\text{C}$, unless otherwise noted. (Note 3)

PARAMETER	SYMBOL	VALUE	UNIT
Drain-Source Voltage	V_{DSS}	60	V
Drain-Gate Voltage	V_{DGR}	60	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current (Continuous)	I_D	250	mA
Maximum Drain Current-Continue	P_D	200	mW
Maximum Junction Temperature	T_J	-55 to +150	$^{\circ}\text{C}$
Storage Junction Temperature	T_{stg}	-55 to +150	$^{\circ}\text{C}$

Notes:

- Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
- Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS

$T_C = 25^\circ\text{C}$, unless otherwise noted.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 10μA	60			V
		V _{GS} = 0V, I _D = 3.0mA	60			
Drain-Source Leakage Current	I _{DSS}	V _{GS} = 0V, V _{DS} = 60V, T _J = 25°C			1	μA
		V _{GS} = 0V, V _{DS} = 60V, T _J = 125°C			500	
Gate- Source Leakage Current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±10	μA
ON CHARACTERISTICS ^(Note 5)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.5	2.5	V
Static Drain-Source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =500mA, T _J = 25°C		1.5	7.5	Ω
		V _{GS} =5V, I _D =50mA, T _J = 25°C		2.0	7.5	
Drain-Source On Voltage	V _{DS(ON)}	V _{GS} =10V, I _D =500mA			3.75	V
		V _{GS} =5V, I _D =50mA			1.5	
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz		25	50	pF
Output Capacitance	C _{oss}			6	25	
Reverse Transfer Capacitance	C _{rss}			1.2	5	
SWITCHING CHARACTERISTICS						
Turn-On Time	T _(on)	V _{DD} = 25V, I _D = 500mA,		7.5	20	nS
Turn-Off Time	t _(off)	R _L =50Ω, V _{GEN} =10V, R _G =25Ω		7.5	20	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Drain-source Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = 115mA		0.85	1.2	V
On-State Drain Current	I _{D(ON)}	V _{DS} = 7V, V _{GS} = 10V	500			mA
Forward Tran Conductance	G _{TS}	V _{DS} =10V, I _D =200mA	80	300		mS

Notes:

5. Pulse test : Pulse width $\leq 300 \mu S$, Duty cycle $\leq 2\%$

TYPICAL PERFORMANCE CHARACTERISTICS

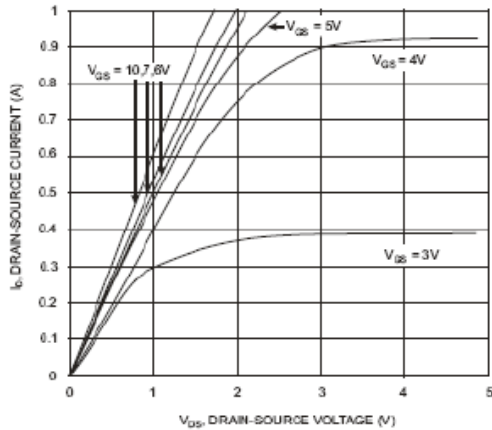


Fig. 1 On-Region Characteristics

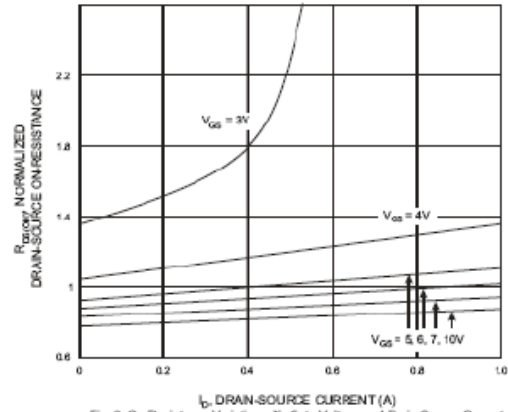


Fig. 2 On-Resistance Variation with Gate Voltage and Drain-Source Current

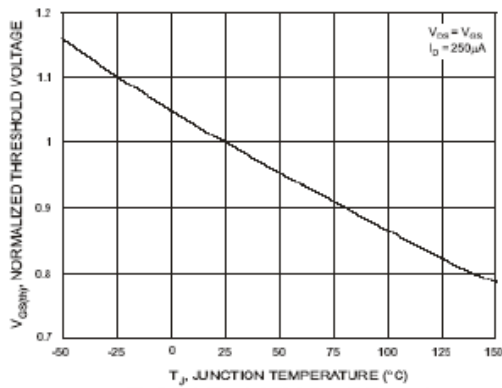


Fig. 3 Gate Threshold Variation with Temperature

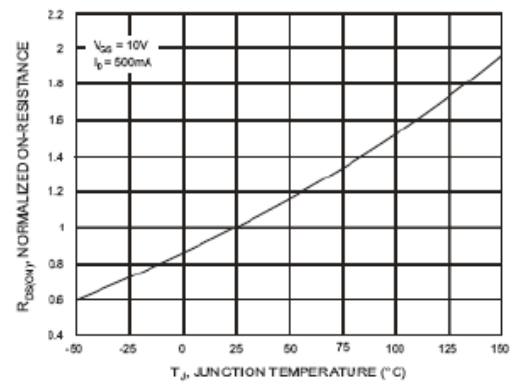


Fig. 4 On-Resistance Variation with Temperature

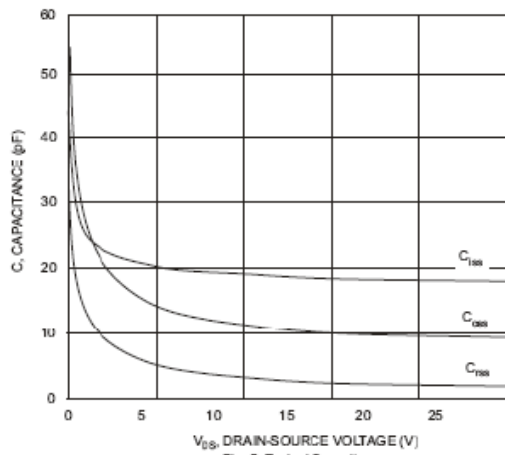


Fig. 5 Typical Capacitance

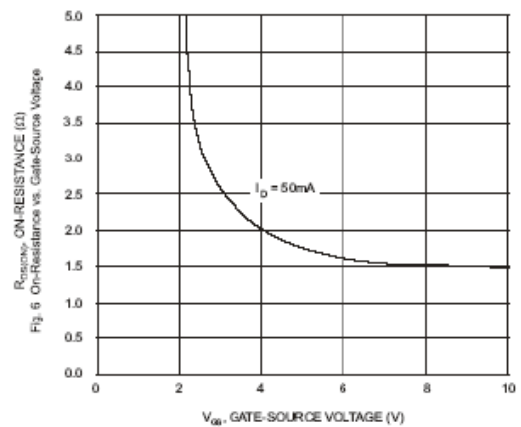


Fig. 6 On-Resistance vs. Gate-Source Voltage

PHYSICAL DIMENSION

Unit : Inch (Millimeter)

