

### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	$3$ m $\Omega$	176A

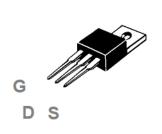
#### **Features**

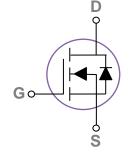
- 30V, 176A,  $RDS(ON) = 3m\Omega@VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

## **Applications**

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

# **TO220 Pin Configuration**





# **Absolute Maximum Ratings** Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>G</sub> s	Gate-Source Voltage	±20	V
I_	Drain Current – Continuous (Tc=25°C)	176	А
ID	Drain Current – Continuous (Tc=100°C)	111	А
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	704	Α
EAS	Single Pulse Avalanche Energy <sup>2</sup>	180	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	60	Α
D-	Power Dissipation (Tc=25°C)	168	W
P <sub>D</sub>	Power Dissipation – Derate above 25°C	1.34	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	℃
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Characteristics**

Symbol Parameter		Тур.	Max.	Unit
R <sub>0JA</sub> Thermal Resistance Junction to ambient			62	°C/W
R <sub>0JC</sub> Thermal Resistance Junction to Case			0.74	°C/W



# **30V N-Channel MOSFETs**

# **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

## **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			٧
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.03		V/°C
I <sub>DSS</sub> Drain-Source Leakage Current		V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> Static Drain-Source On-Resistance <sup>3</sup>	Static Drain Source On Posictance <sup>3</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =30A		2.4	3	mΩ
	V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		3.2	4	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	V V I 250	1.2	1.6	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	─V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-5		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =2A		16		S

# **Dynamic Characteristics**

0	Total Gate Charge <sup>3,4</sup>	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =24A	 40	75	
Qg	Total Gate Charge		 82	160	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>	V <sub>DS</sub> =15V , V <sub>GS</sub> =10V , I <sub>D</sub> =24A	 12	24	nc nc
$Q_gd$	Gate-Drain Charge <sup>3, 4</sup>		 17	35	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3,4</sup>		 20	40	
Tr	Rise Time <sup>3, 4</sup>	$V_{DD}$ =15 $V$ , $V_{GS}$ =10 $V$ , $R_{G}$ =1 $\Omega$	 32	60	ns
$T_{d(off)}$	Turn-Off Delay Time <sup>3,4</sup>	I <sub>D</sub> =1A	 75	130	115
Tf	Fall Time <sup>3, 4</sup>		 28	55	
Ciss	Input Capacitance		 4800	8000	
Coss	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz	 735	1300	pF
Crss	Reverse Transfer Capacitance		 420	800	
$R_g$	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	 1.6	3.5	Ω

#### **Drain-Source Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V- V- OV Force Current			176	Α
Isм	Pulsed Source Current <sup>3</sup>	-V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			352	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>3</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V
t <sub>rr</sub>	Reverse Recovery Time	VR=30V, Is=10A		140		ns
Qrr	Reverse Recovery Charge	di/dt=100A/µs, Tյ=25°C		300		nC

#### Note :

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2.  $V_{DD}$ =25V, $V_{GS}$ =10V,L=0.1mH, $I_{AS}$ =60A., $R_G$ =25 $\Omega$ ,Starting  $T_J$ =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%.
- 4. Essentially independent of operating temperature.

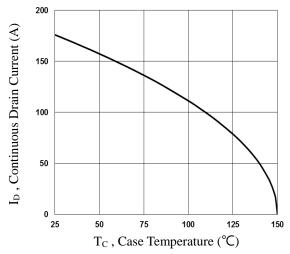


Fig.1 Continuous Drain Current vs. Tc

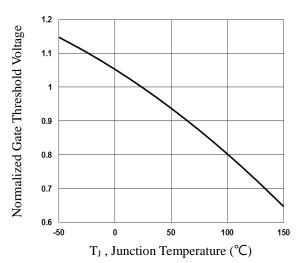


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

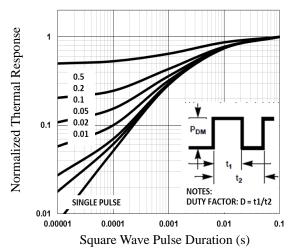


Fig.5 Normalized Transient Impedance

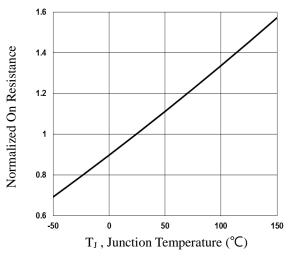


Fig.2 Normalized RDSON vs. TJ

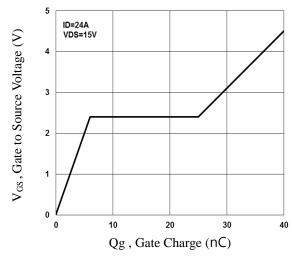


Fig.4 Gate Charge Waveform

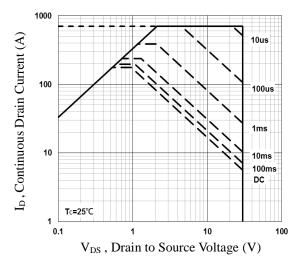


Fig.6 Maximum Safe Operation Area





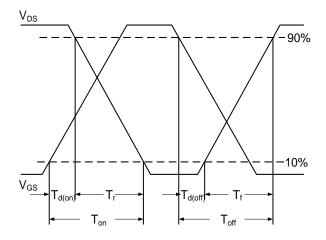


Fig.7 Switching Time Waveform

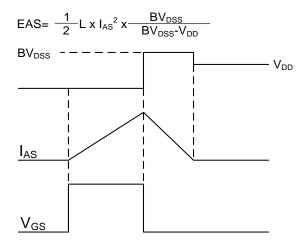
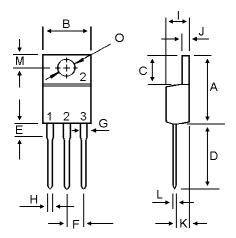


Fig.8 EAS Waveform



# **TO220 PACKAGE INFORMATION**



	MILLIMETERS			
DIM	MIN	MAX		
Α	14.68	16.00		
В	9.78	10.42		
С	5.02	6.60		
D	13.00	14.62		
Е	3.10	4.19		
F	2.41	2.67		
G	1.10	1.67		
Н	0.69	1.01		
_	4.22	4.98		
J	1.14	1.40		
K	2.20	3.30		
L	0.28	0.61		
M	2.48	3.00		
0	3.50	4.00		



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