

650V 40A Trench and Field Stop IGBT

DESCRIPTION :

- High ruggedness performance
- 10 μ s short circuit capability
- Positive $V_{CE(SAT)}$ temperature coefficient
- High efficiency for motor control
- Excellent current sharing in parallel operation
- RoHS compliant.



TYPICAL APPLICATIONS :

- Home appliances
- Motor drives
- General inverter

TO-247

IGBT

MAXIMUM RATINGS (Tvj=25°C unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Collector-Emitter Voltage		V_{CES}	650	V
Continuous collector current	Tc=25°C Tc=100°C	$I_{C\ nom}$	80 40	A
Pulsed collector current	t _p limited by Tvjmax	I_{CM}	160	A
Gate emitter voltage		V_{GE}	±20	V
Short circuit withstand time		t _{SC}	10	us
Power dissipation	Tc=25°C Tc=100°C	P_{tot}	300 150	W
Temperature under switching conditions		Tvj op	-40~+175	°C
Storage temperature		T _{STG}	-55~+150	°C

THERMAL CHARACTERISTICS

Characteristic	Condition	Symbol	Max.	Unit
IGBT thermal resistance, junction - case		$R_{th(j-C)}$	0.5	K/W
Diode thermal resistance, junction - case		$R_{th(j-C)}$	0.9	K/W
Thermal resistance, junction - ambient		$R_{th(j-A)}$	40	K/W

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Collector-emitter cut-off current VCE=650V, VGE=0V Tvj=25°C	I_{CES}			50	μA
Gate-emitter leakage current VCE=0V, VGE=20V Tvj=25°C	I_{GES}			100	nA
Gate-Emitter threshold voltage IC=1.0mA, VGE= VCE Tvj=25°C	$V_{GE(th)}$	4.0	5.0	6.0	V
Collector-Emitter saturation voltage VGE=15V, IC=40A Tvj=25°C VGE=15V, IC=40A Tvj=150°C	$V_{CE(SAT)}$		1.9 2.3		V
Input capacitance f=1MHz, VCE=30 V, VGE=0 V Tvj=25°C	C_{ies}		2480		pF
Output capacitance f=1MHz, VCE=30 V, VGE=0 V Tvj=25°C	C_{oes}		95		pF
Reverse transfer capacitance f=1MHz, VCE=30 V, VGE=0 V Tvj=25°C	C_{res}		21		pF
Gate charge IC = 40A, VGE = 15 V, VCC = 520V Tvj=25°C	Q_G		78		nC
Turn-on delay time IC=40A, VCC=400 V Tvj=25°C VGE=0/15 V, RG=10Ω Tvj=150°C (inductive load)	$t_{d(ON)}$		32 28		ns
Rise time IC=40A, VCC=400 V Tvj=25°C VGE=0/15 V, RG=10Ω Tvj=150°C (inductive load)	t_r		55 52		ns
Turn-off delay time IC=40A, VCC=400 V Tvj=25°C VGE=0/15 V, RG=10Ω Tvj=150°C (inductive load)	$t_{d(OFF)}$		106 128		ns
Fall time IC=40A, VCC=400 V Tvj=25°C VGE=0/15 V, RG=10Ω Tvj=150°C (inductive load)	t_f		51 75		ns
Turn-on energy IC=40A, VCC=400 V Tvj=25°C VGE=0/15 V, RG=10Ω Tvj=150°C (inductive load)	$E_{(ON)}$		0.9 0.9		mJ

Turn-off energy loss per pulse IC=40A, VCC=400 V Tvj=25°C VGE=0/15 V, RG=10Ω Tvj=150°C (inductive load)	$E_{(OFF)}$		0.5 0.9		mJ
--	-------------	--	------------	--	----

Diode

MAXIMUM RATINGS (Tvj=25°C unless otherwise specified)

Characteristic	Condition	Symbol	Value	Unit
Repetitive peak reverse voltage	Tvj=25°C	V_{RRM}	650	V
Continuous forward current	Tc=100°C	I_F	40	A
Diode maximum current	t_P limited by Tvj max	I_{FM}	160	A

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Forward voltage IF=40A, VGE=0 V Tvj=25°C IF=40A, VGE=0 V Tvj=150°C	V_F		2.4 1.8		V
Reverse Recovered Time IF=40 A, Tvj=25°C -diF/dt =950A/μs Tvj=150°C VR=400 V	T_{rr}		68 106		ns
Peak reverse recovery current IF=40 A, Tvj=25°C -diF/dt =950A/μs Tvj=150°C VR=400 V	I_{RRM}		15 24		A
Reverse Recovered charge IF=40 A, Tvj=25°C -diF/dt =950A/μs Tvj=150°C VR=400 V	Q_{rr}		522 1423		nC

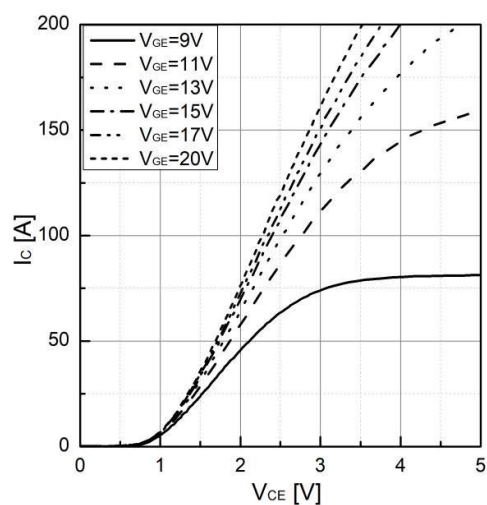


Figure 1. Typical output characteristics ($T_{vj}=25^{\circ}\text{C}$)

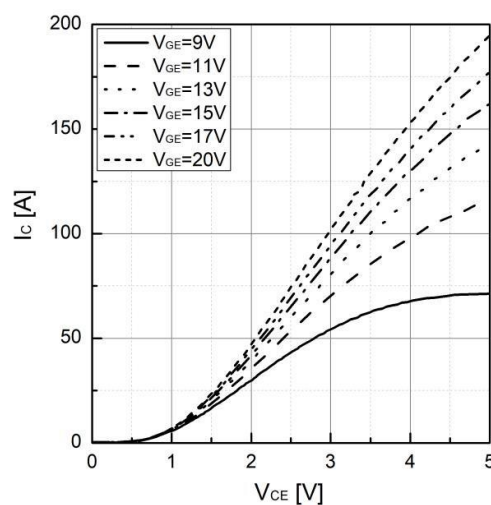


Figure 2. Typical output characteristics ($T_{vj}=175^{\circ}\text{C}$)

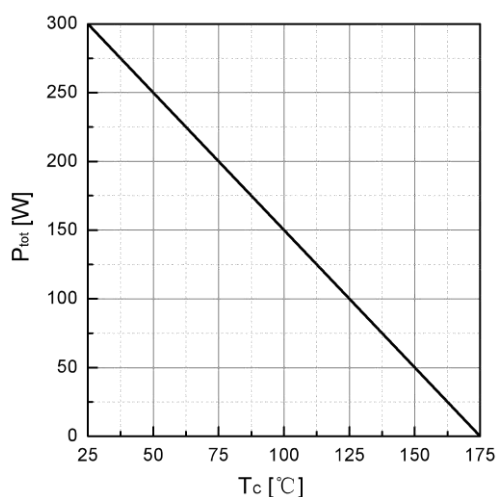


Figure 3. Power dissipation as a function of T_c

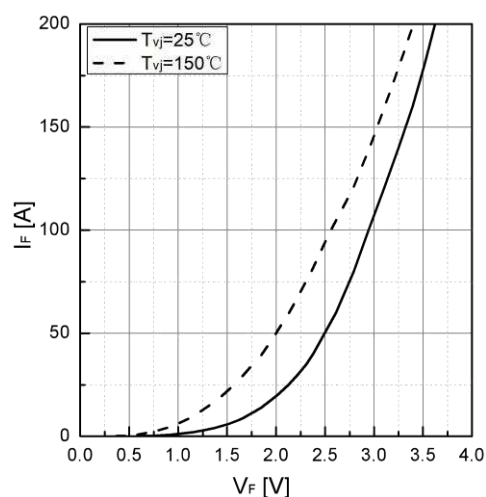


Figure 4. Typical I_F as a function of V_F

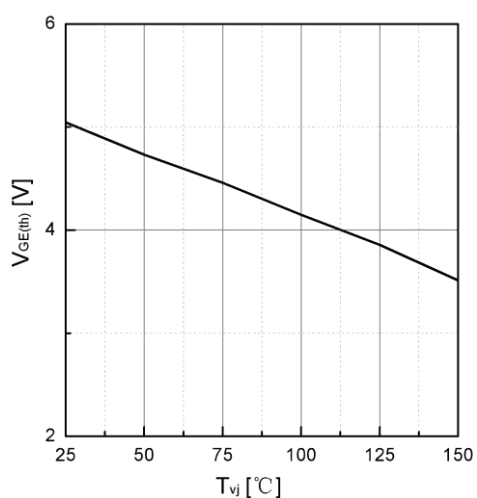


Figure 5. Typical $V_{GE(th)}$ as a function of T_{vj} ($I_c=1\text{mA}$)

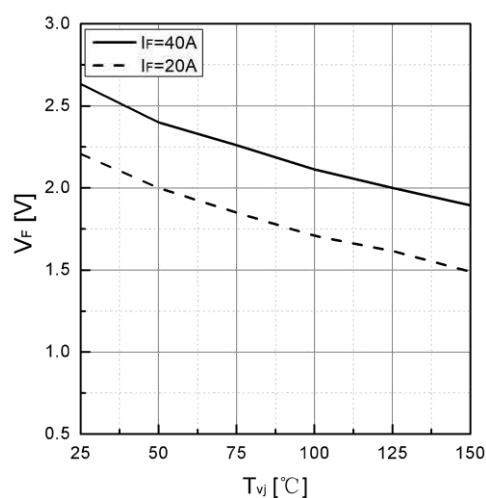


Figure 6. Typical V_F as a function of T_{vj}

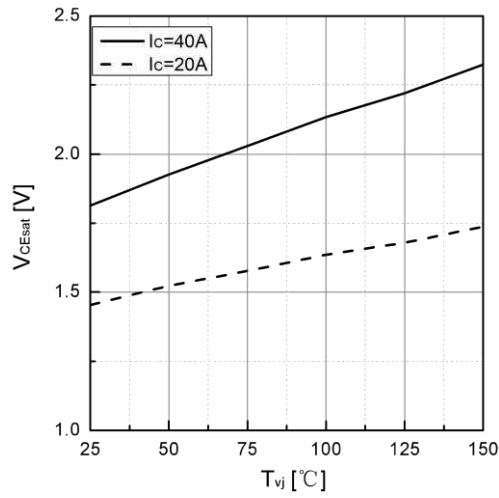


Figure 7. Typical V_{CEsat} as a function of T_{vj}

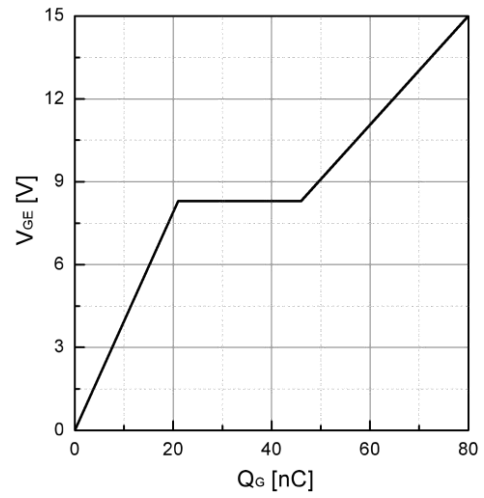


Figure 8. Typical Gate charge

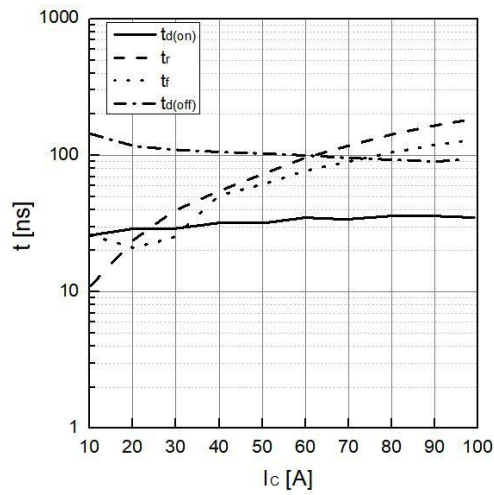


Figure 9. Typical switching times as a function of I_C

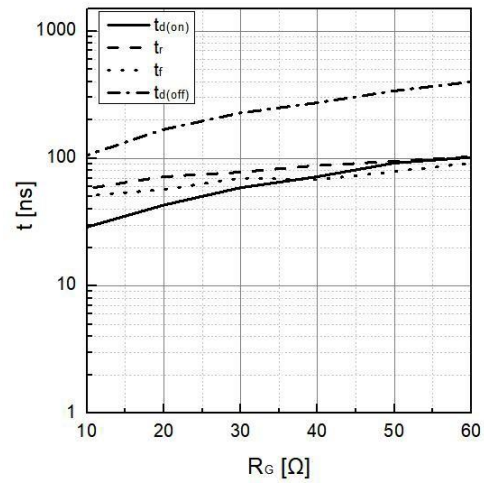


Figure 10. Typical switching times as a function of R_G

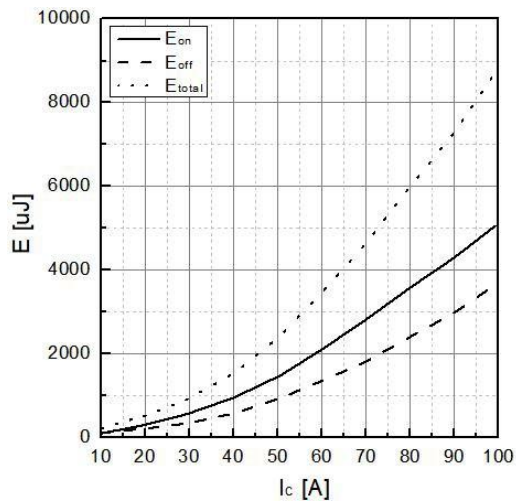


Figure 11. Typical switching energy losses as a function of I_C

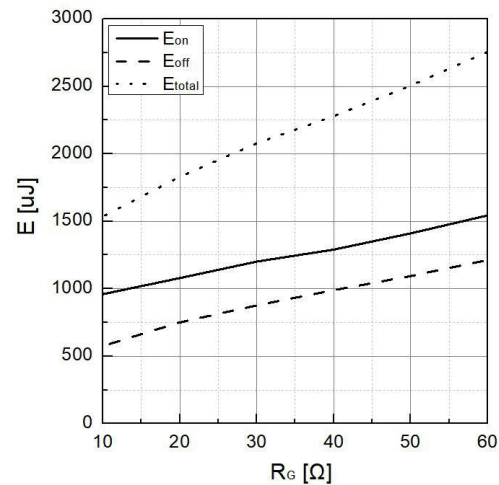


Figure 12. Typical switching energy losses as a function of R_G

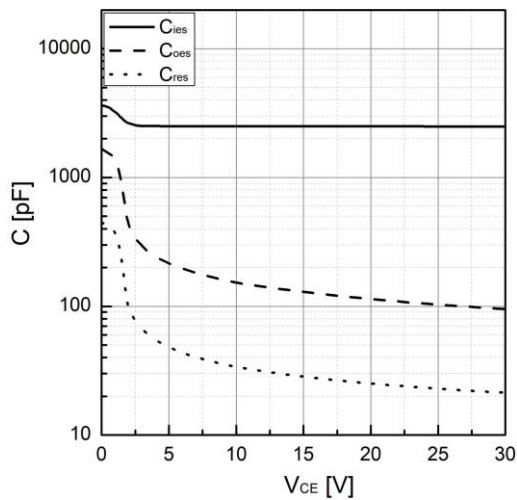


Figure 13. Typical capacitance as a function of VCE (f=1Mhz, VGE=0V)

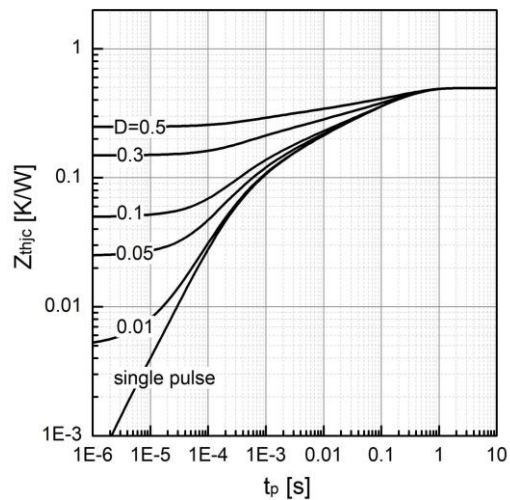
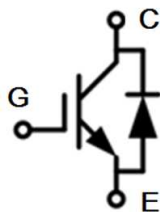
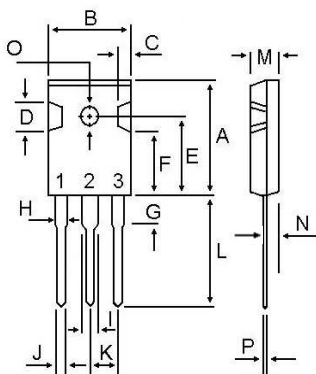


Figure 14. Transient thermal impedance, IGBT

• Circuit diagram



• Package outlines : Dimensions in (mm)



DIM	MILLIMETERS	
	MIN	MAX
A	20.80	21.80
B	15.38	16.20
C	1.90	2.70
D	5.10	6.10
E	14.50	15.50
F	11.20	13.20
G	3.75	4.35
H	1.90	2.30
I	2.90	3.30
J	1.00	1.40
K	5.26	5.66
L	19.50	20.50
M	4.68	5.36
N	2.30	2.60
O	3.45	3.85
P	0.48	0.72

Notice

MOSPEC reserves the rights to make changes of the content herein the document anytime without notification. MOSPEC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies. Please refer to MOSPEC website for the last document.

MOSPEC disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially incurred.

Application shown on the herein document are examples of standard use and operation. Customers are responsible for comprehending suitable use in particular applications. MOSPEC makes no representation or warranty that such application will be suitable for the specified use without further testing or modification.

The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by MOSPEC for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of MOSPEC or others.

These MOSPEC products are intended for usage in general electronic equipment. Please make sure to consult with MOSPEC before you use these MOSPEC products in equipment which require specialized quality and/or reliability, and in equipment which could have major impact to the welfare of human life (atomic energy control, aeronautics , traffic control, combustion control, safety devices etc.)