MOSPEC

Surface Mount High Efficiency Power Rectifiers

Ideally suited for high voltage, high frequency rectification, or as free wheeling and protection diodes in surface mount applications where compact size and weight are critical to the system.

- * Low Power Loss, High efficiency
- * Glass Passivated chips junction
- * 150 °C Operating Junction Temperature
- * Low Stored Charge Majority Carrier Conduction
- * Low Forward Voltage Drop , High Current Capability
 * High-Switching Speed 50 & 75 Nanosecond Recovery Time
- * Small Compact Surface Mountable Package with J-Bend Lead
- * Plastic Material used Carries Underwriters Laboratory Flammability Classification 94V-O

MAXIMUM RATINGS

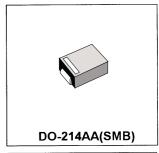
Characteristic	Symbol	MH51	MH52	MH53	MH54	MH55	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	50	100	200	300	400	٧
RMS Reverse Voltage	V _{R(RMS)}	35	70	140	210	280	٧
Average Rectifier Forward Current	lo	5.0			Α		
Non-Repetitive Peak Surge Current (Surge applied at rate load conditions halfware, single phase, 60Hz)	I _{FSM}	100			Α		
Operating and Storage Junction Temperature Range	T _J , T _{stg}		- 6	5 to +	150		°C

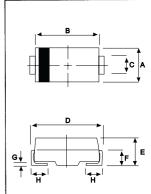
ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	MH51	MH52	MH53	MH54	MH55	Unit
Maximum Instantaneous Forward Voltage $(I_F=5.0 \text{ Amp}, T_C=25 ^{\circ}\text{C})$	V _F		1.00		1.	30	V
Maximum Instantaneous Reverse Current (Rated DC Voltage, T _c = 25 °C) (Rated DC Voltage, T _c = 125 °C)	I _R	5.0 70			uA		
Reverse Recovery Time (I _F = 0.5 A, I _R =1.0, I _{rr} =0.25 A)	T _{rr}		50		7	'5	ns
Typical Junction Capacitance (Reverse Voltage of 4 volts & f=1 MHz)	C _P		55		4	5	pF

HIGH EFFICIENCY RECTIFIERS

5.0 AMPERES 50 -- 400 VOLTS





DIM	MILLMETERS				
	MIN	MAX			
Α	3.30	3.90			
В	4.20	4.60			
С	1.80	2.20			
D	4.90	5.60			
Ε	1.90	2.50			
F		1.30			
G		0.22			
Н	0.85	1.45			

CASE---Transfer molded plastic

POLARITY---Cathode indicated polarity band

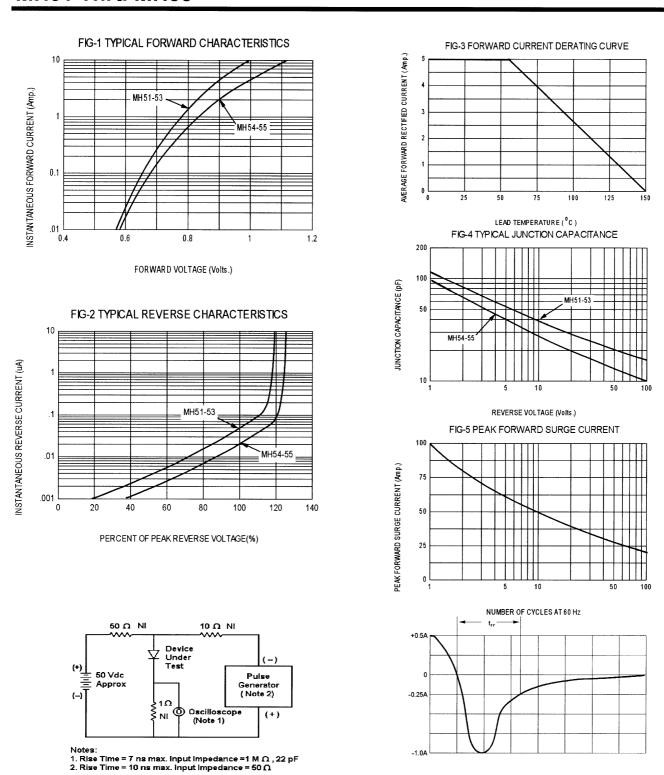


Fig-6 Reverse Recovery Time Characteristic and Test Circuit Diagram

-1.0A

Set time base for 20 ns/div



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