

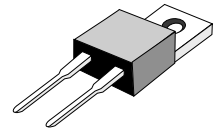
High Efficiency Recovery Rectifier Diodes

... Designed for use in switching power supplies, inverters and as free wheeling diodes. These state-of-the-art devices have the following features:

- * High Surge Capacity
- * Low Power Loss, High efficiency
- * Glass Passivated chip junctions
- * 150 °C Operating Junction Temperature
- * Low Stored Charge Majority Carrier Conduction
- * Low Forward Voltage , High Current Capability
- * High-Switching Speed 75 & 100 Nanosecond Recovery Time
- * Plastic Material used Carries Underwriters Laboratory Flammability Classification 94V-O

**HIGH EFFICIENCY
RECTIFIERS**

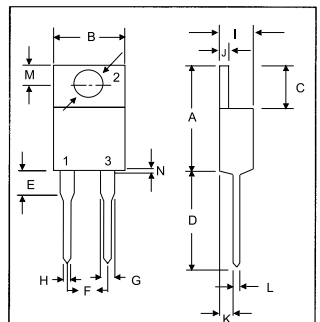
**8 AMPERES
300 -- 600 VOLTS**



TO-220A

MAXIMUM RATINGS

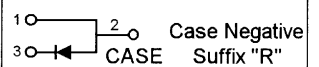
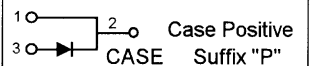
Characteristic	Symbol	H08A				Unit
		30	40	50	60	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	300	400	500	600	V
RMS Reverse Voltage	$V_{R(RMS)}$	210	280	350	420	V
Average Rectifier Forward Current	$I_{F(AV)}$	8.0				A
Non-Repetitive Peak Surge Current (Surge applied at rate load conditions halfwave, single phase, 60Hz)	I_{FSM}	125				A
Operating and Storage Junction Temperature Range	T_J, T_{stg}	- 65 to + 150				°C



DIM	MILLMETERS	
	MIN	MAX
A	14.68	15.32
B	9.78	10.42
C	6.01	6.52
D	13.06	14.62
E	3.57	4.07
F	4.83	5.33
G	1.12	1.36
H	0.72	0.96
I	4.22	4.98
J	1.14	1.36
K	2.20	2.97
L	0.33	0.55
M	2.48	2.98
N	--	1.00
O	3.70	3.90

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	H08A				Unit
		30	40	50	60	
Maximum Instantaneous Forward Voltage ($I_F=8.0$ Amp, $T_C=25$ °C) ($I_F=8.0$ Amp, $T_C=100$ °C)	V_F	1.30 1.12		1.50 1.34		V
Maximum Instantaneous Reverse Current (Rated DC Voltage, $T_C=25$ °C) (Rated DC Voltage, $T_C=100$ °C)	I_R		10 500			uA
Reverse Recovery Time ($I_F=0.5$ A, $I_R=1.0$ A , $I_{rr}=0.25$ A)	T_{rr}		75		100	ns
Typical Junction Capacitance (Reverse Voltage of 4 volts & f=1 MHz)	C_p		70			pF



H08A30 Thru H08A60

FIG-1 TYPICAL FORWARD CHARACTERISTICS

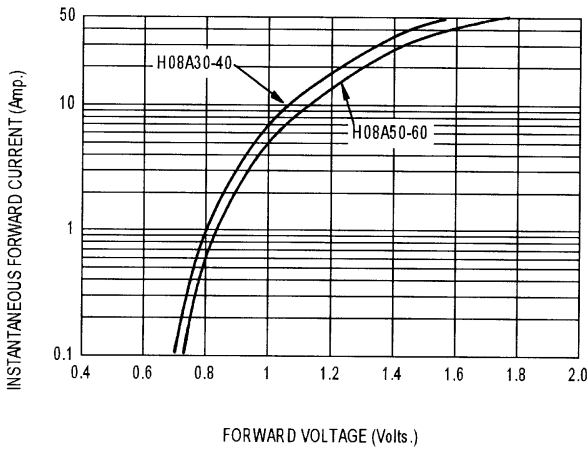


FIG-2 TYPICAL REVERSE CHARACTERISTICS

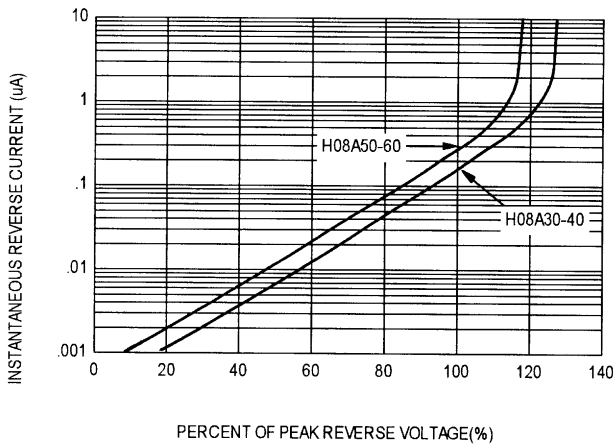


FIG-3 FORWARD CURRENT DERATING CURVE

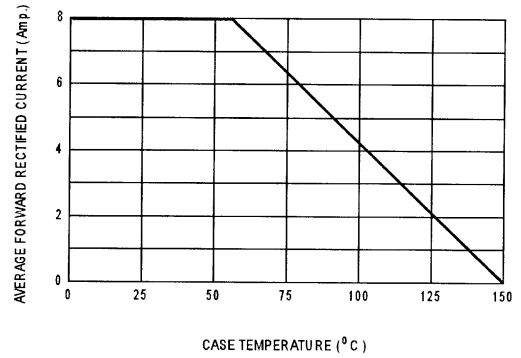


FIG-4 TYPICAL JUNCTION CAPACITANCE

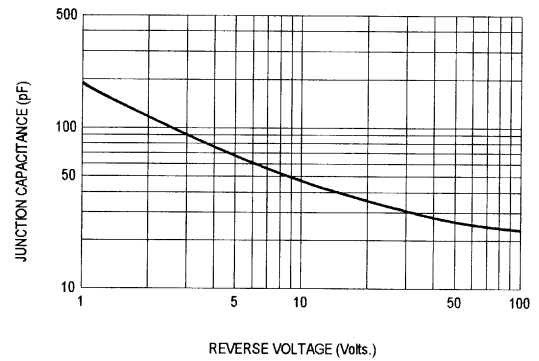
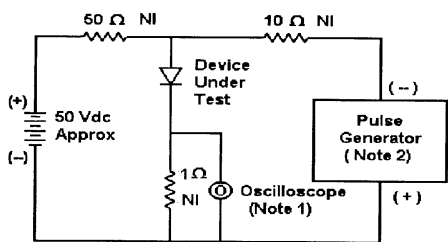
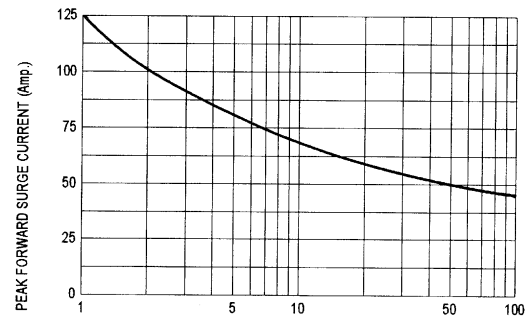
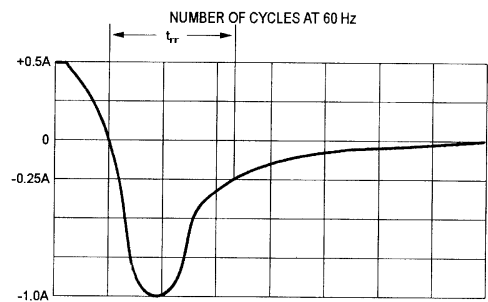


FIG-5 PEAK FORWARD SURGE CURRENT



- Notes:
1. Rise Time = 7 ns max. Input Impedance = 1 M Ω, 22 pF
 2. Rise Time = 10 ns max. Input Impedance = 50 Ω



Set time base for 20/50 ns/div

Fig-6 Reverse Recovery Time Characteristic and Test Circuit Diagram

Notice

MOSPEC reserves the rights to make changes of the content herein the document anytime without notification. MOSPEC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies. Please refer to MOSPEC website for the last document.

MOSPEC disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially incurred.

Application shown on the herein document are examples of standard use and operation. Customers are responsible for comprehending suitable use in particular applications. MOSPEC makes no representation or warranty that such application will be suitable for the specified use without further testing or modification.

The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by MOSPEC for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of MOSPEC or others.

These MOSPEC products are intended for usage in general electronic equipment. Please make sure to consult with MOSPEC before you use these MOSPEC products in equipment which require specialized quality and/or reliability, and in equipment which could have major impact to the welfare of human life (atomic energy control, aeronautics , traffic control, combustion control, safety devices etc.)